

**Figure 1a**

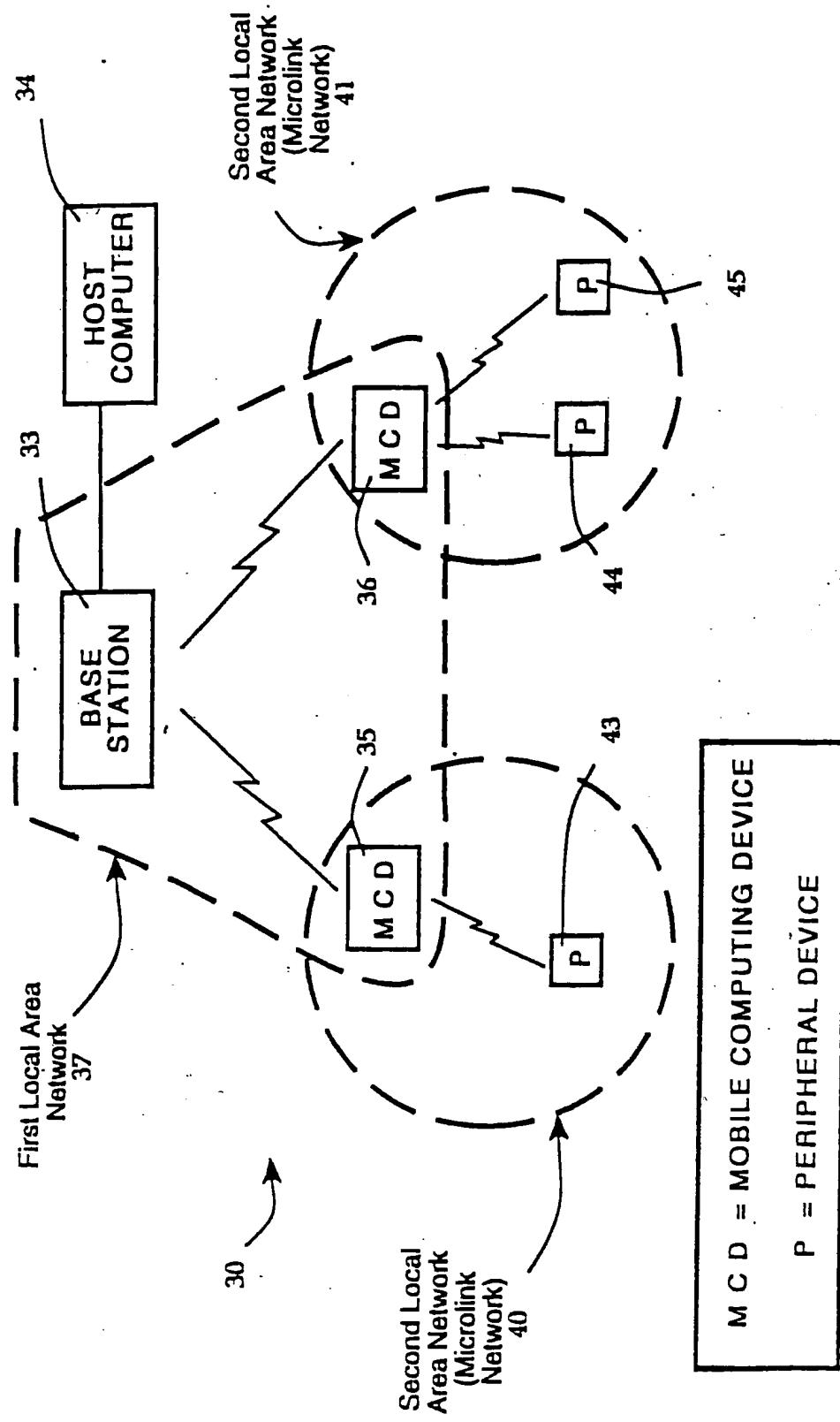


Figure 1b

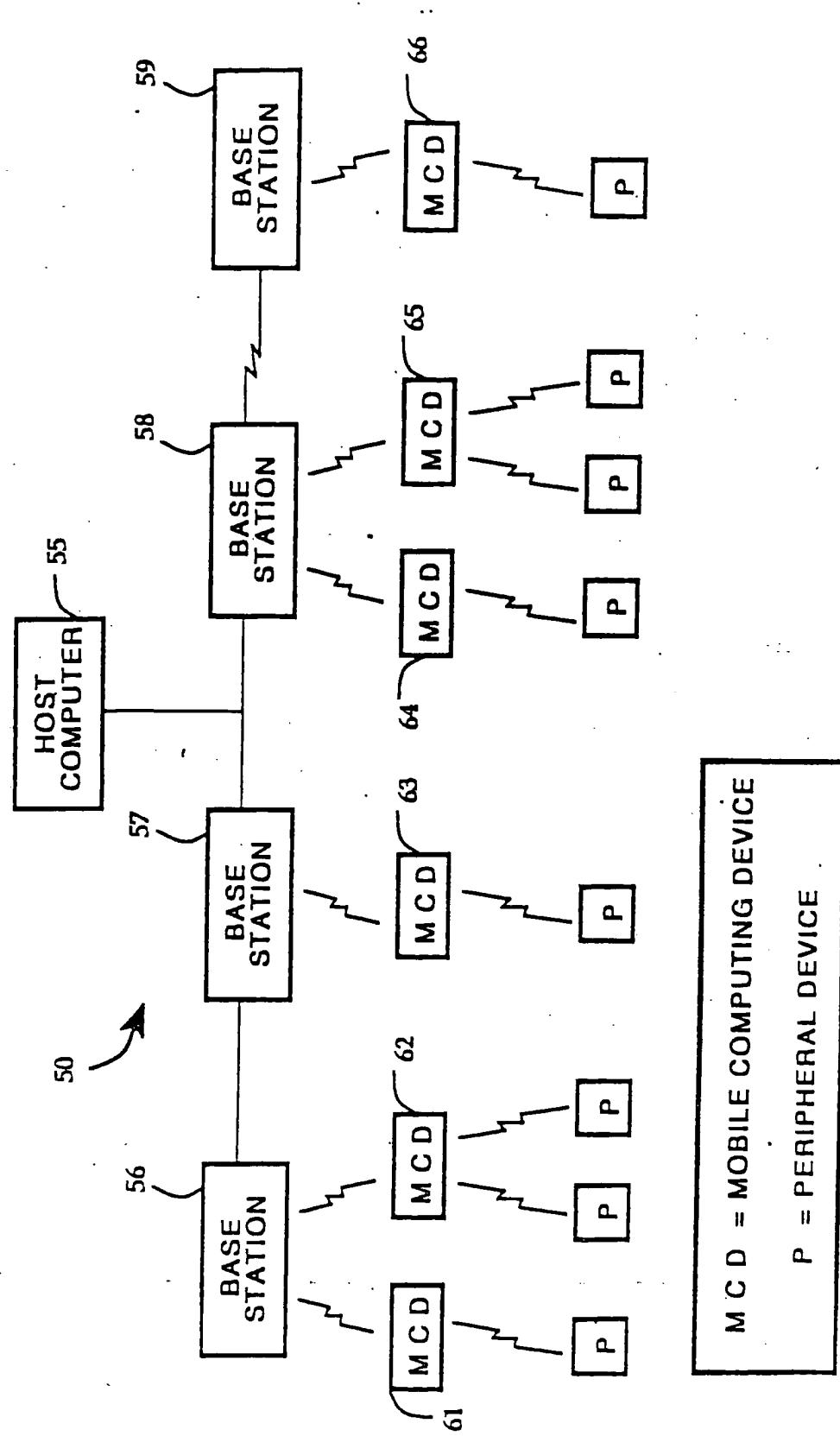
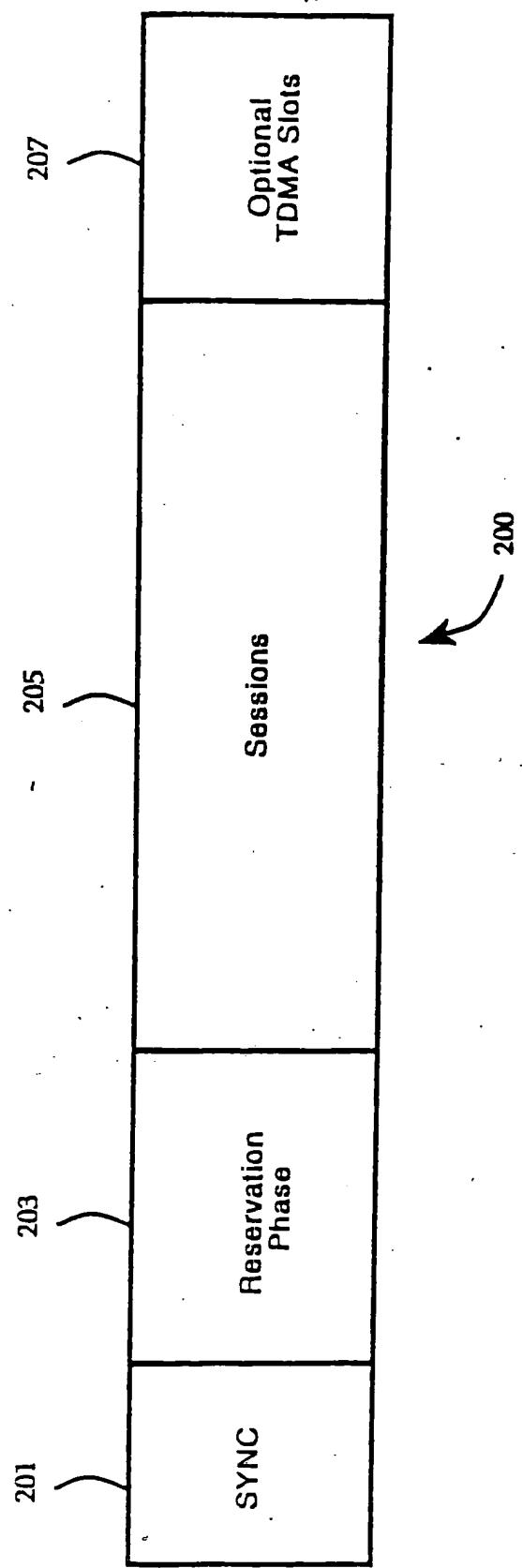


Figure 1c



**Figure 2**

10 ms	10 ms	10 ms	10 ms				
Freq. 1 2466 MHz	Freq. 2 2411 MHz	Freq. 3 2417 MHz	Freq. 4 2472 MHz	Freq. 5 2444 MHz	.....	Freq. 79 2403 MHz	Freq. 1 2466 MHz
301	301	301	301	301	.....	301	301
301	301	301	301	301	.....	301	301

Figure 3a

20 ms	20 ms	20 ms	20 ms				
Freq. 1 2466 MHz	Freq. 2 2411 MHz	Freq. 3 2417 MHz	Freq. 4 2472 MHz	Freq. 5 2444 MHz	.....	Freq. 79 2403 MHz	Freq. 1 2466 MHz
303	303	303	303	303	.....	303	303
303	303	303	303	303	.....	303	303

Figure 3b

Freq 1: 2466 MHz		Freq 2: 2411 MHz		Freq 3: 2417 MHz		Freq 5: 2444 MHz	
AI. 1 20 ms	AI. 2 20 ms						

Figure 4a

401



Freq 1: 2466 MHz				Freq 2: 2411 MHz			
AI. 1 20 ms	AI. 2 20 ms	AI. 3 20 ms	AI. 4 20 ms	AI. 1 20 ms	AI. 2 20 ms	AI. 3 20 ms	AI. 4 20 ms

Figure 4b

401



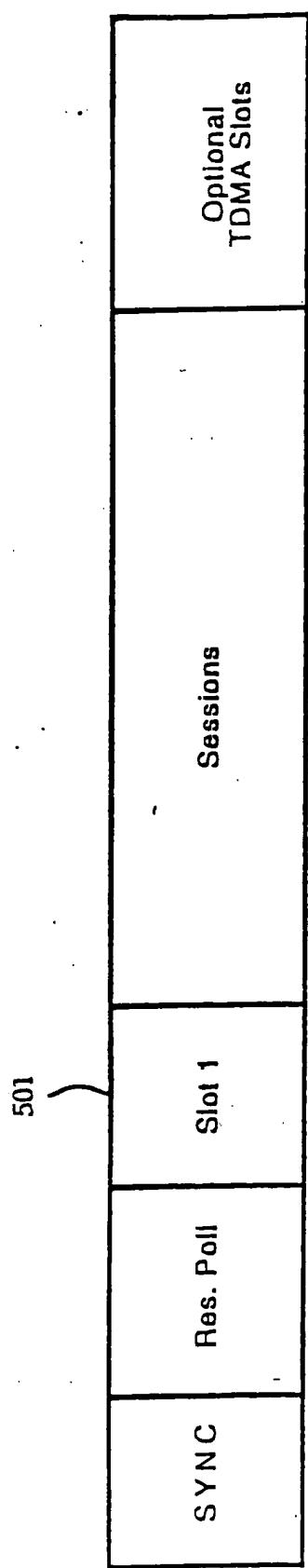


Figure 5a

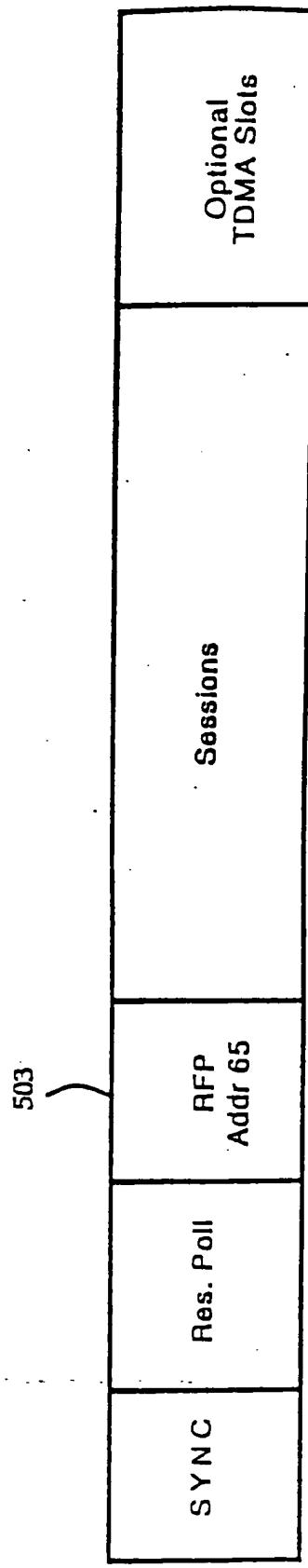


Figure 5b

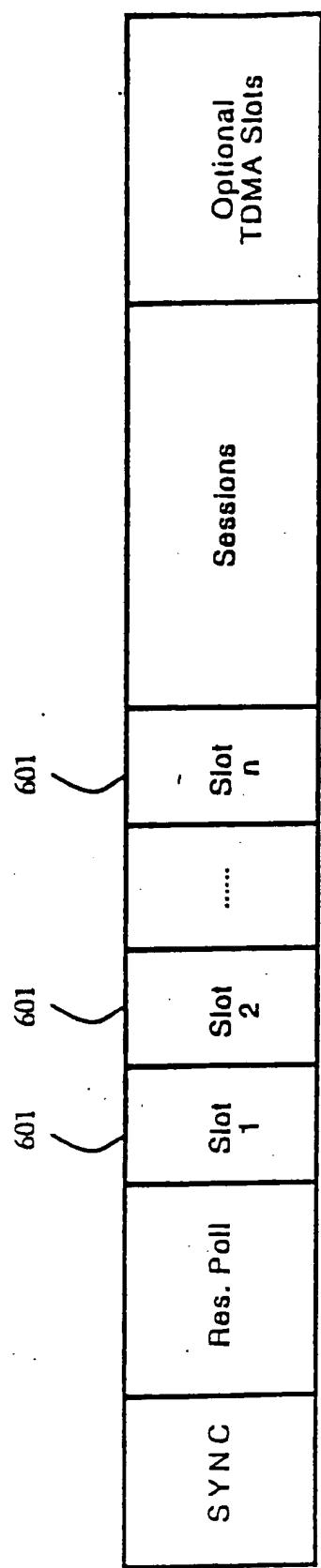


Figure 6a

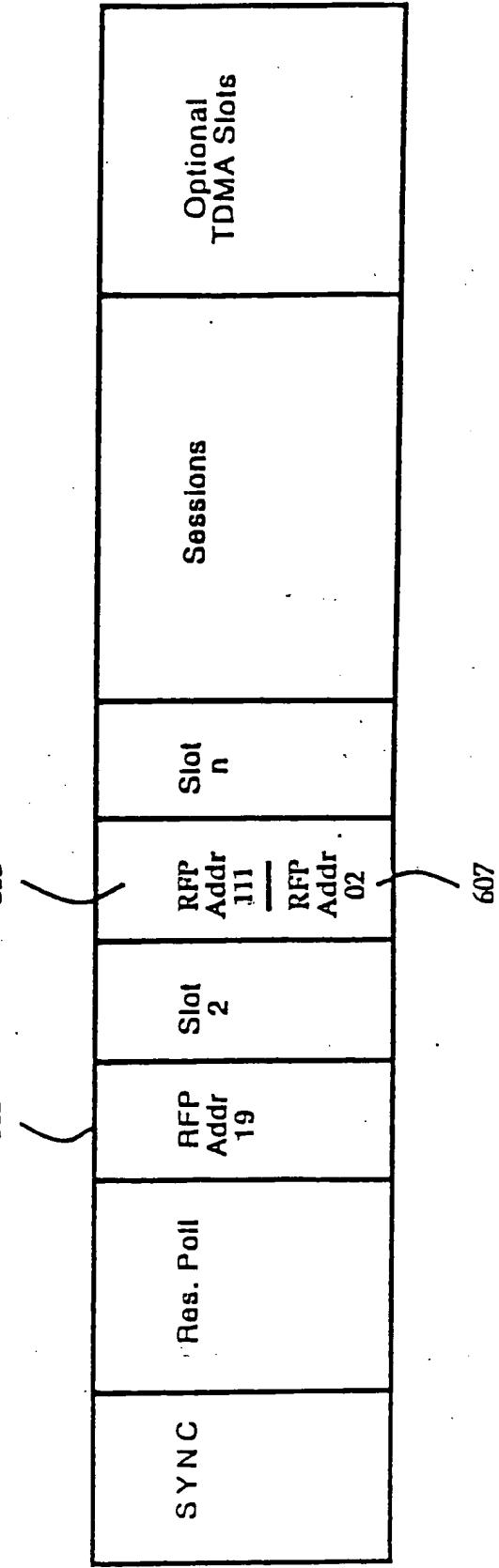


Figure 6b

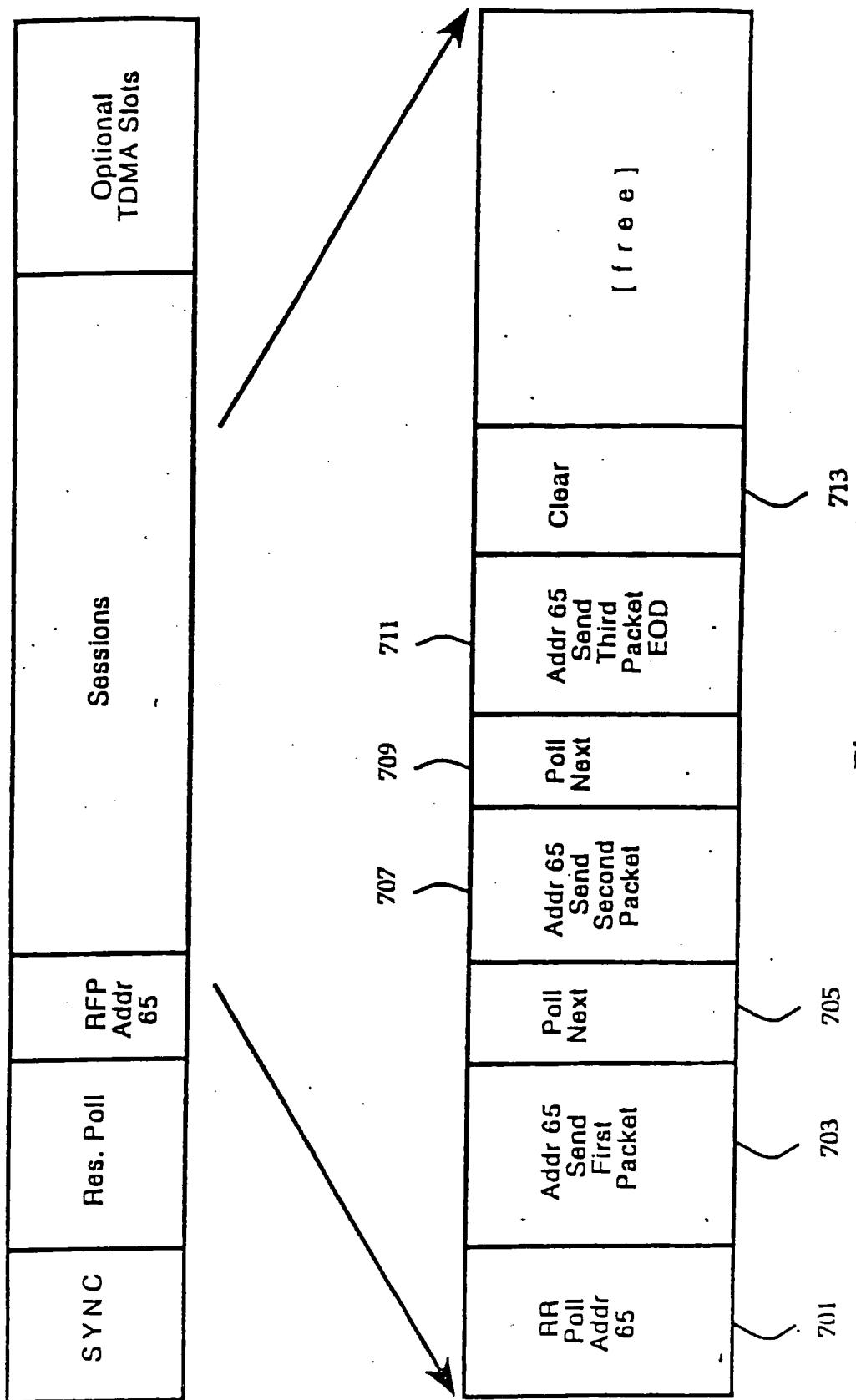


Figure 7a

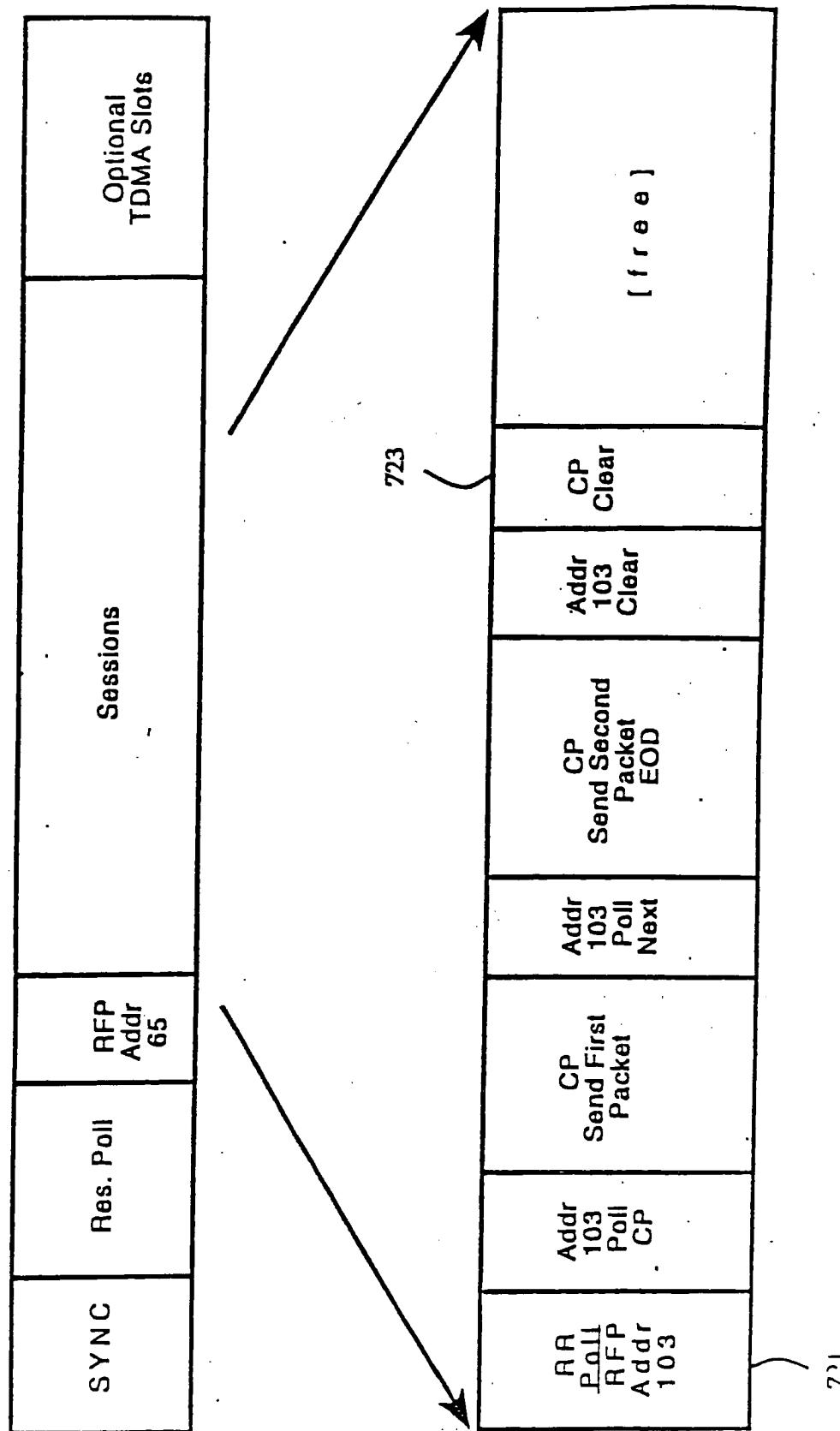


Figure 7b

**Figure 8**

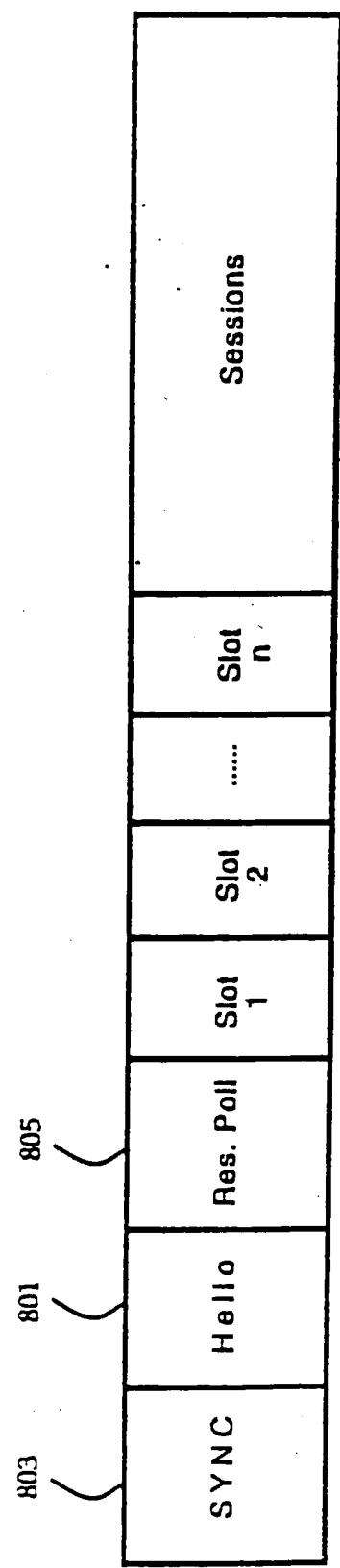


Figure 9a

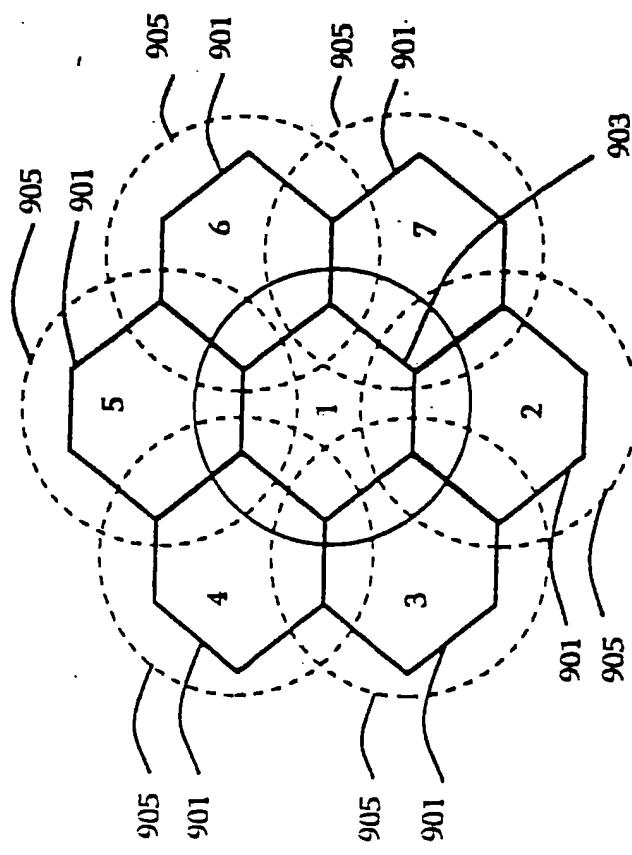
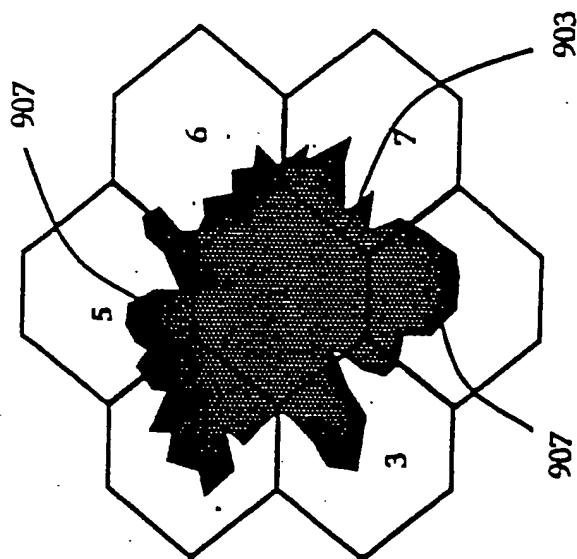


Figure 9b



BEST AVAILABLE COPY

BEST AVAILABLE COPY

Figure 10

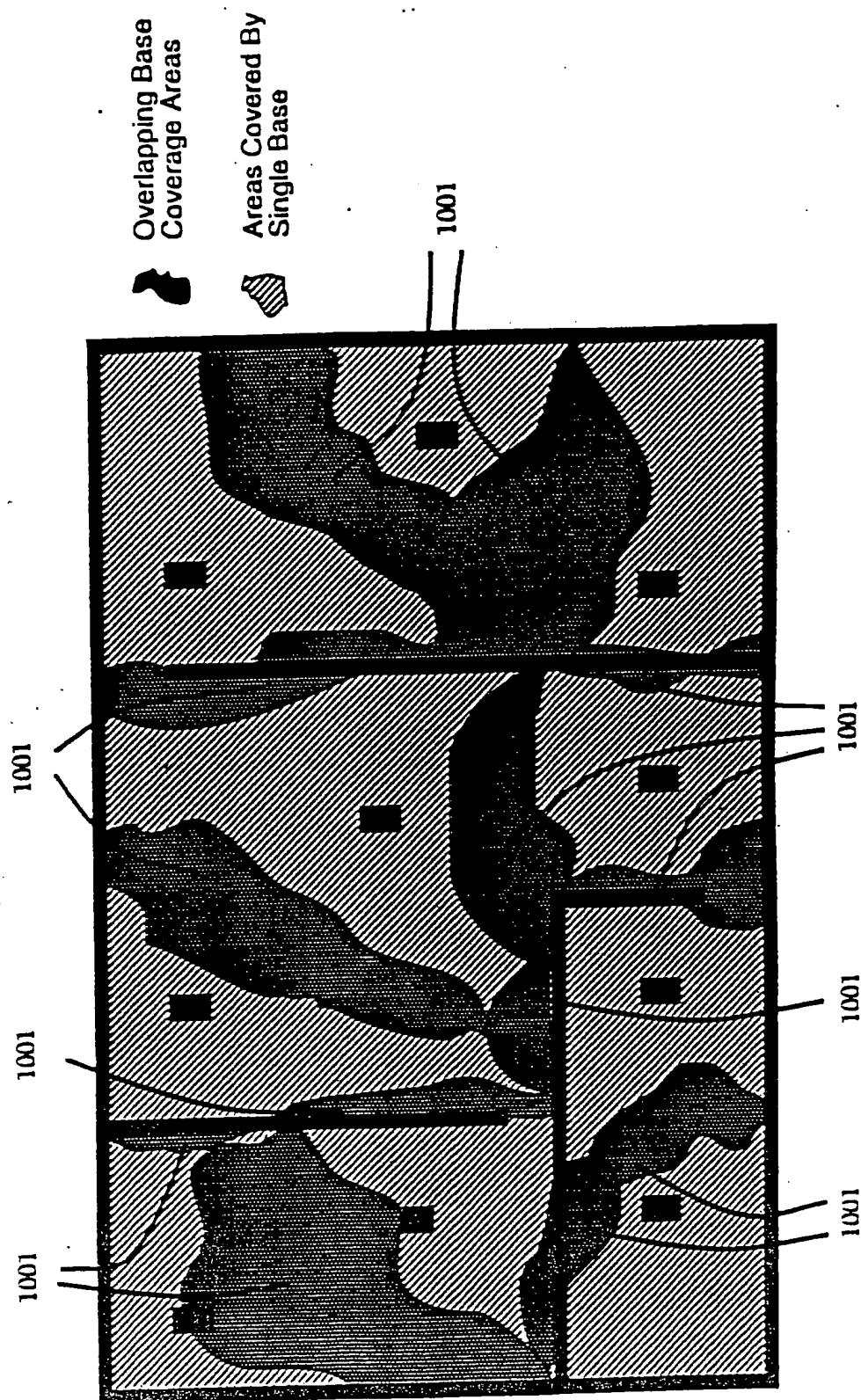
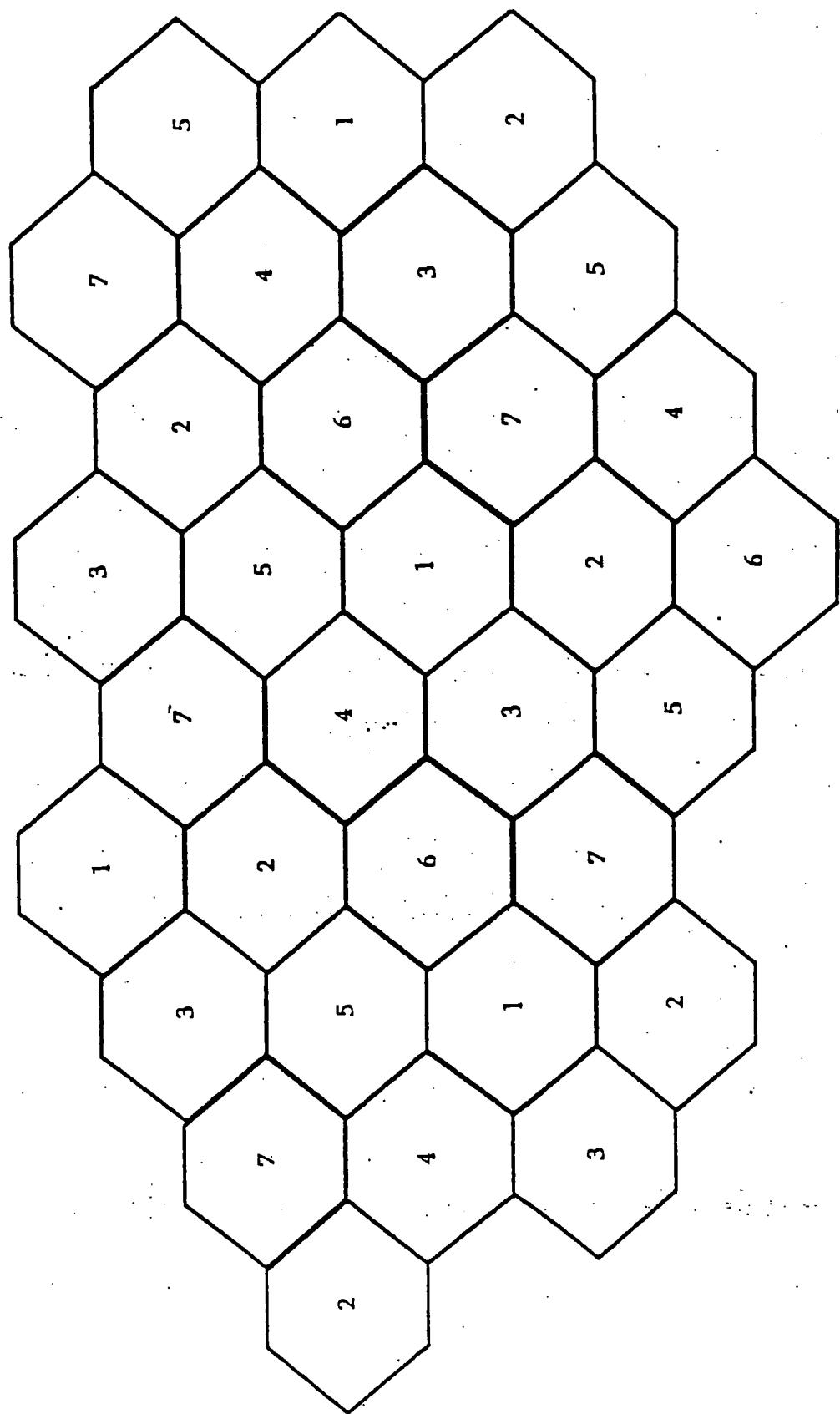


Figure 11



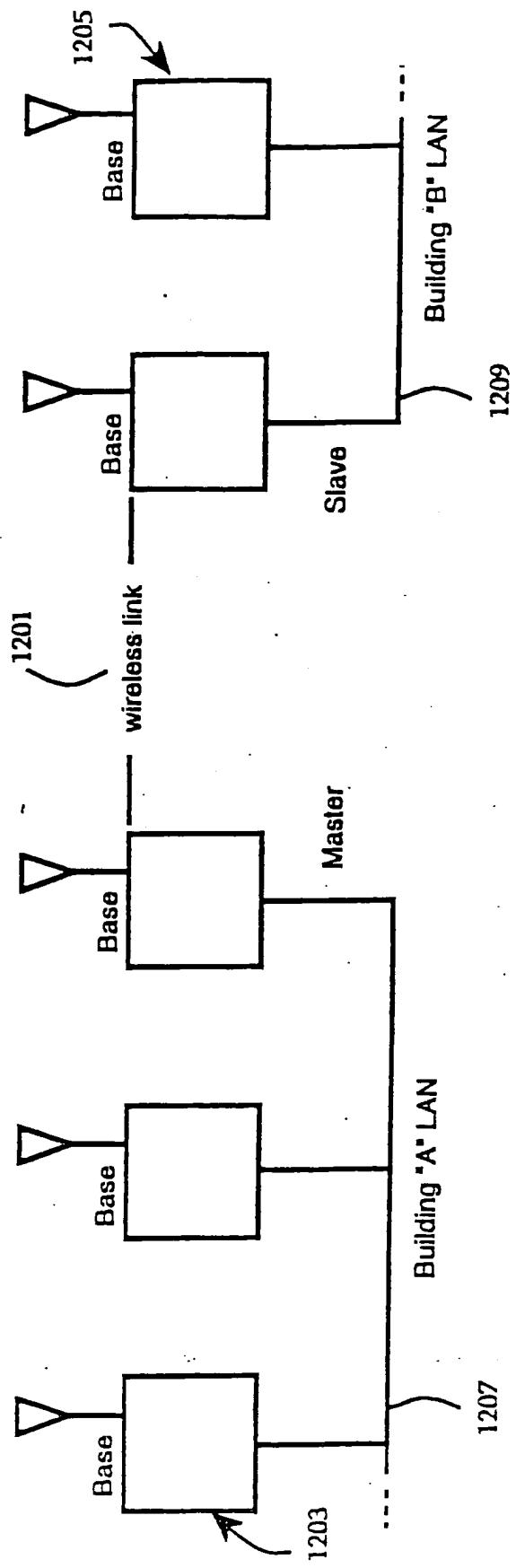
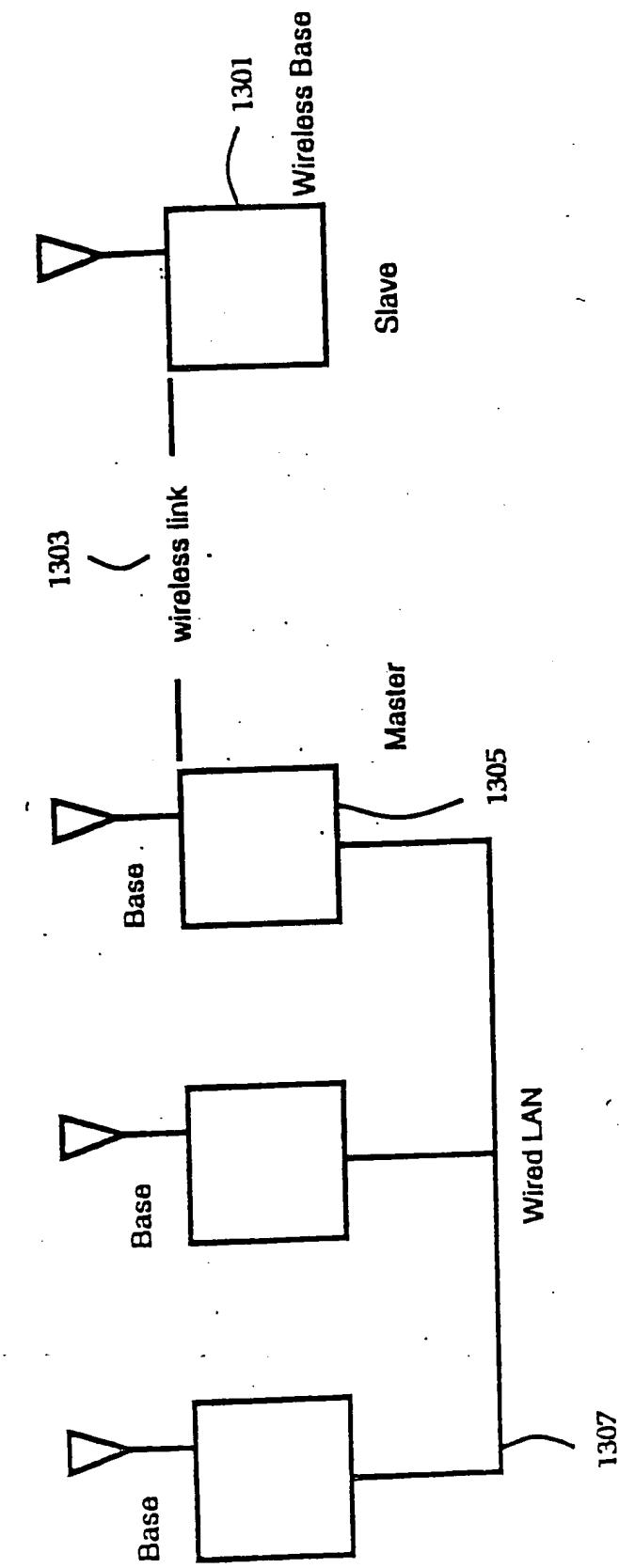


Figure 12

Figure 13



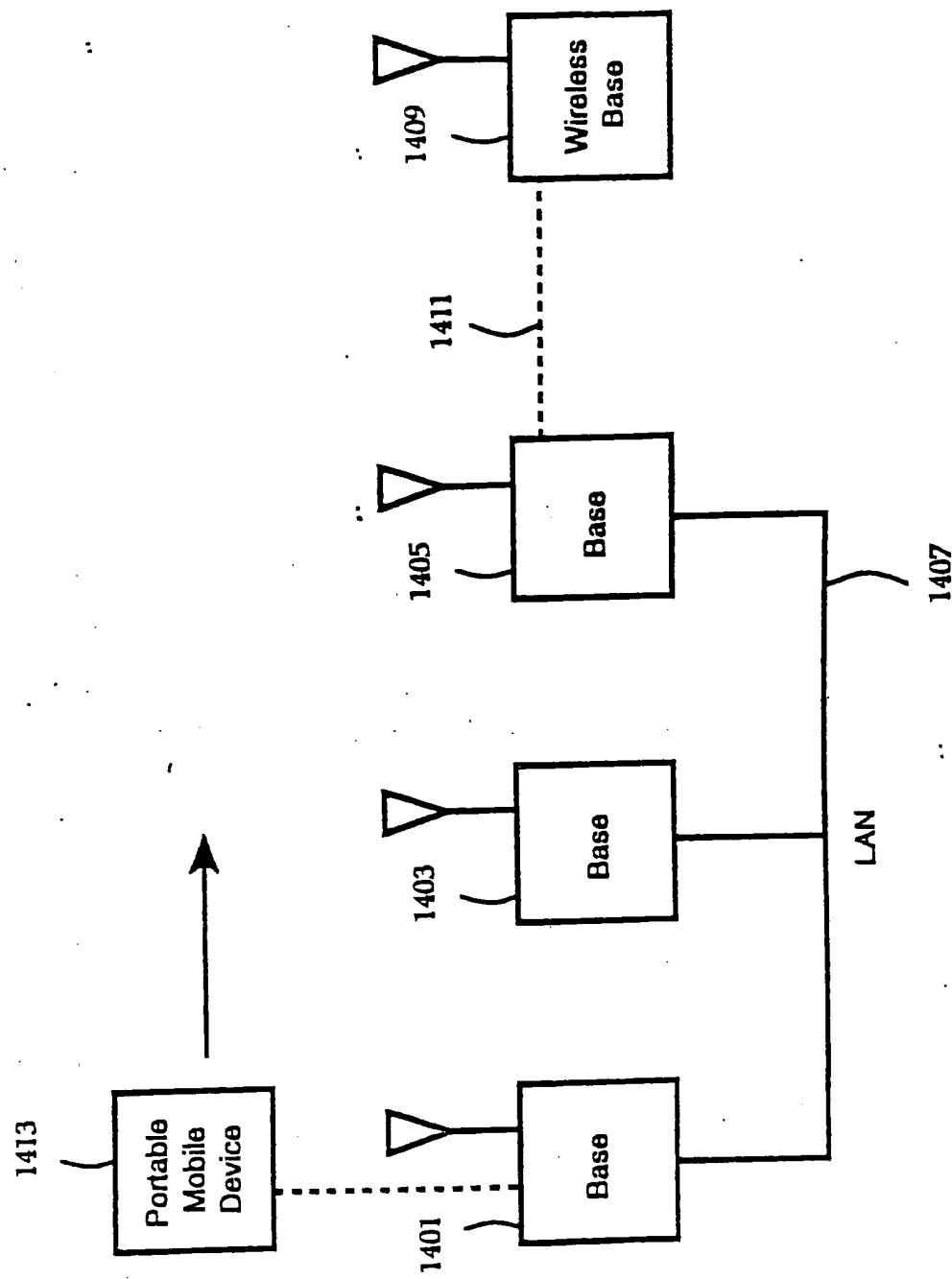


Figure 14

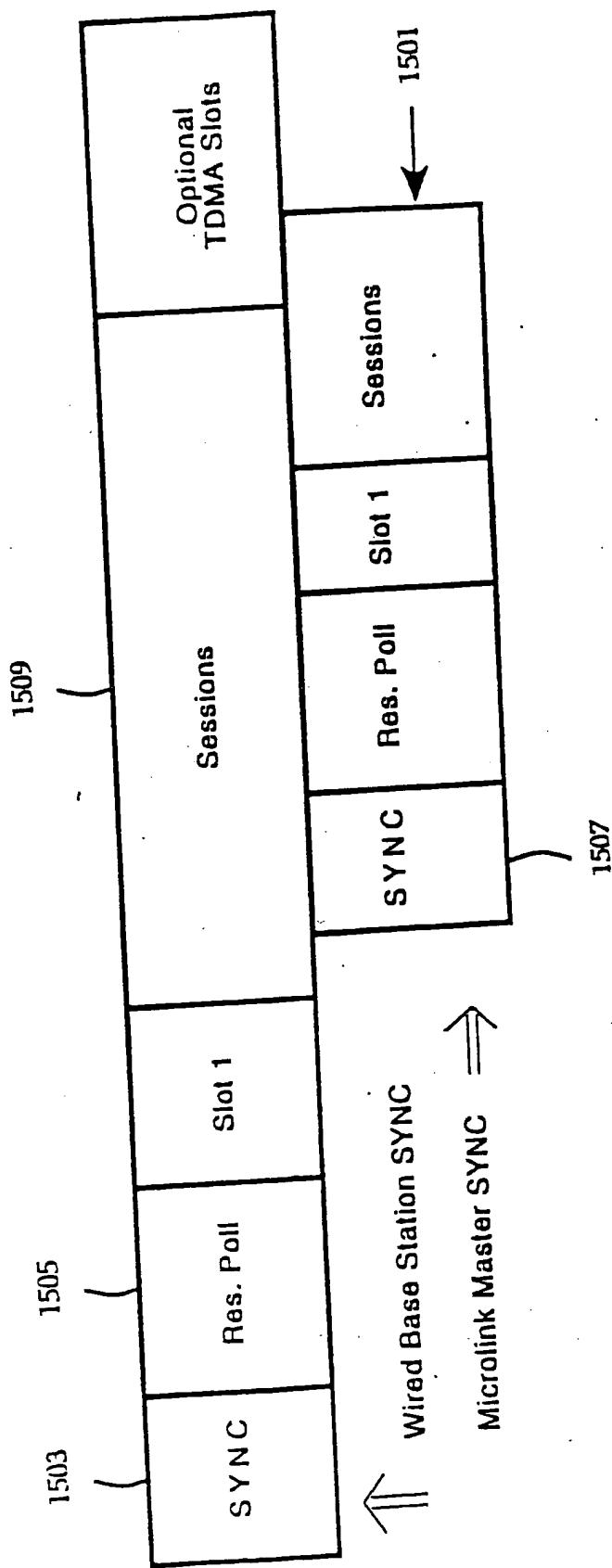


Figure 15

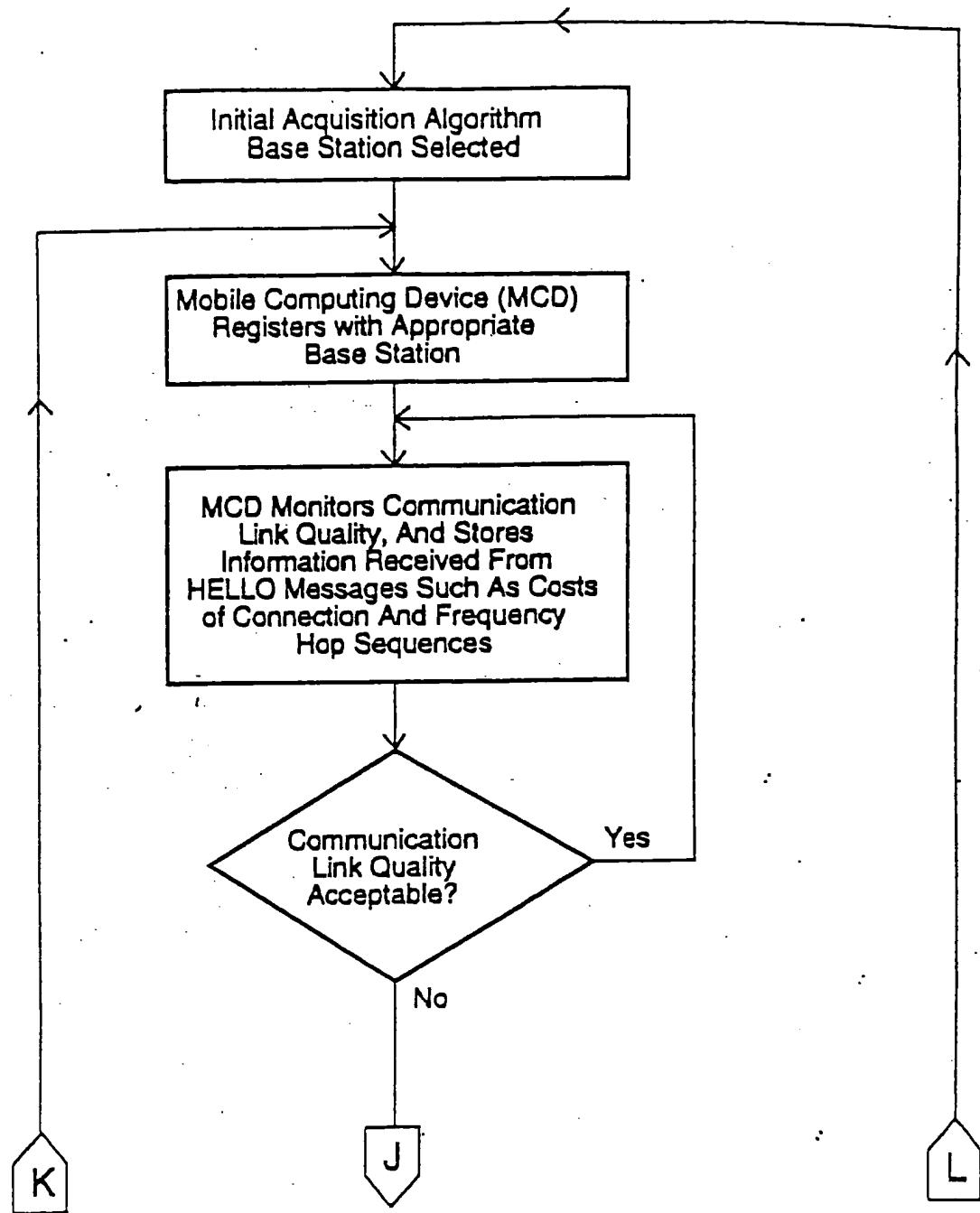


Figure 16a

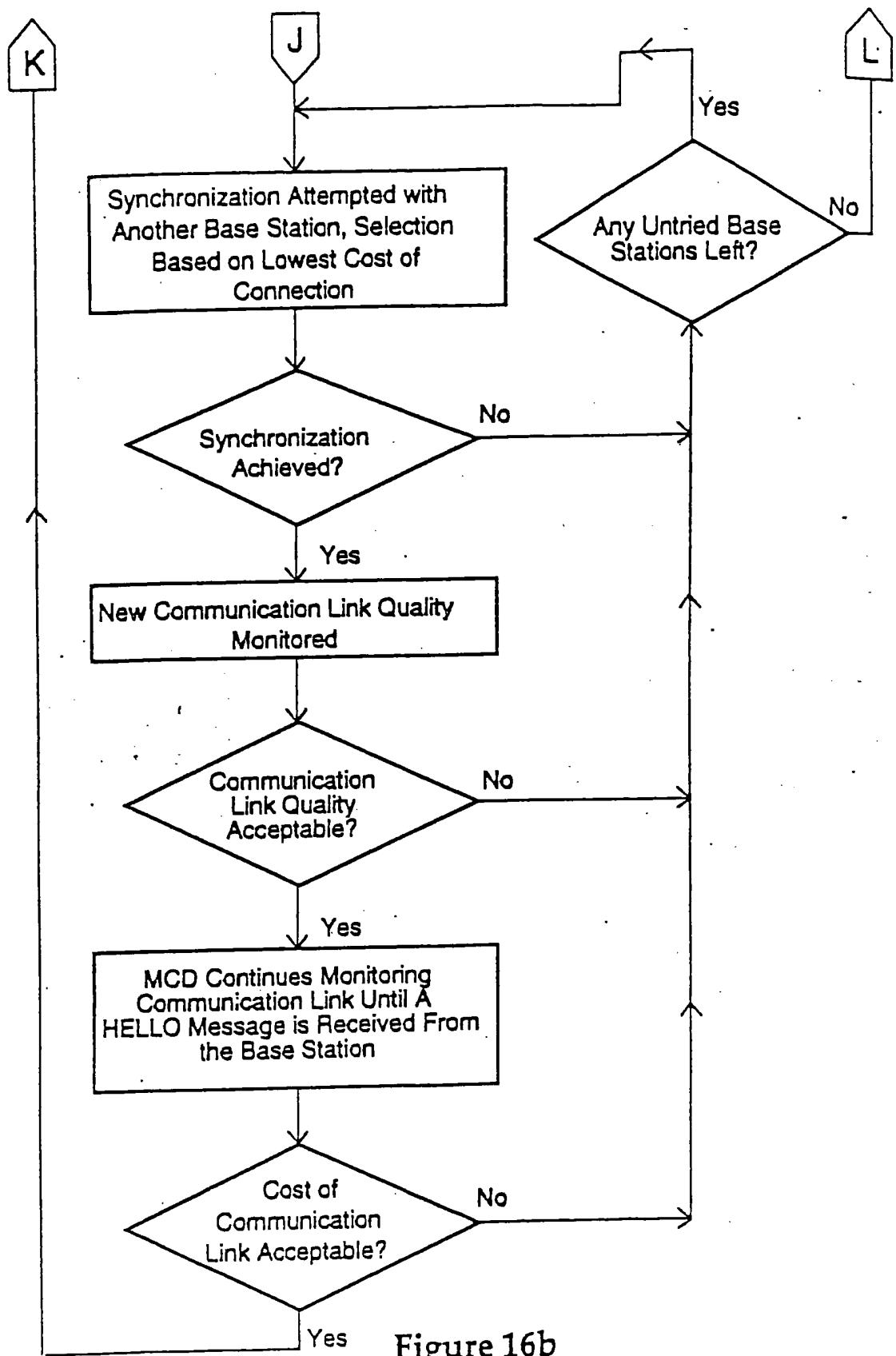


Figure 16b

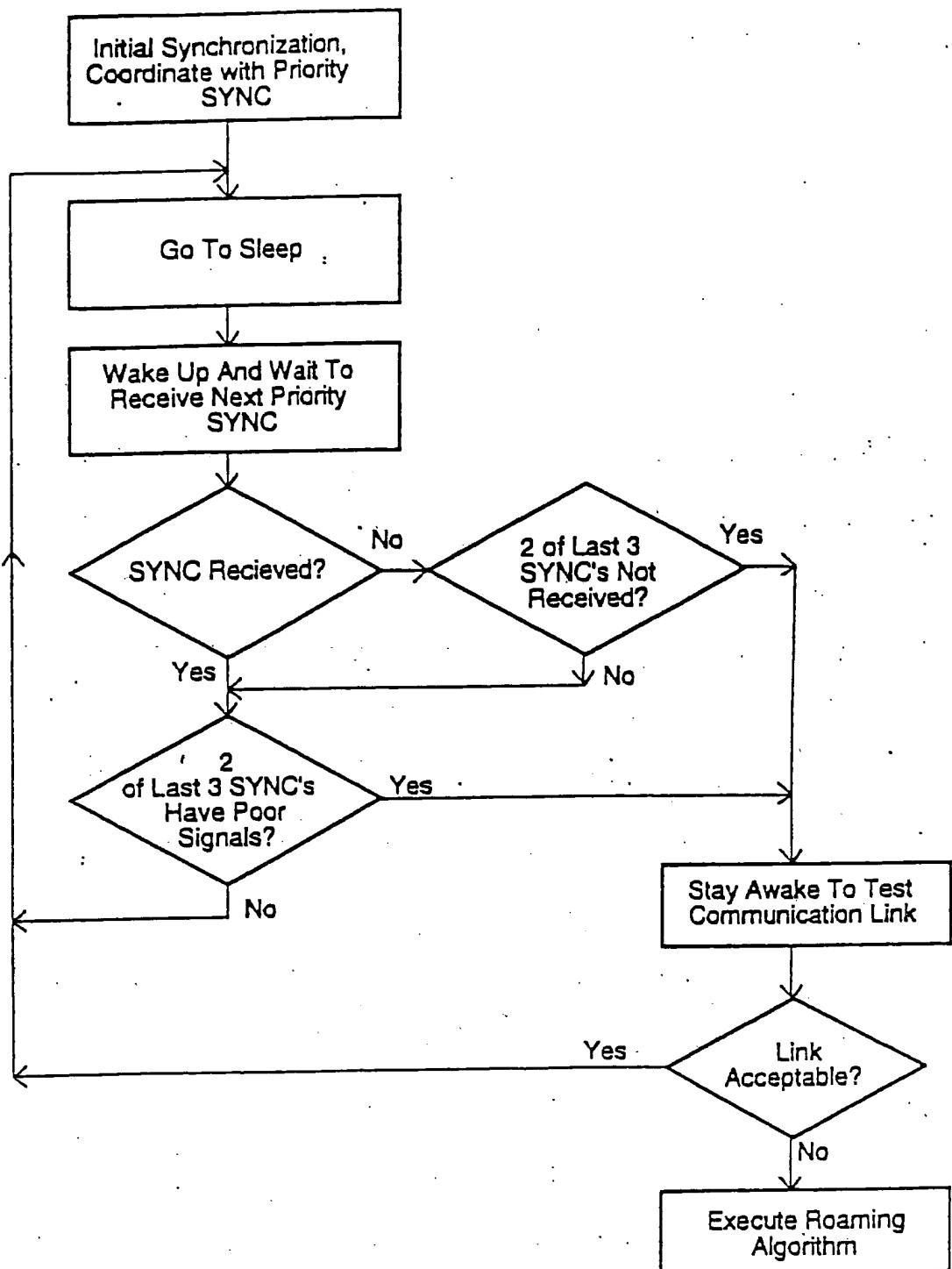


Figure 17

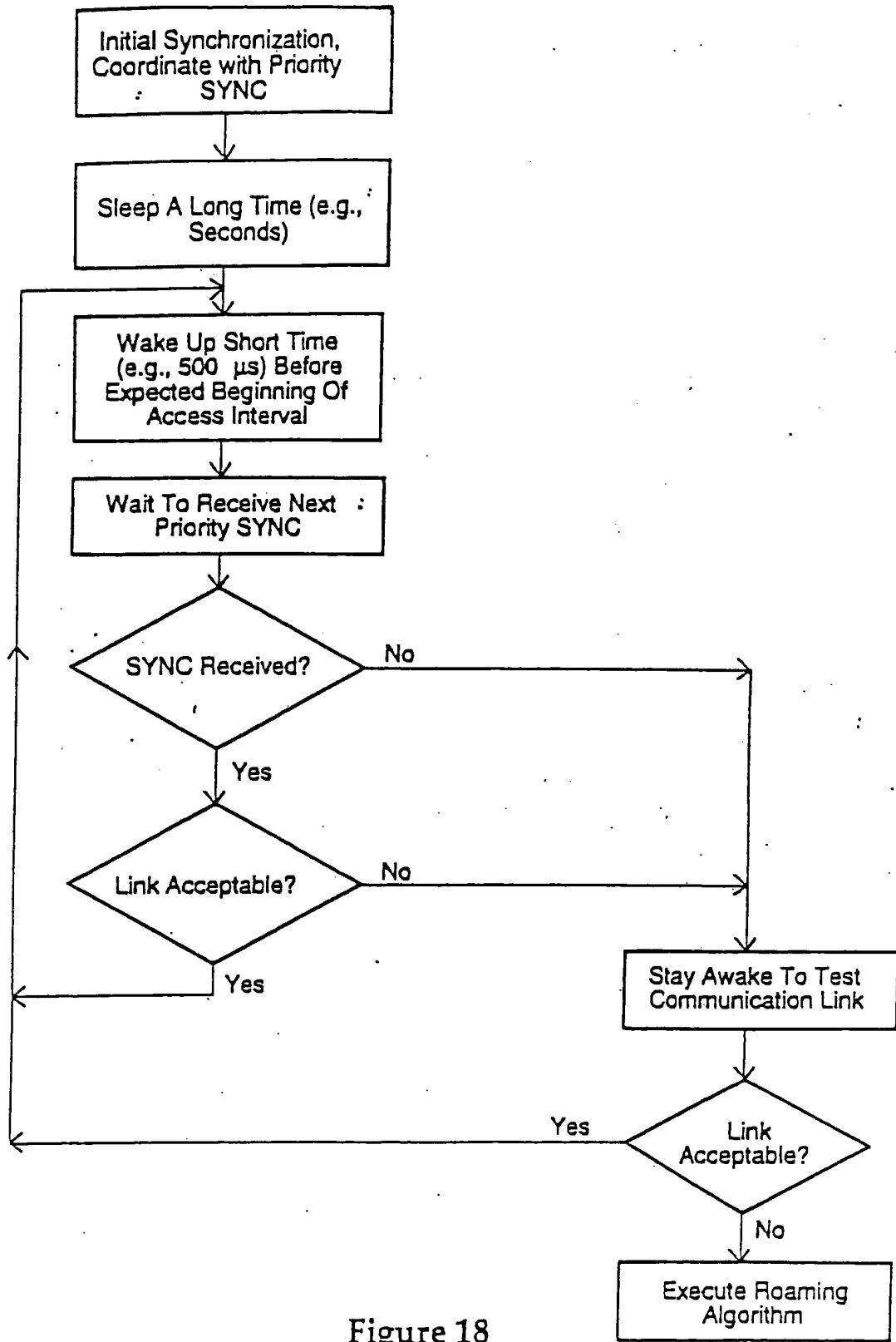


Figure 18

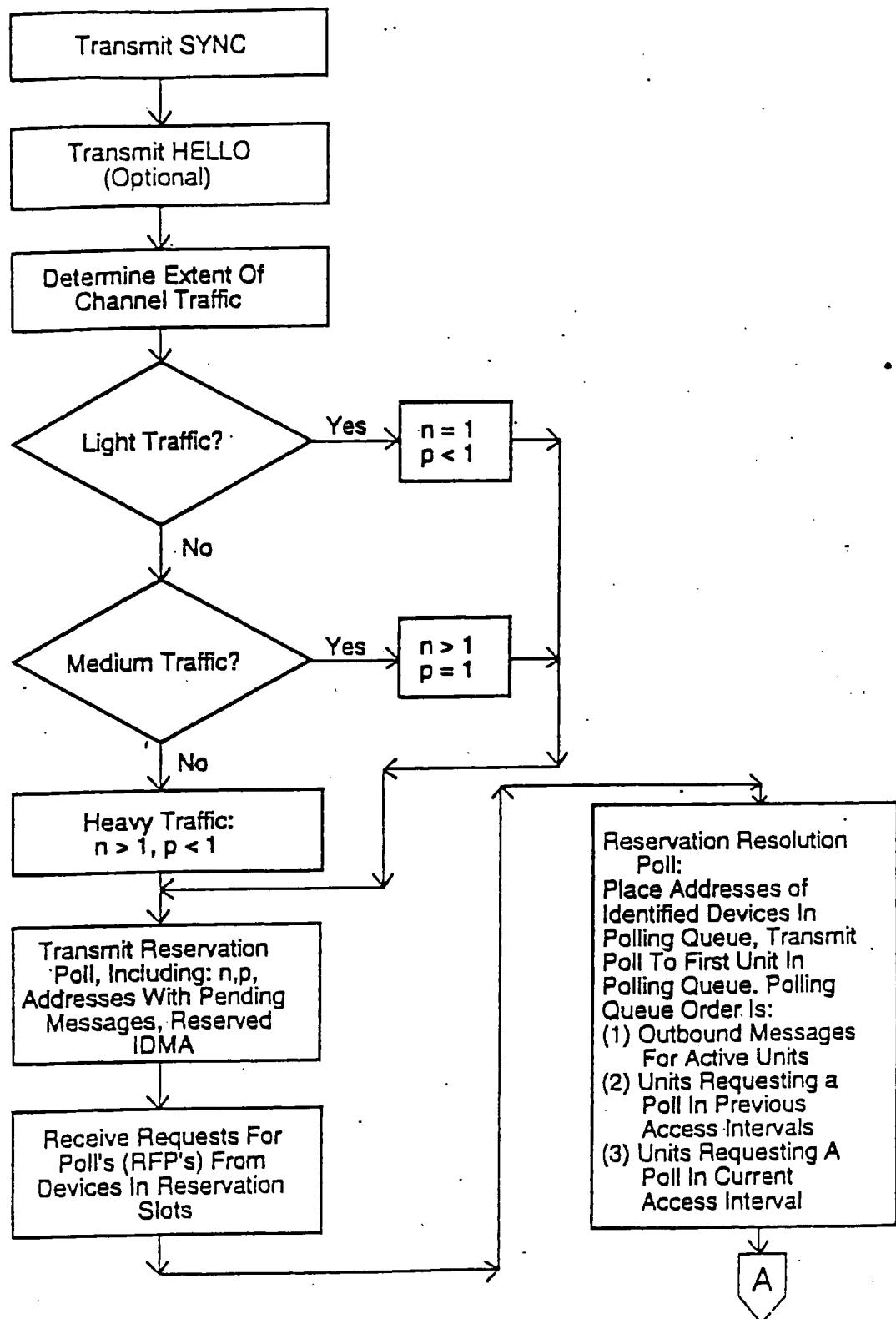


Figure 19a

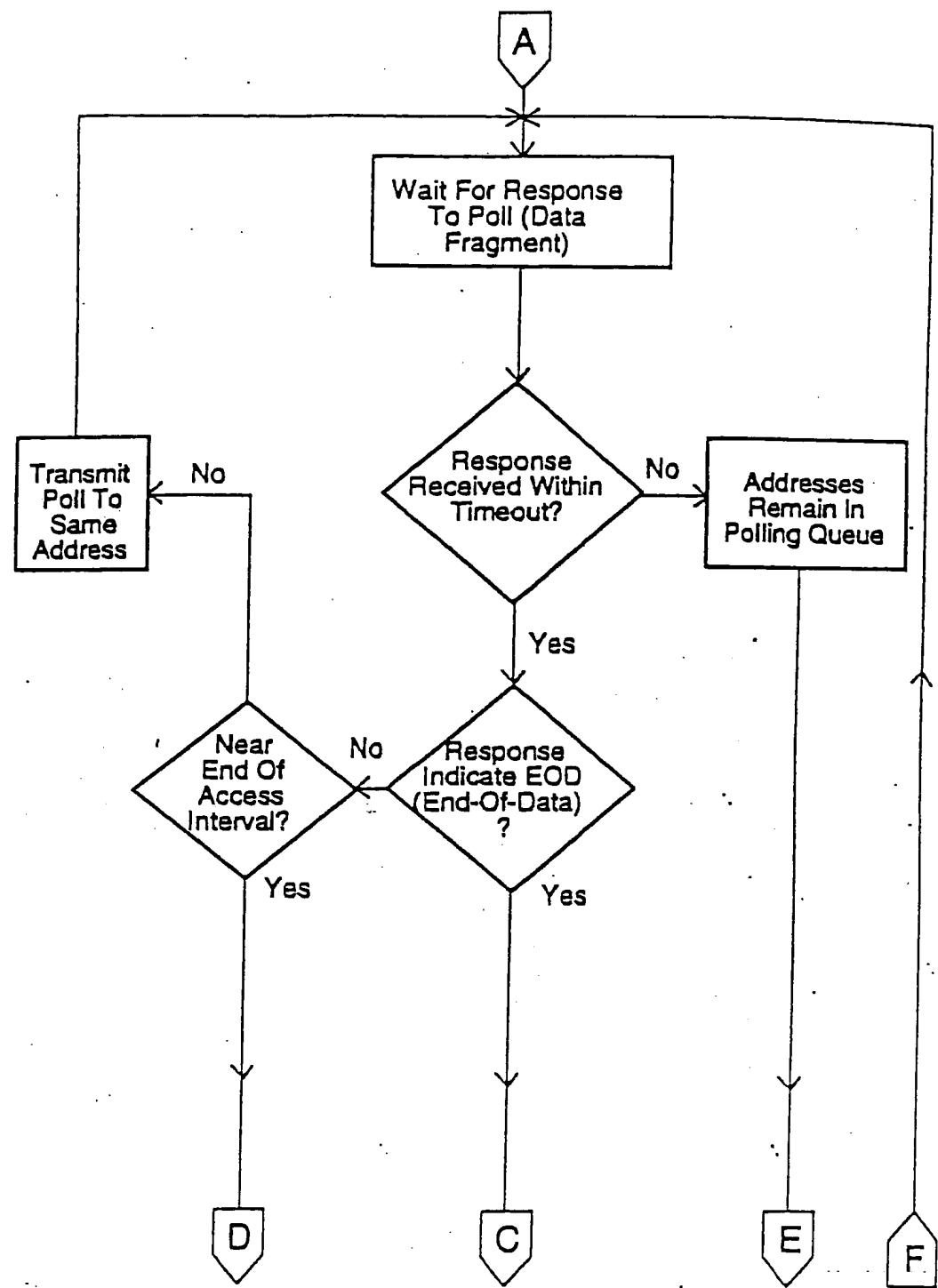


Figure 19b-1

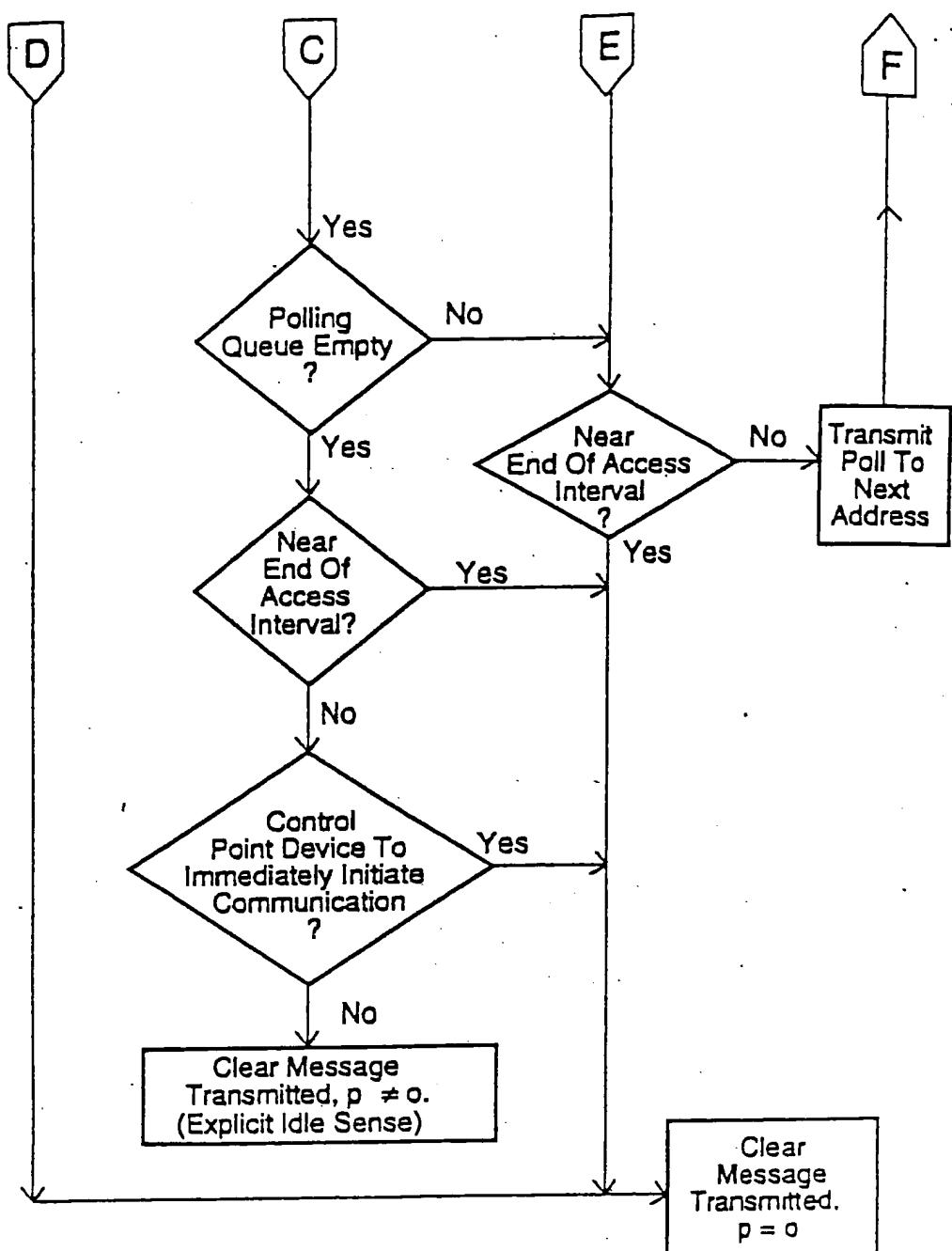
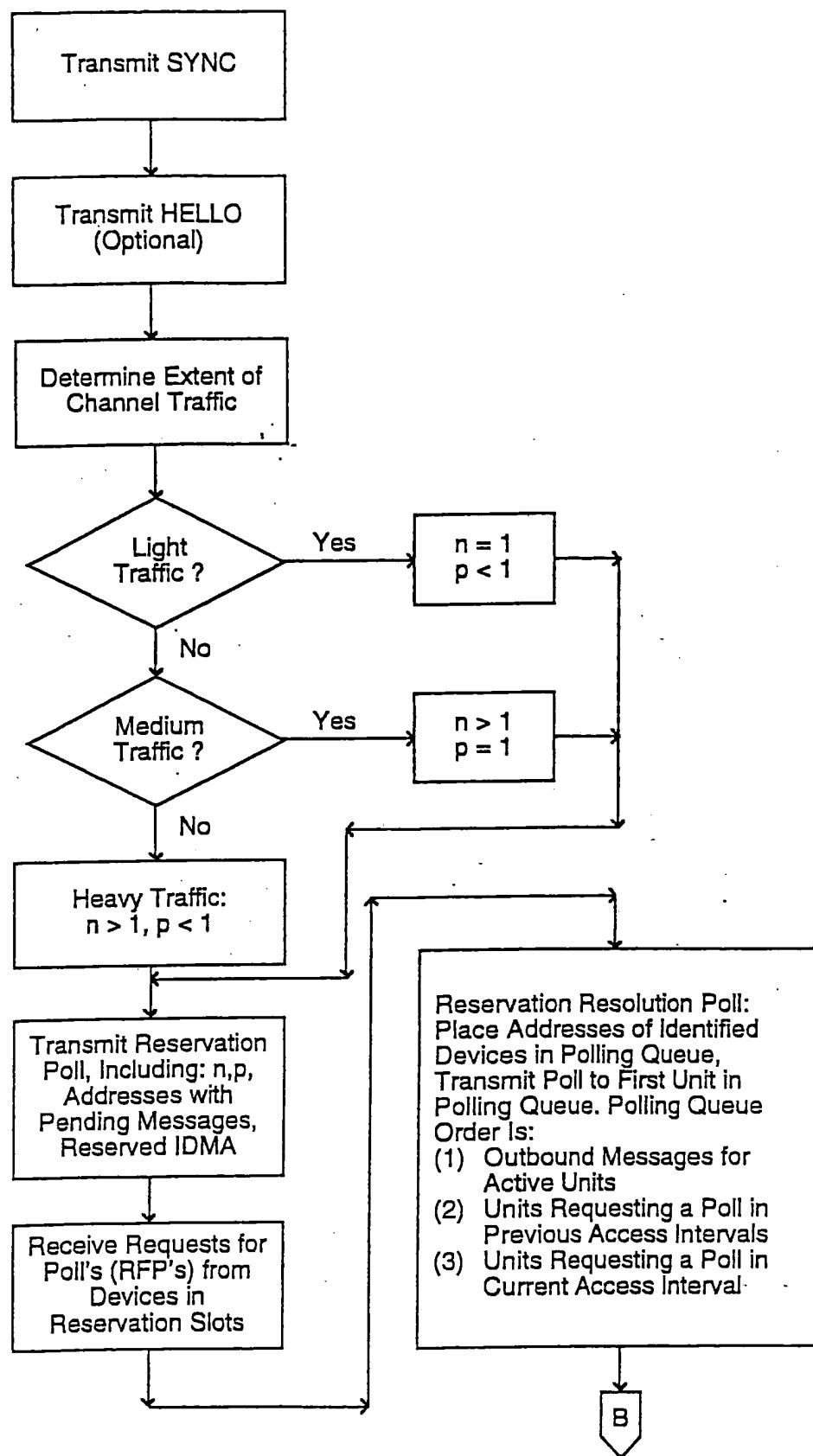


Figure 19b-2



**Fig. 20a**

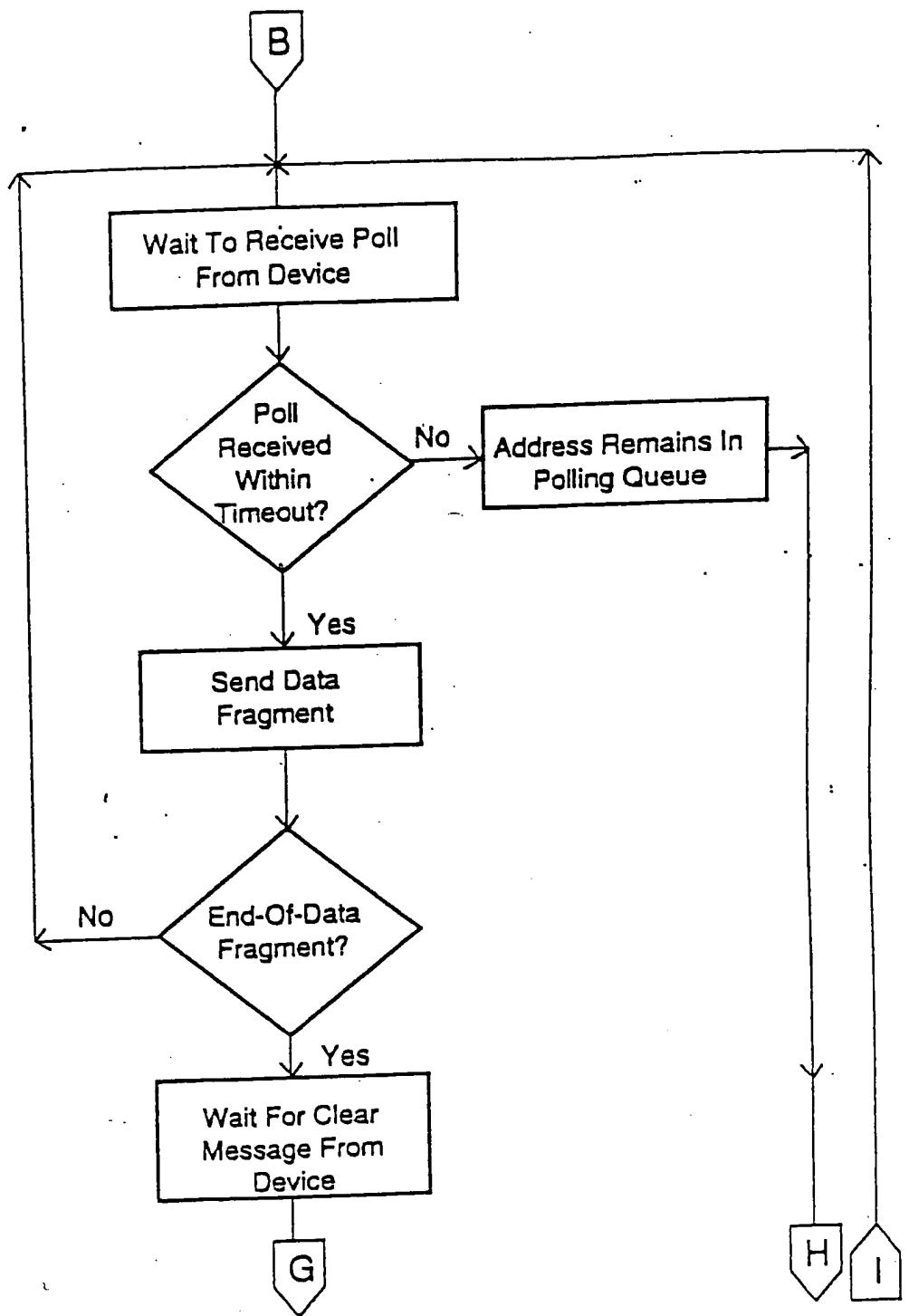


Figure 20b-1

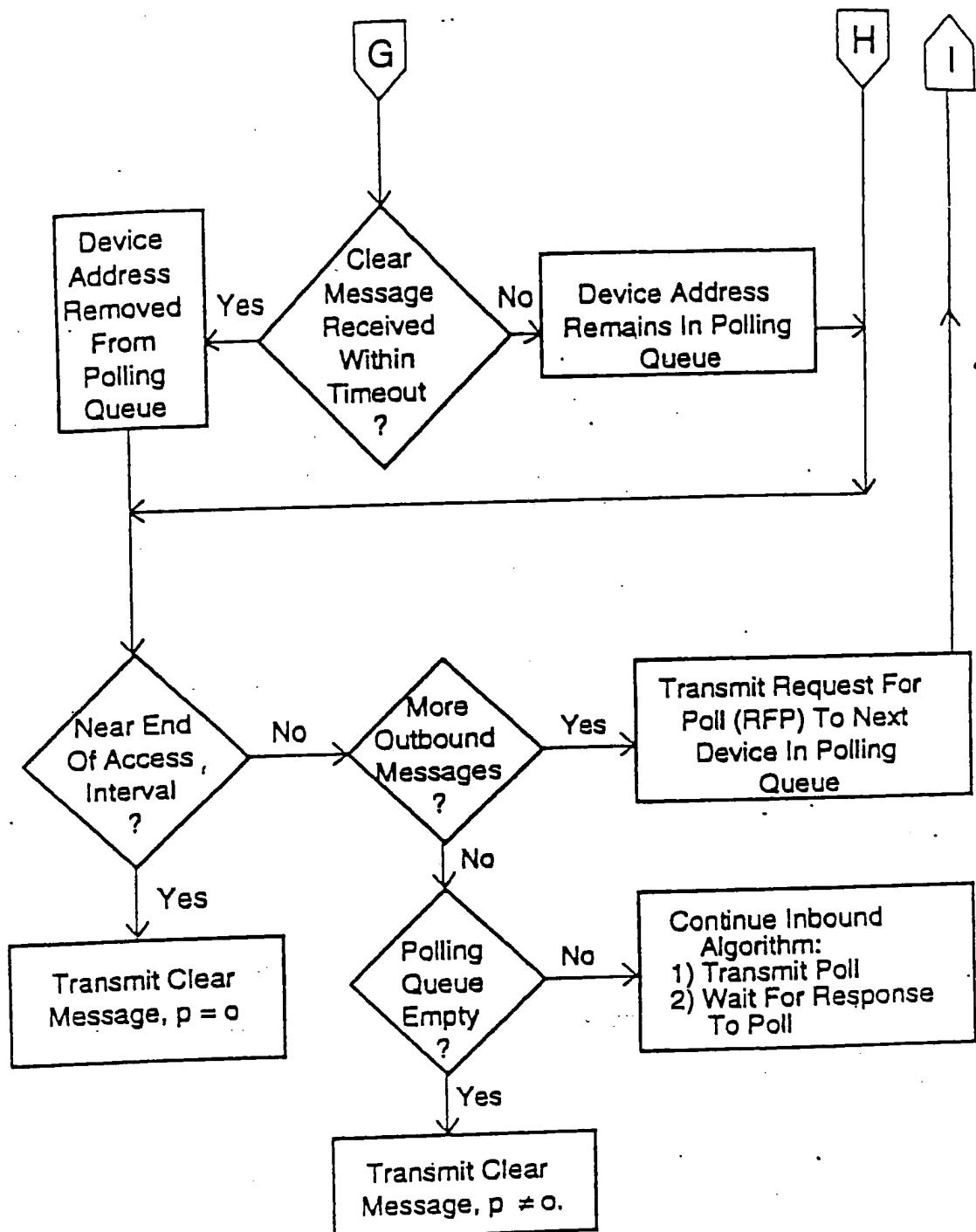


Figure 20b-2

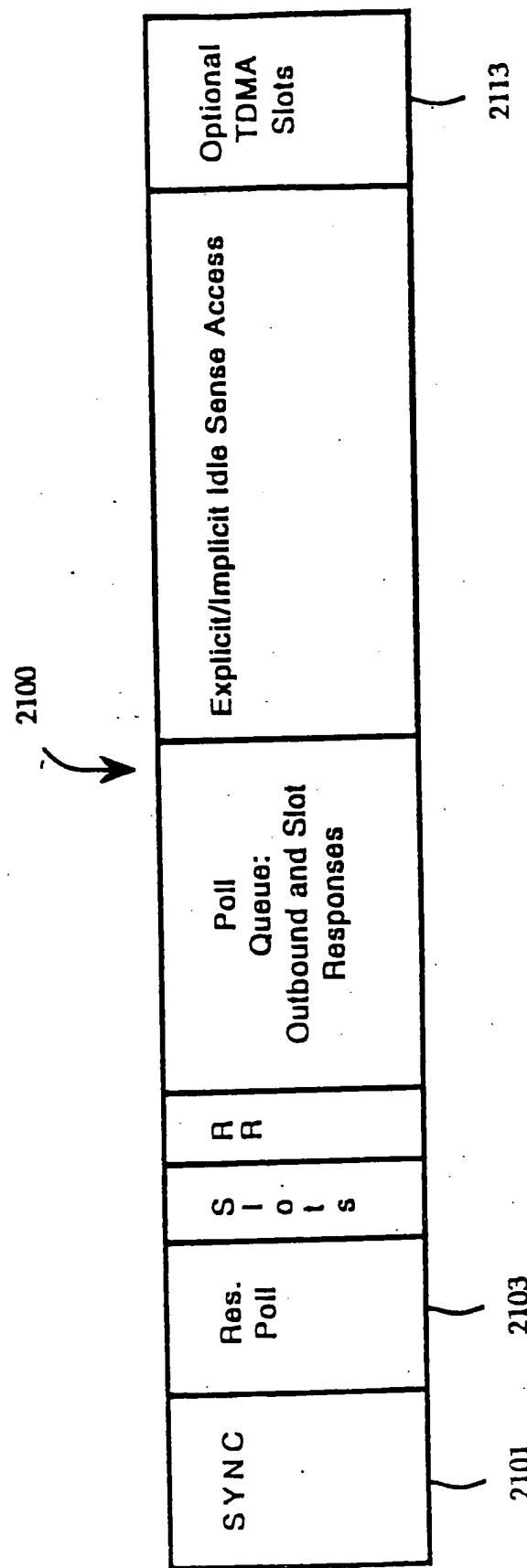
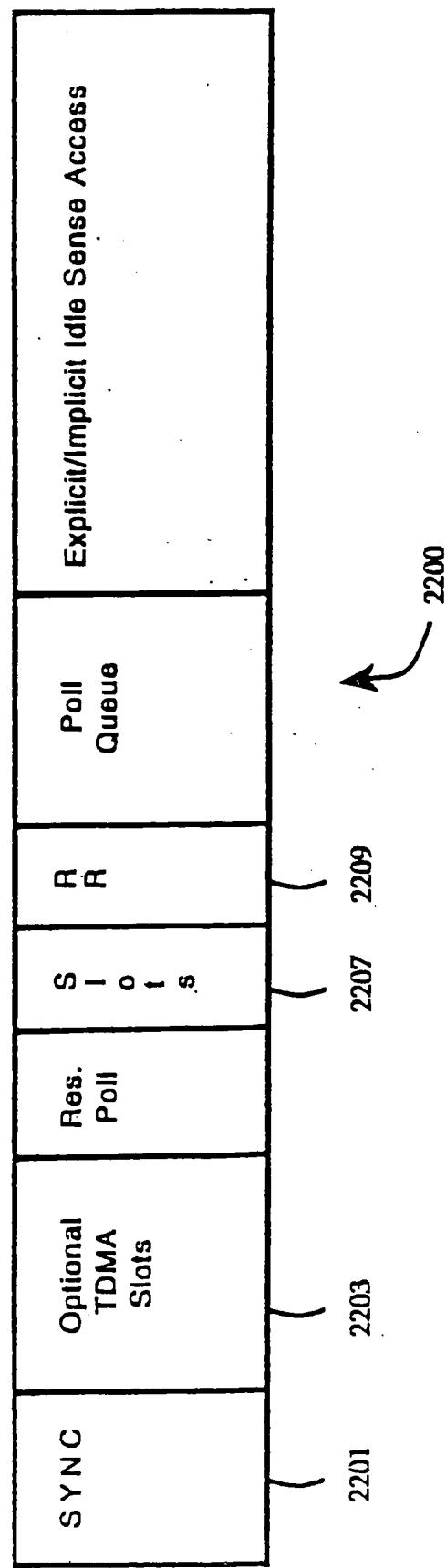


Figure 21

**Figure 22**



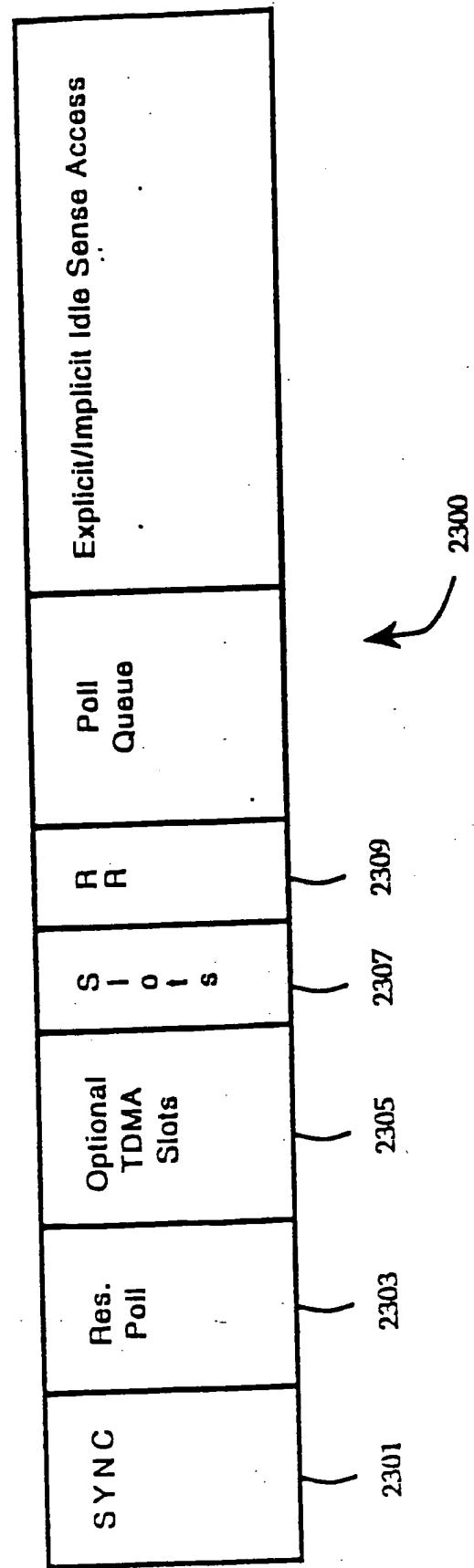
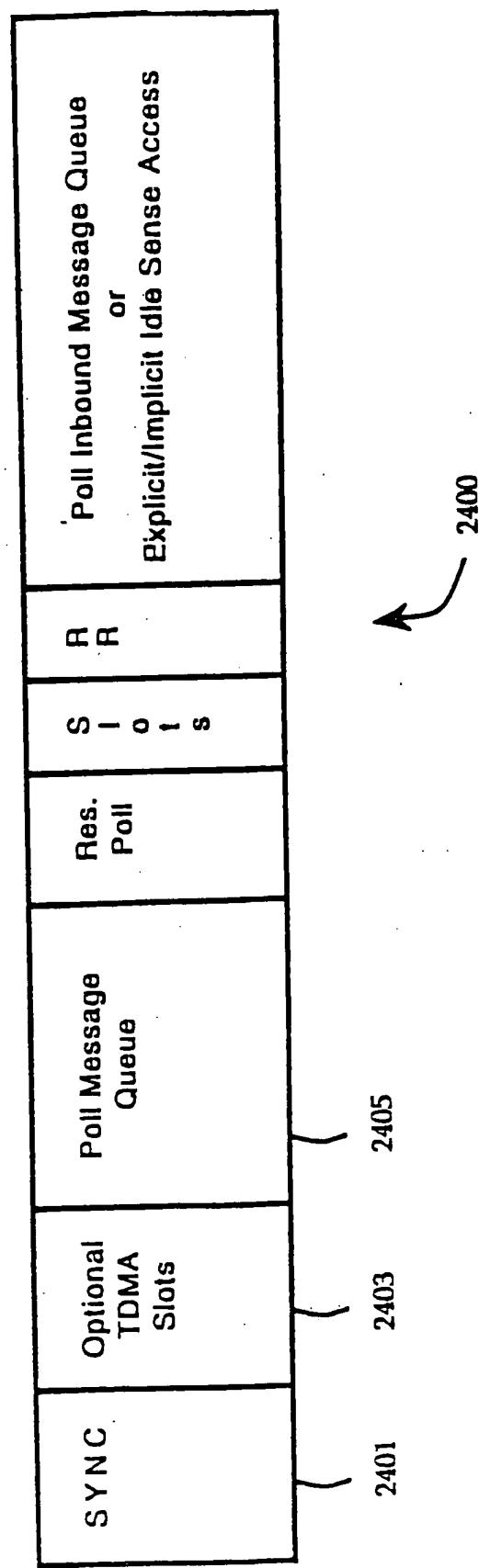


Figure 23

Figure 24



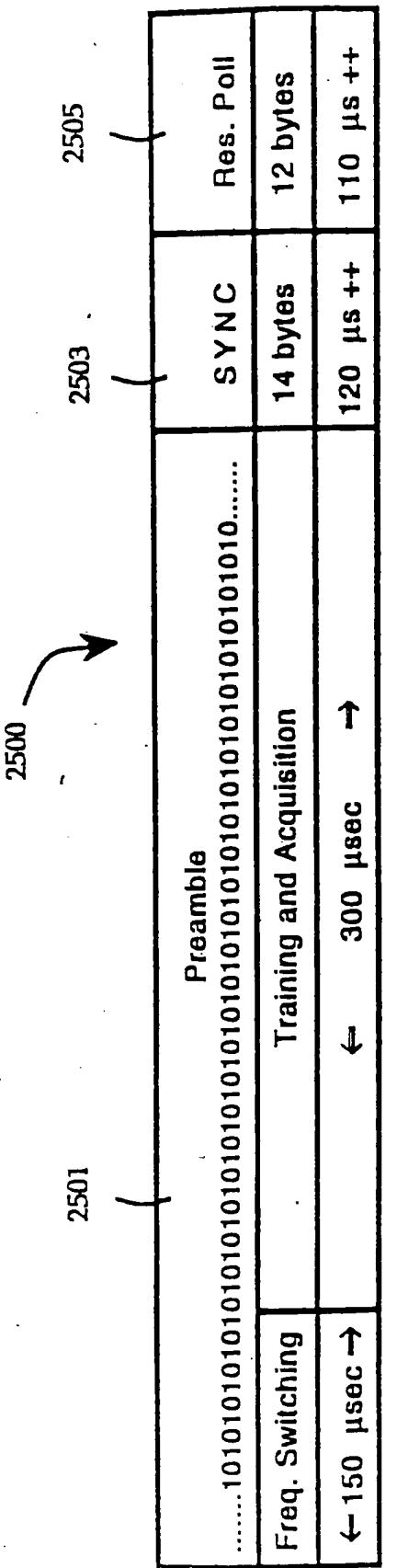


Figure 25

SYNC

↙ 2600

Byte	Content	Definition
1	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF <sub>H</sub> )
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type=SYNC, Priority Sync, or Priority SYNC with HELLO
5	Source Address 1	Control Point Address
6	Source Address 2	Control Point Address, 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8*	NET ID	Net ID
9*	Command/Status 1	Control Point Status
	2 Bits	Control Point Type
	2 Bits	Loading Factor
	1 Bit	Scheduled Service Availability
	3 Bits	Reserved
10*	Command/Status 2	Scheduled Services
	5 Bits	Number of Allocated TDMA Time Slots
	3 Bits	Number of Available Time Slots
11*	Random	Randomly Selected Character for Improved Encryption
12	CRC 1	Error Check
13	CRC 2	Error Check
14	FLAG	Frame Delimiter

Figure 26 \*Optionally Encrypted Bytes

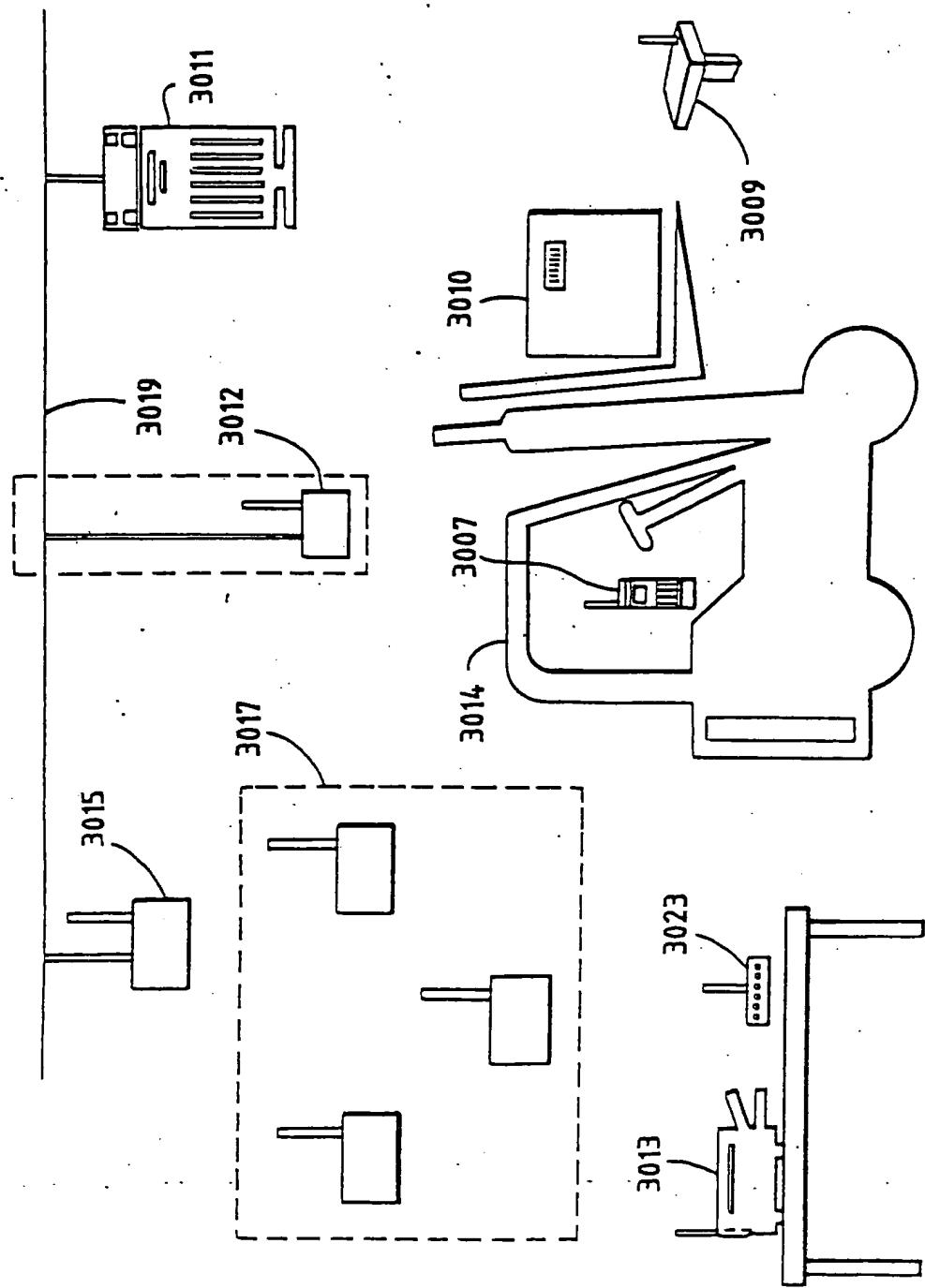
### Reservation Poll

2700

Byte	Content	Definition
1	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (RES = Broadcast = FFFF <sub>H</sub> )
3	Dest. Address 2	Destination Address, 2nd Byte
4	TYPE	Message Type = RES POLL
5	SLOT PARMs	3 Bits Number of Slots
6		5 Bits Probability Factor
7*	Sequence Index	Hopping Sequence ID
8*		Hopping Sequence Index
9*	NET PARMs	
		3 Bits Hop Rate
		2 Bits Al's per Hop
		3 Bits Reserved
10	CRC 1	Error Check
11	CRC 2	Error Check
12	FLAG	Frame Delimiter

Figure 27

Fig. 28A



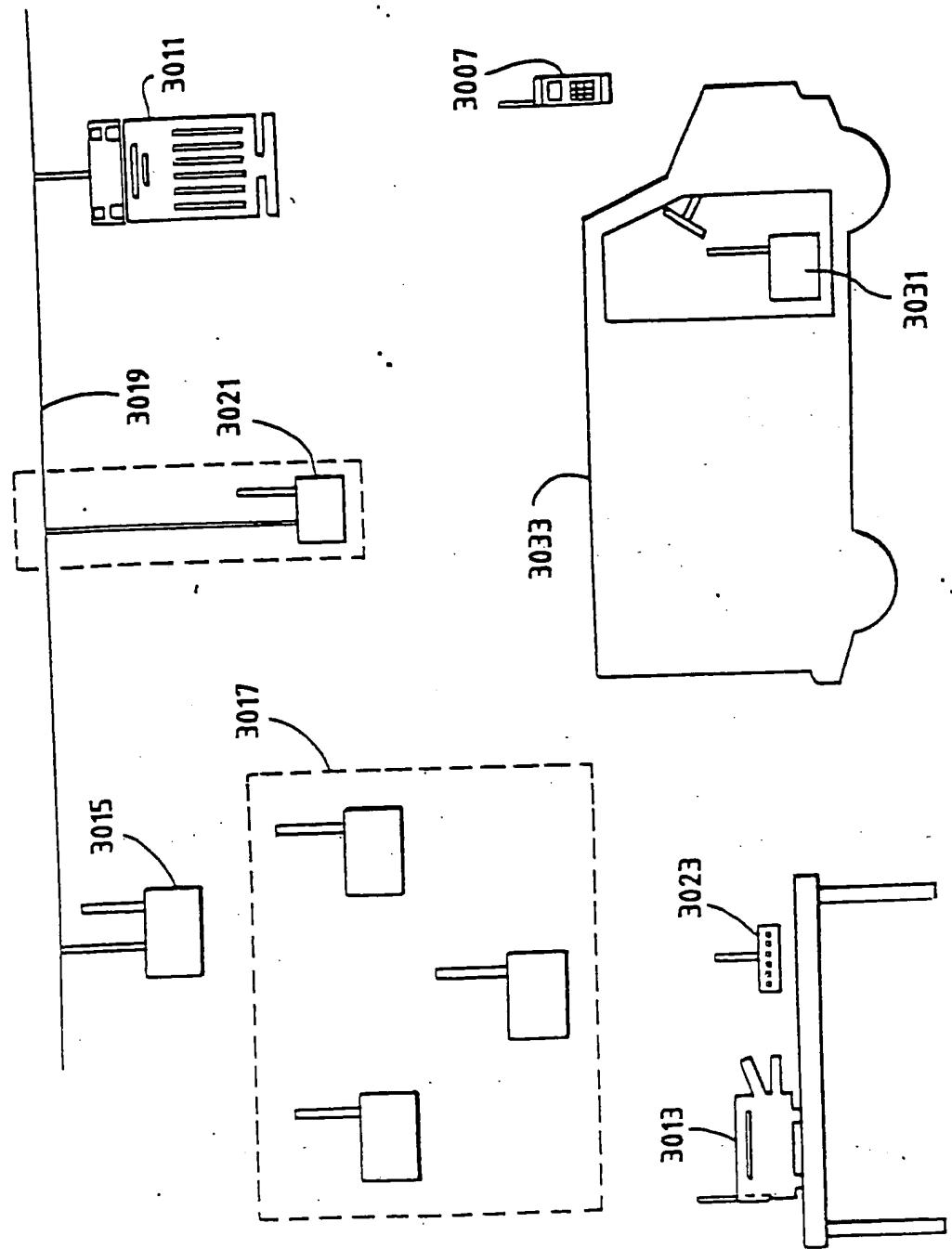


Fig. 28b

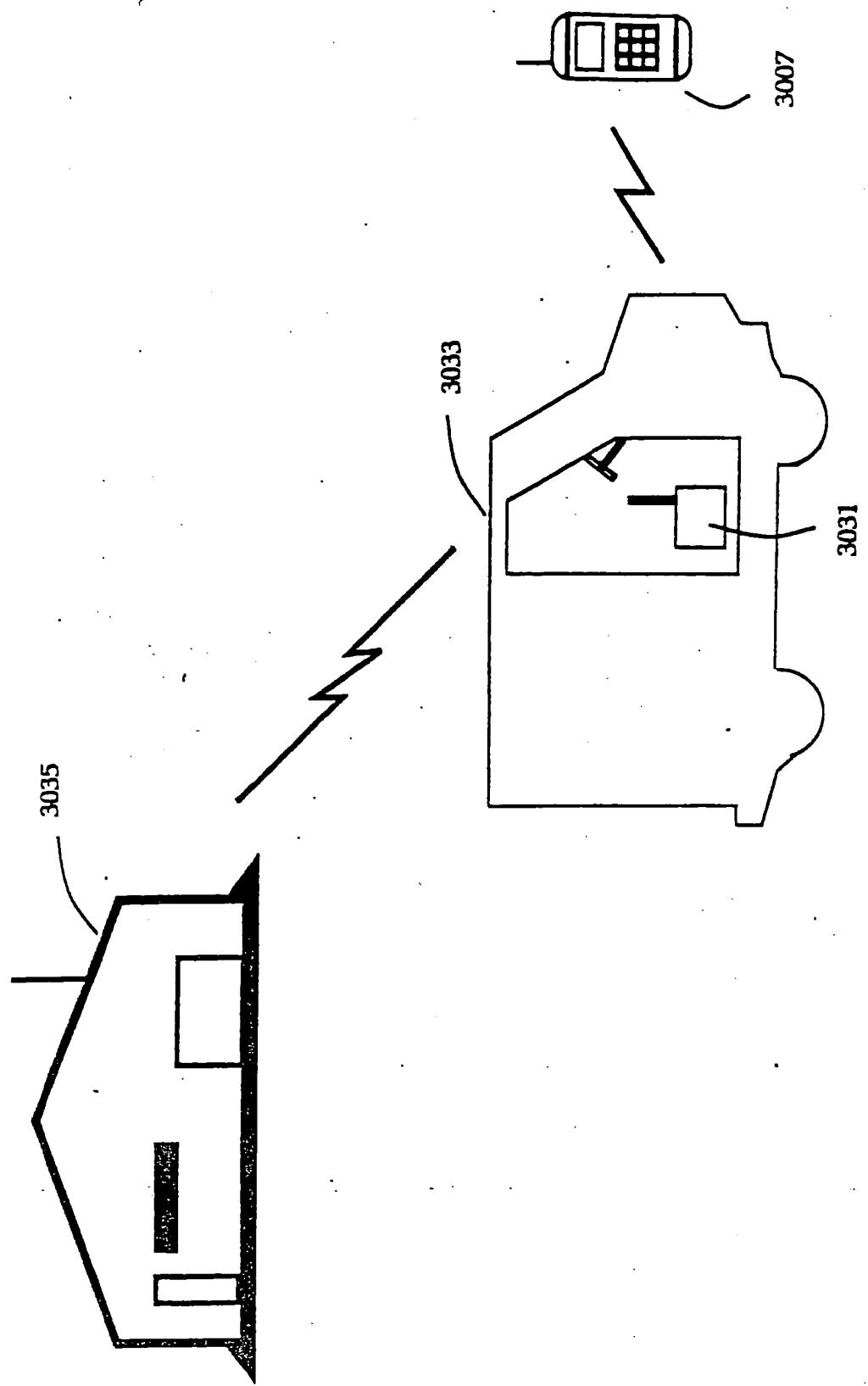
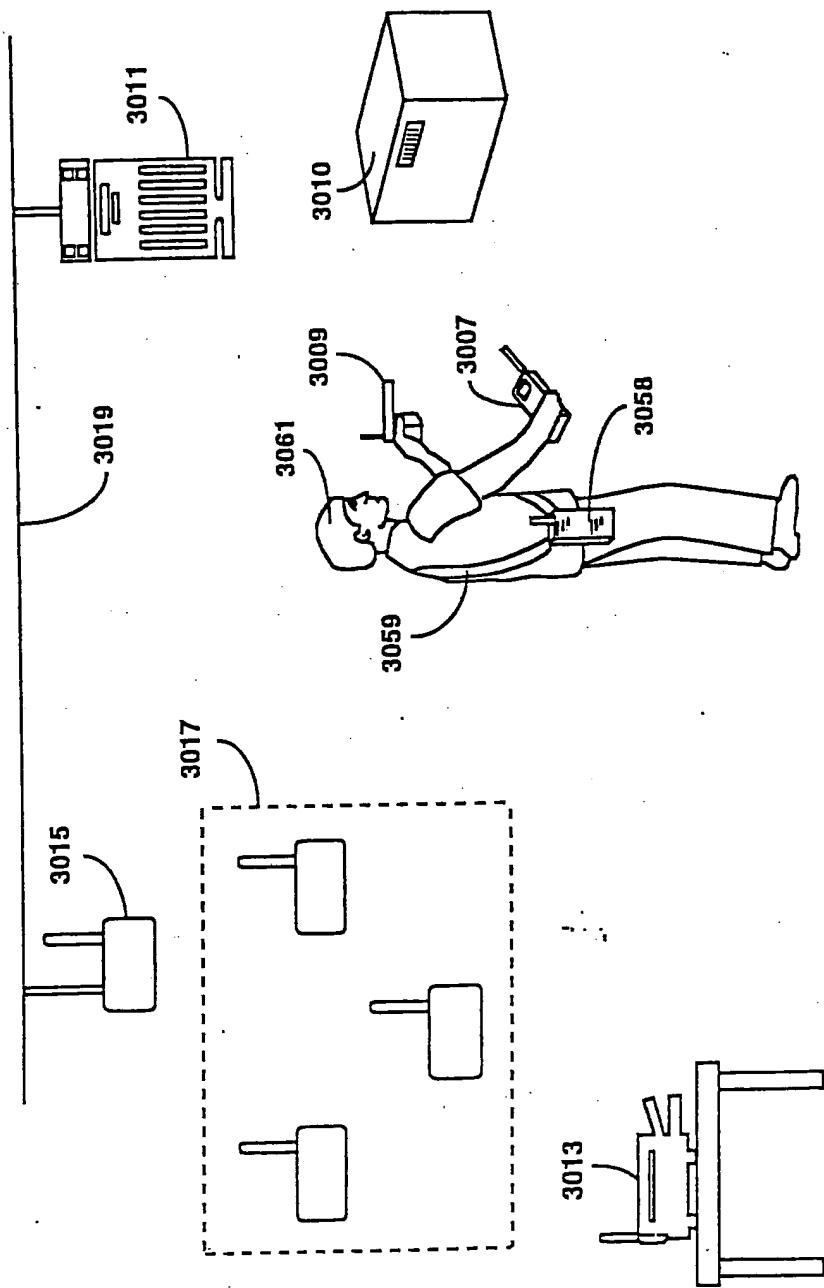
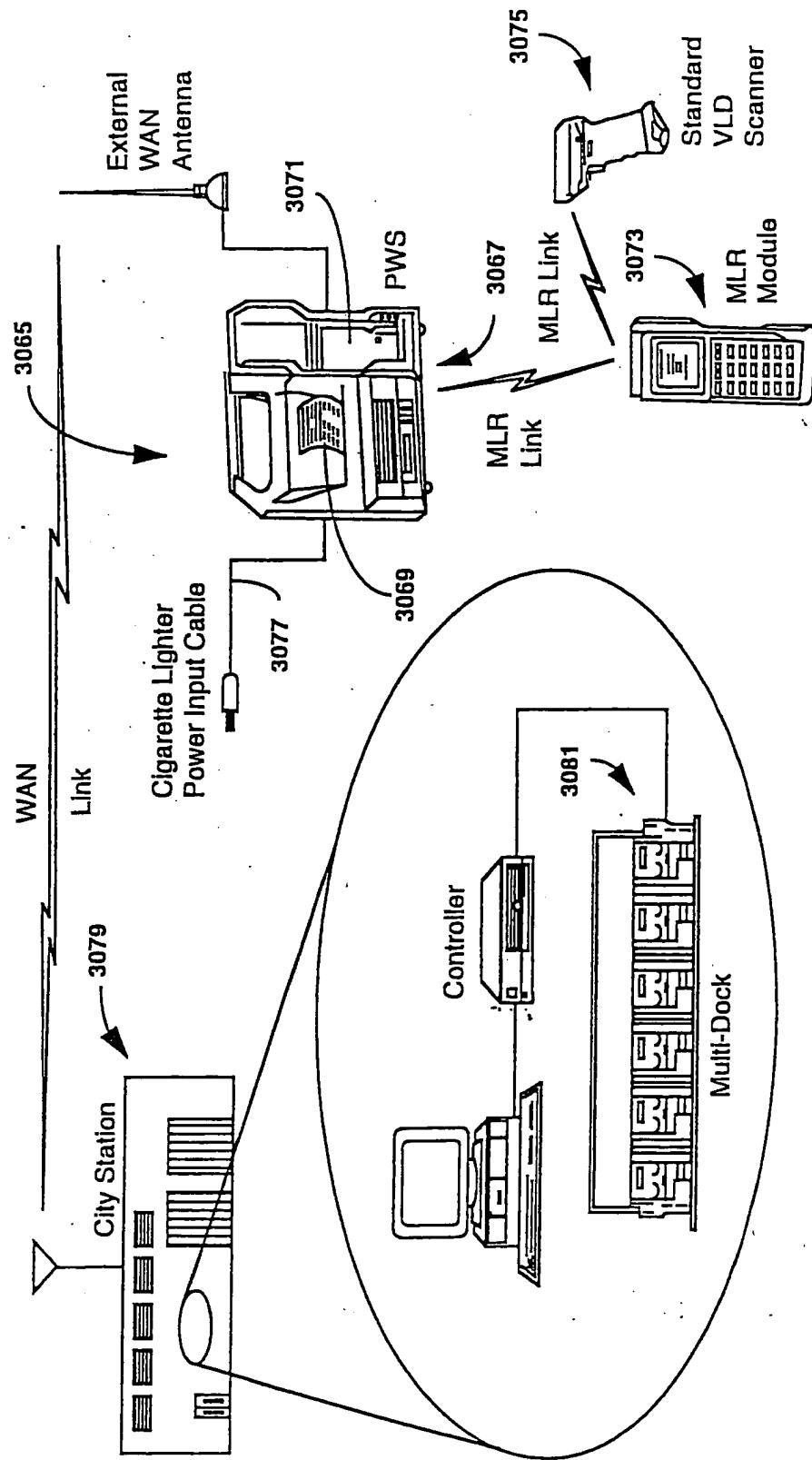


Figure 28C

Fig. 29A



**Fig. 29B**



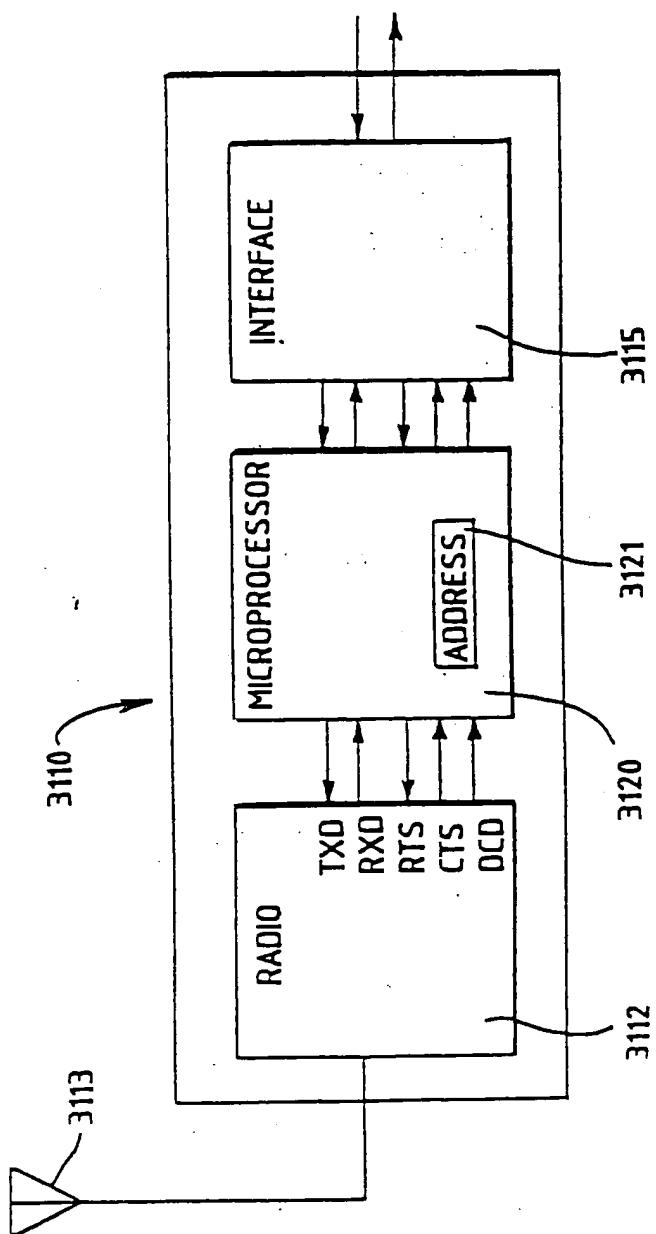
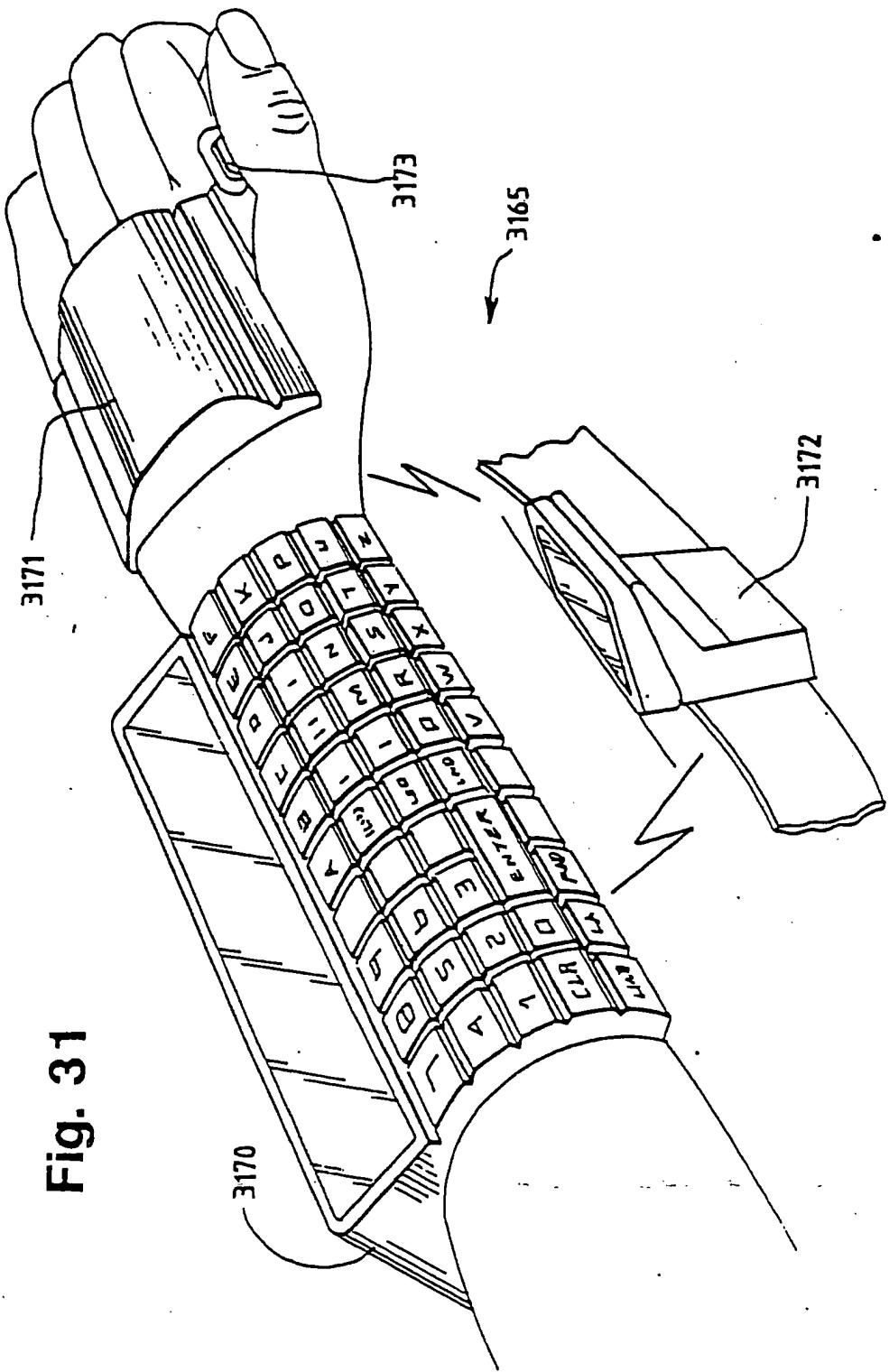
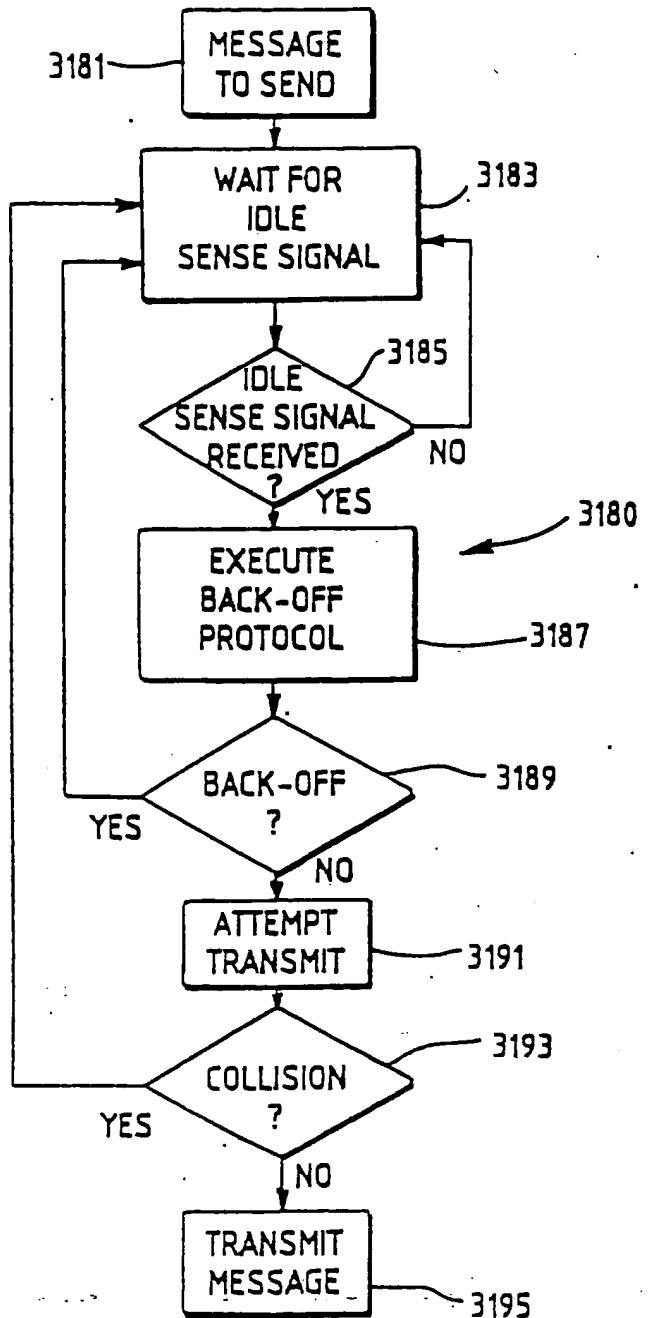


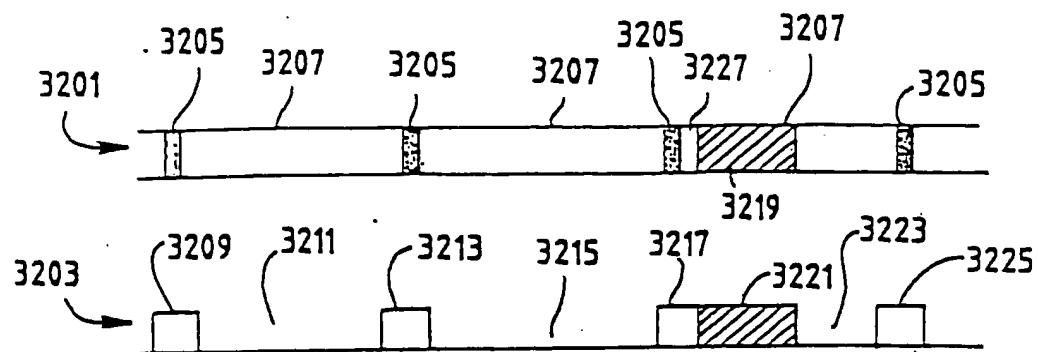
Fig. 30



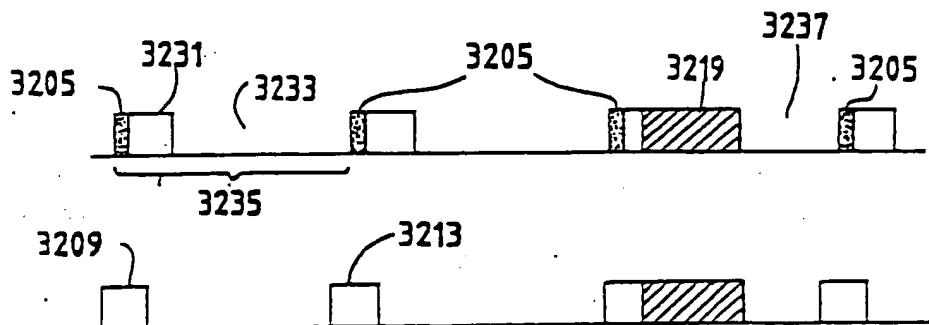
**Fig. 31**

**Fig. 32**

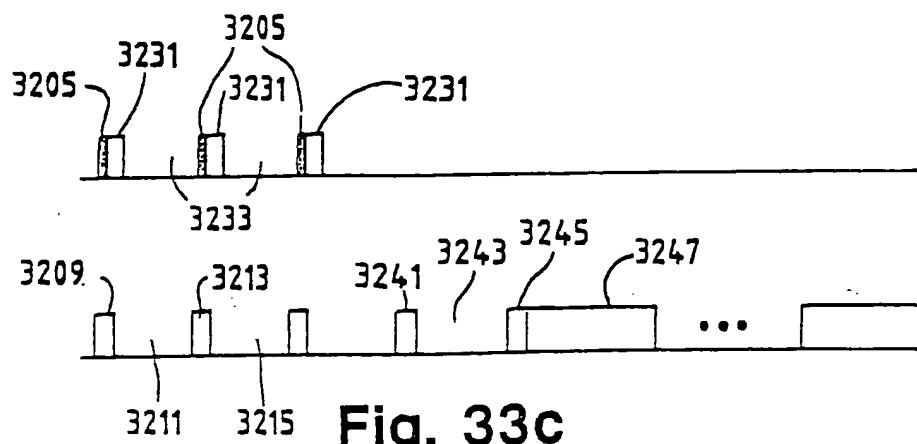




**Fig. 33a**

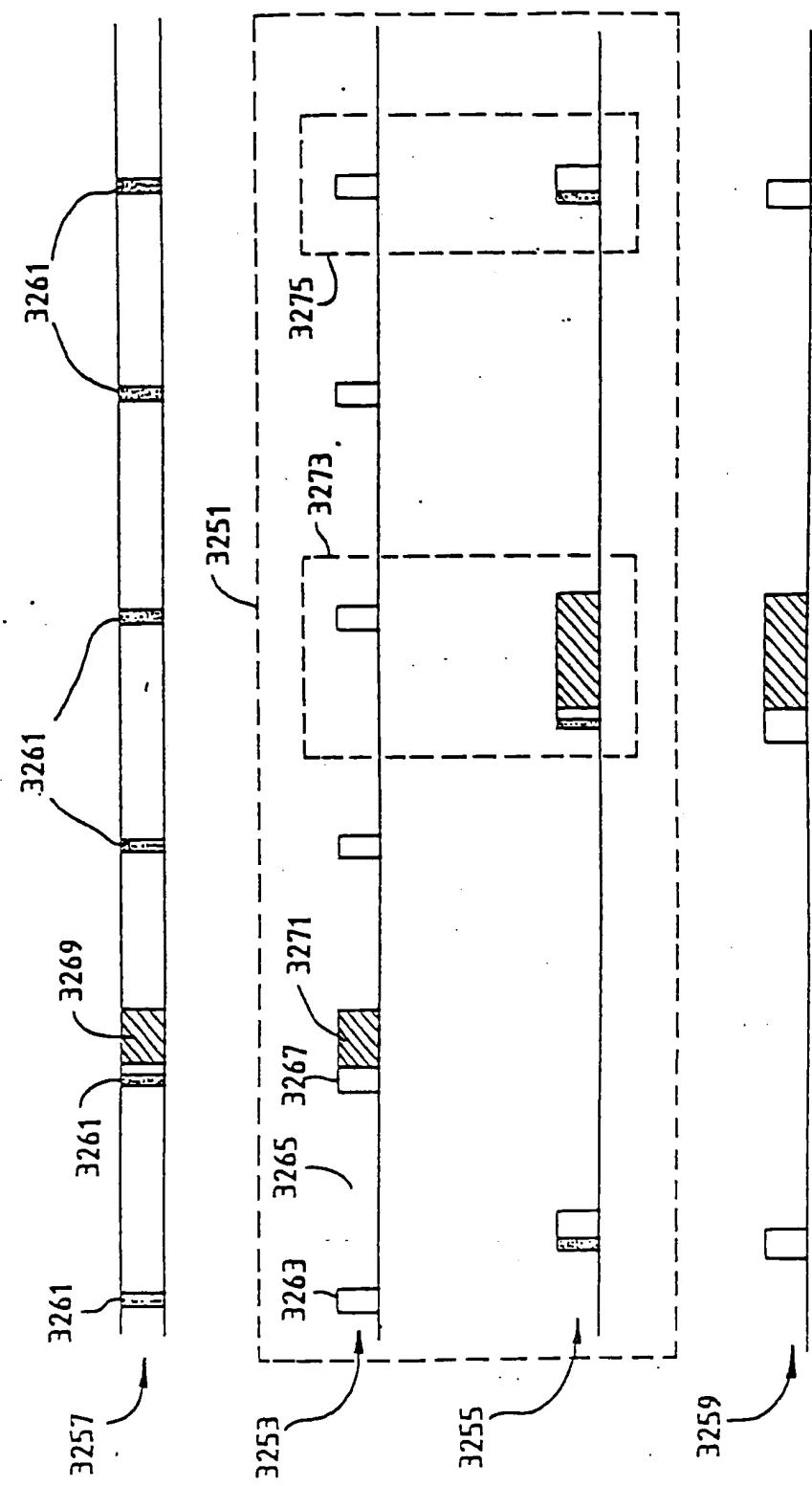


**Fig. 33b**



**Fig. 33c**

**Fig. 34**



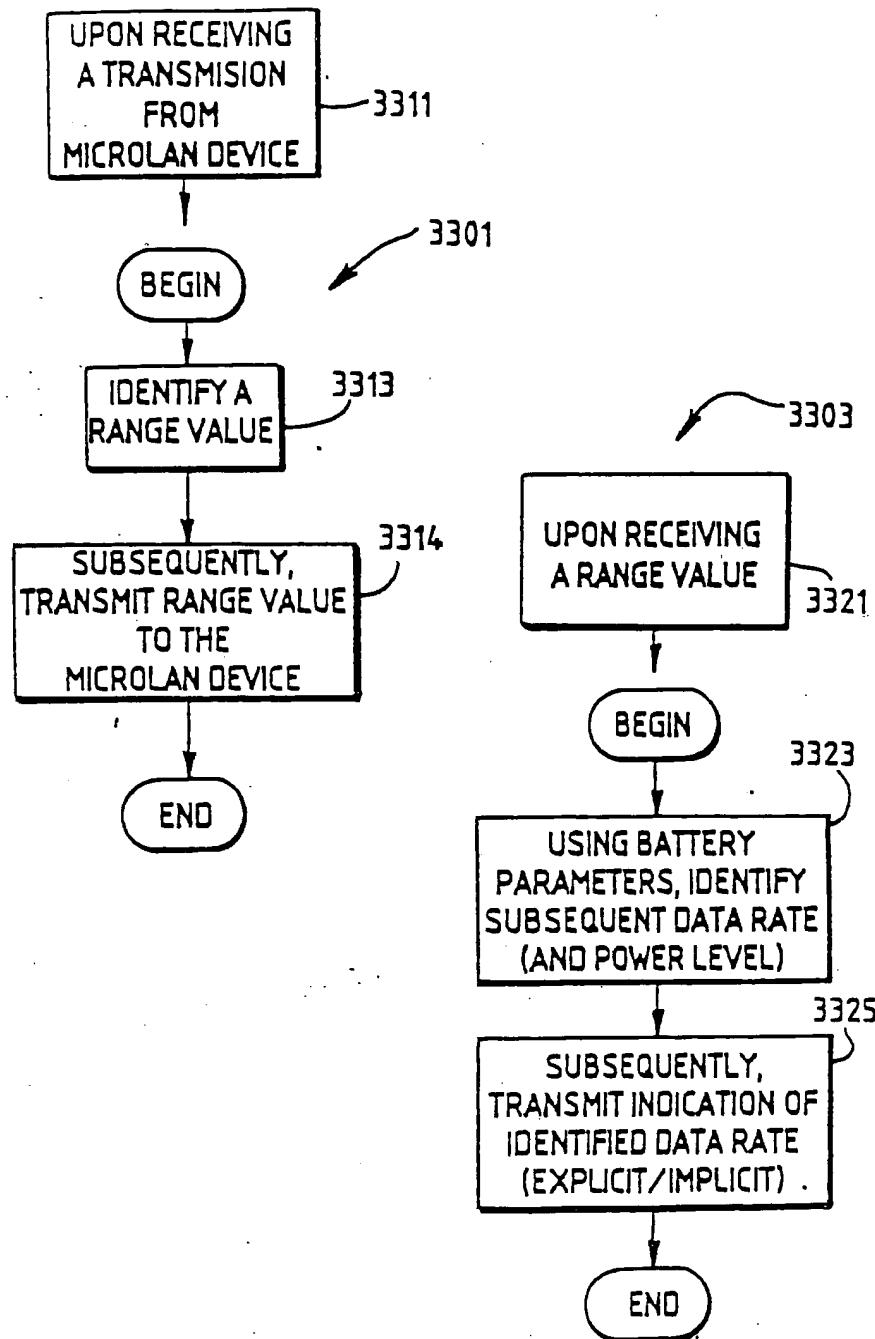
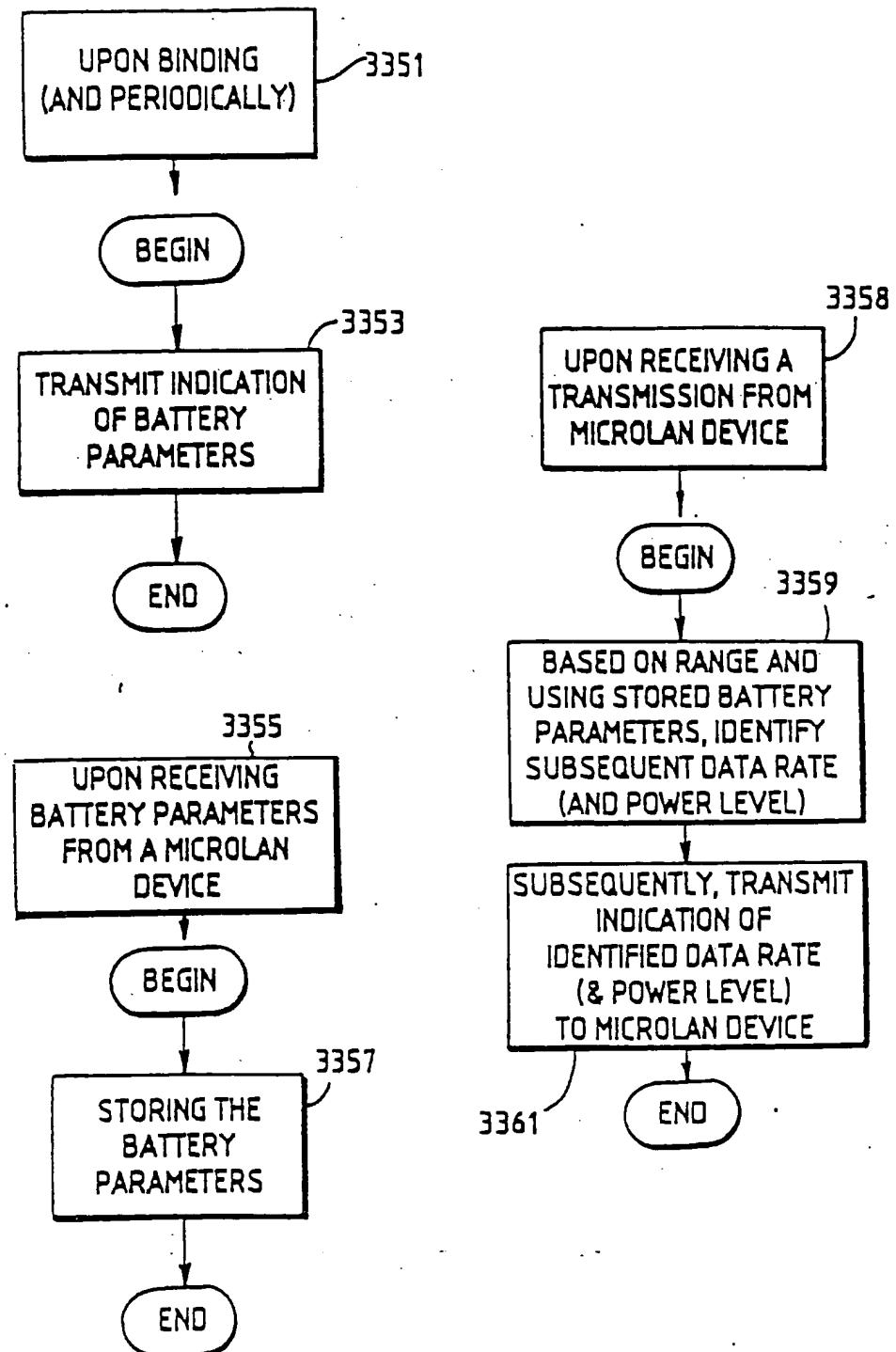


Fig. 35

Fig. 36



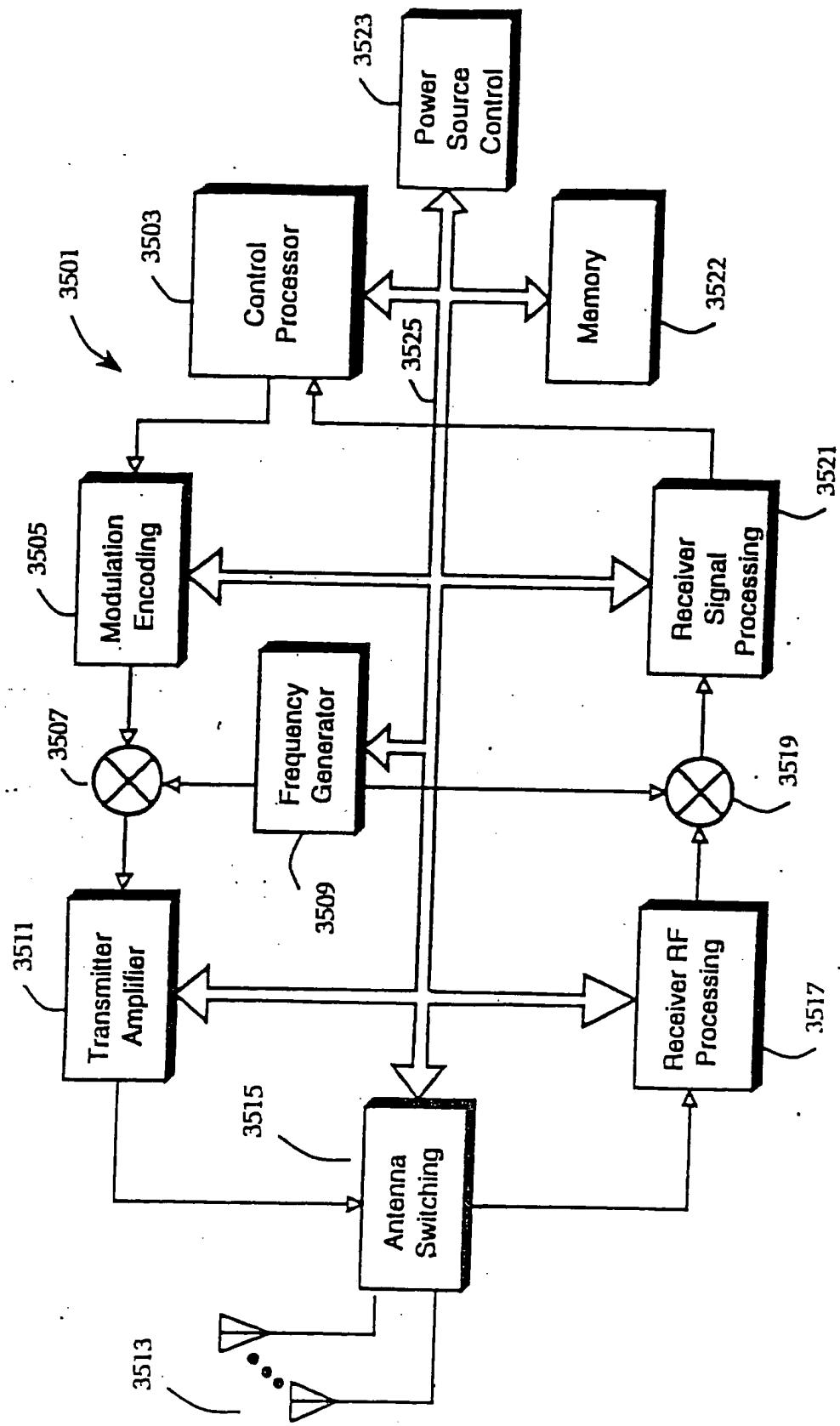


Figure 37

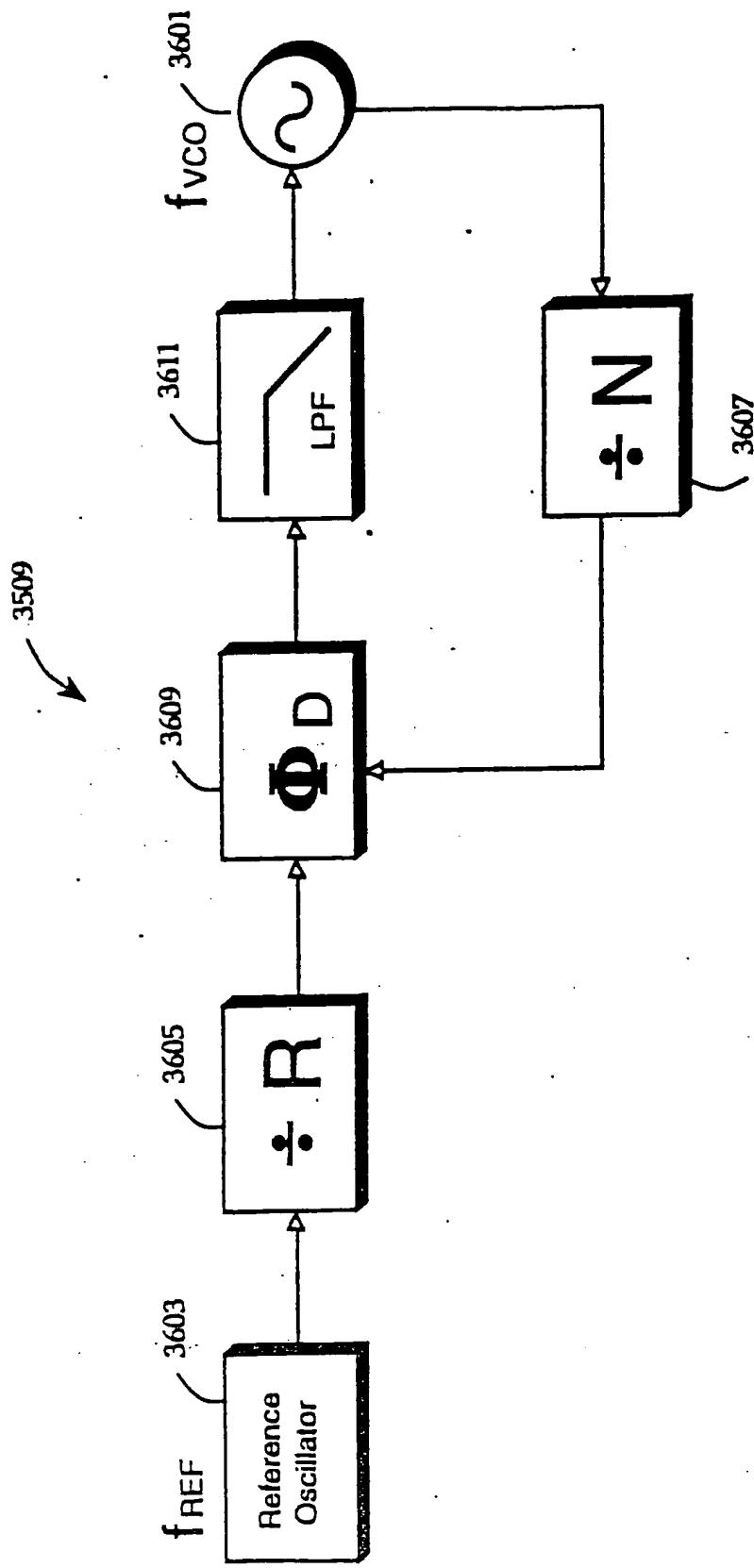


Figure 38

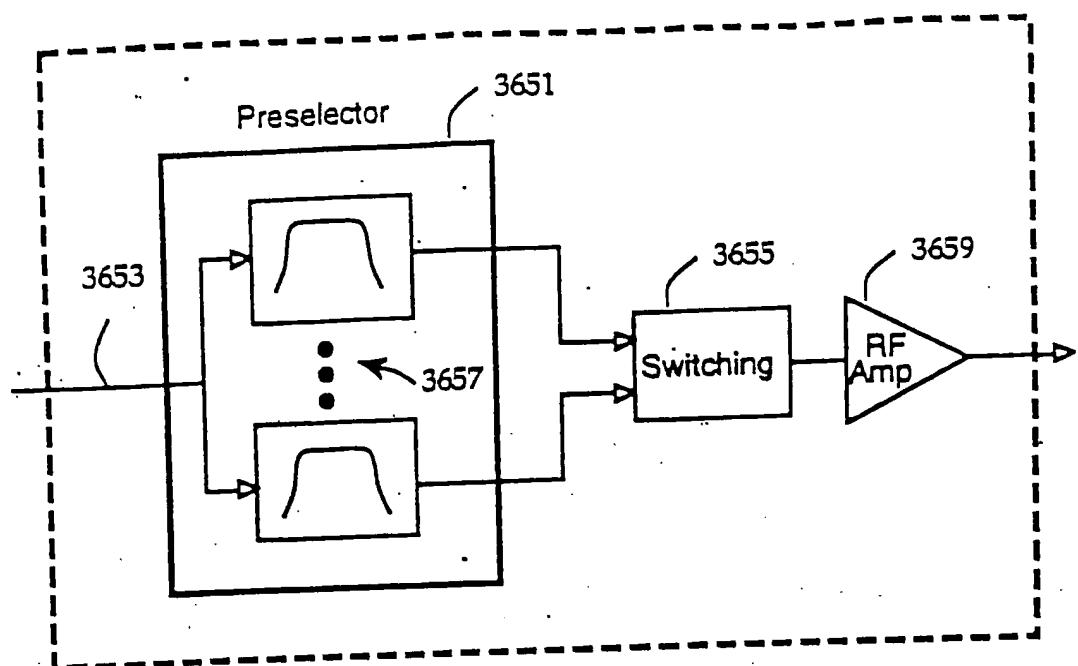


Figure 39

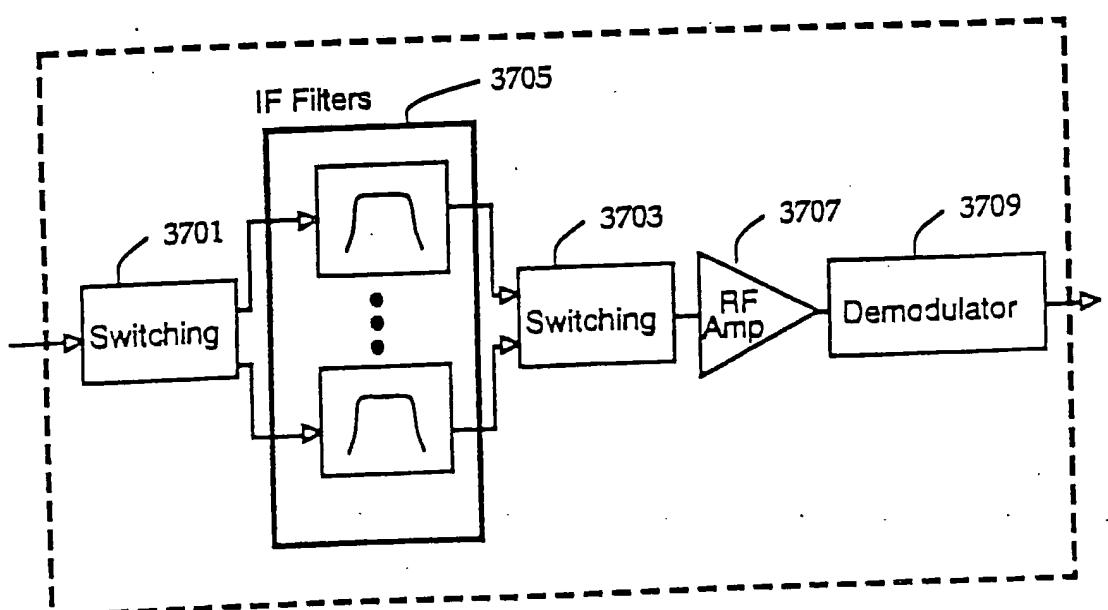


Figure 40

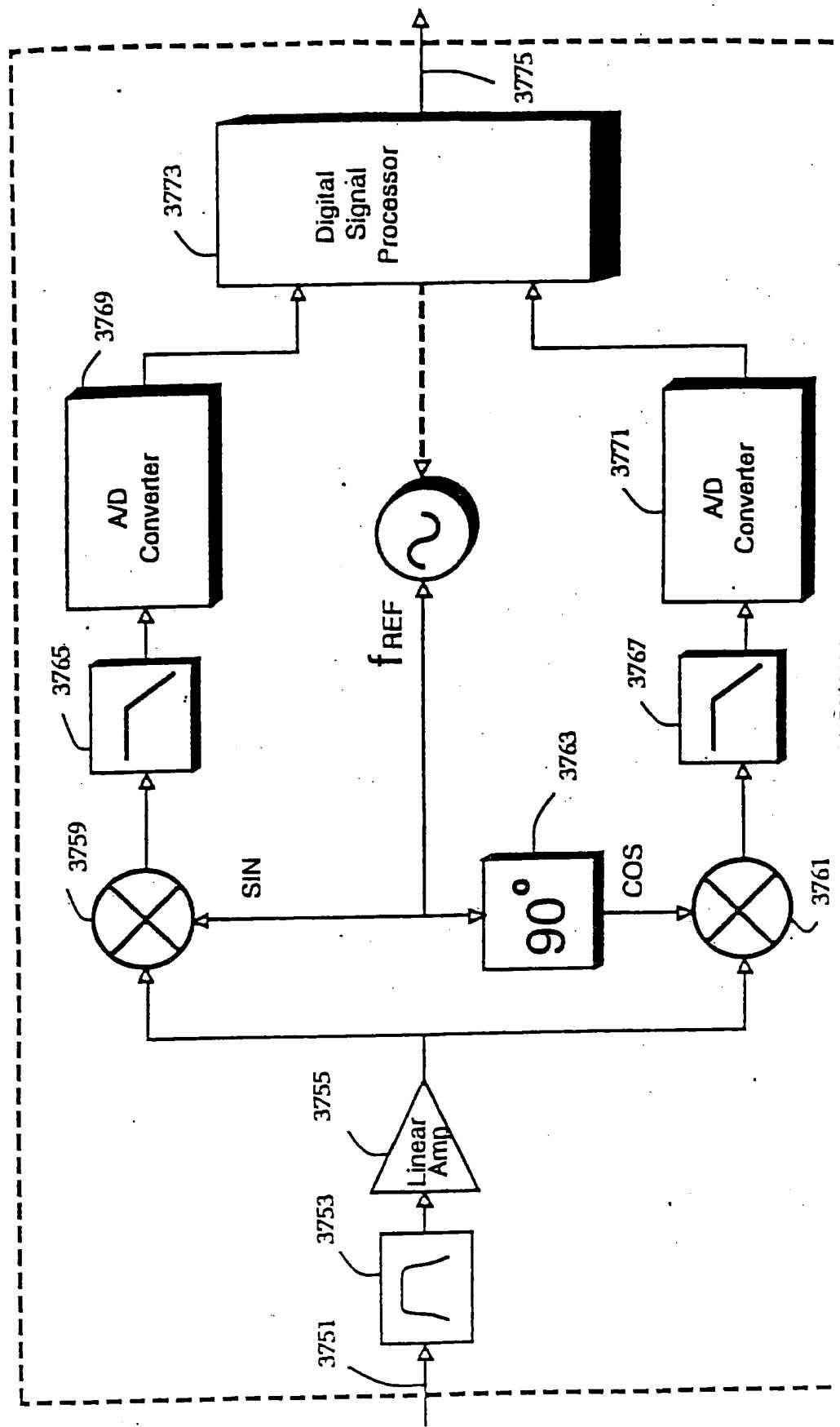


Figure 41

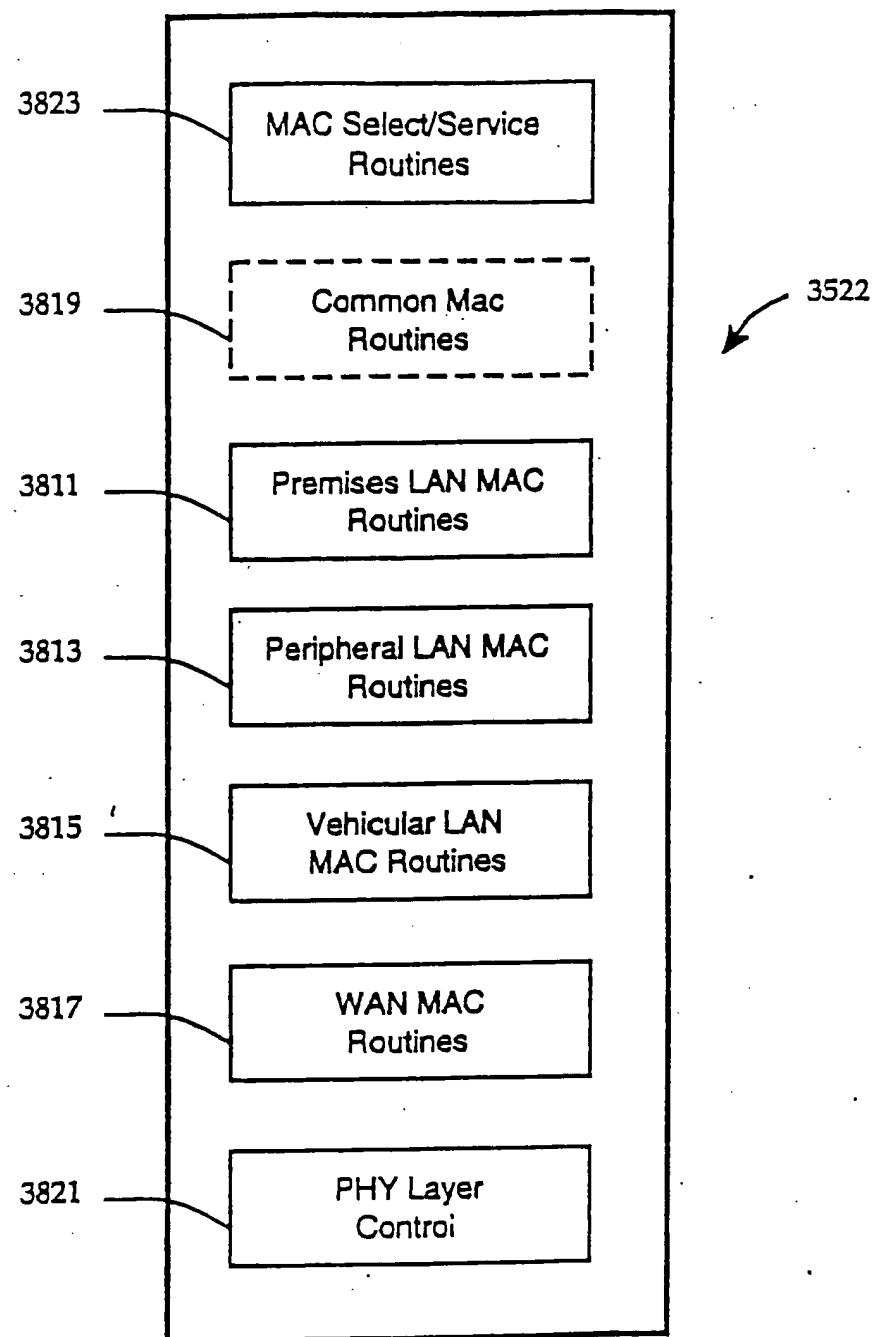


Figure 42

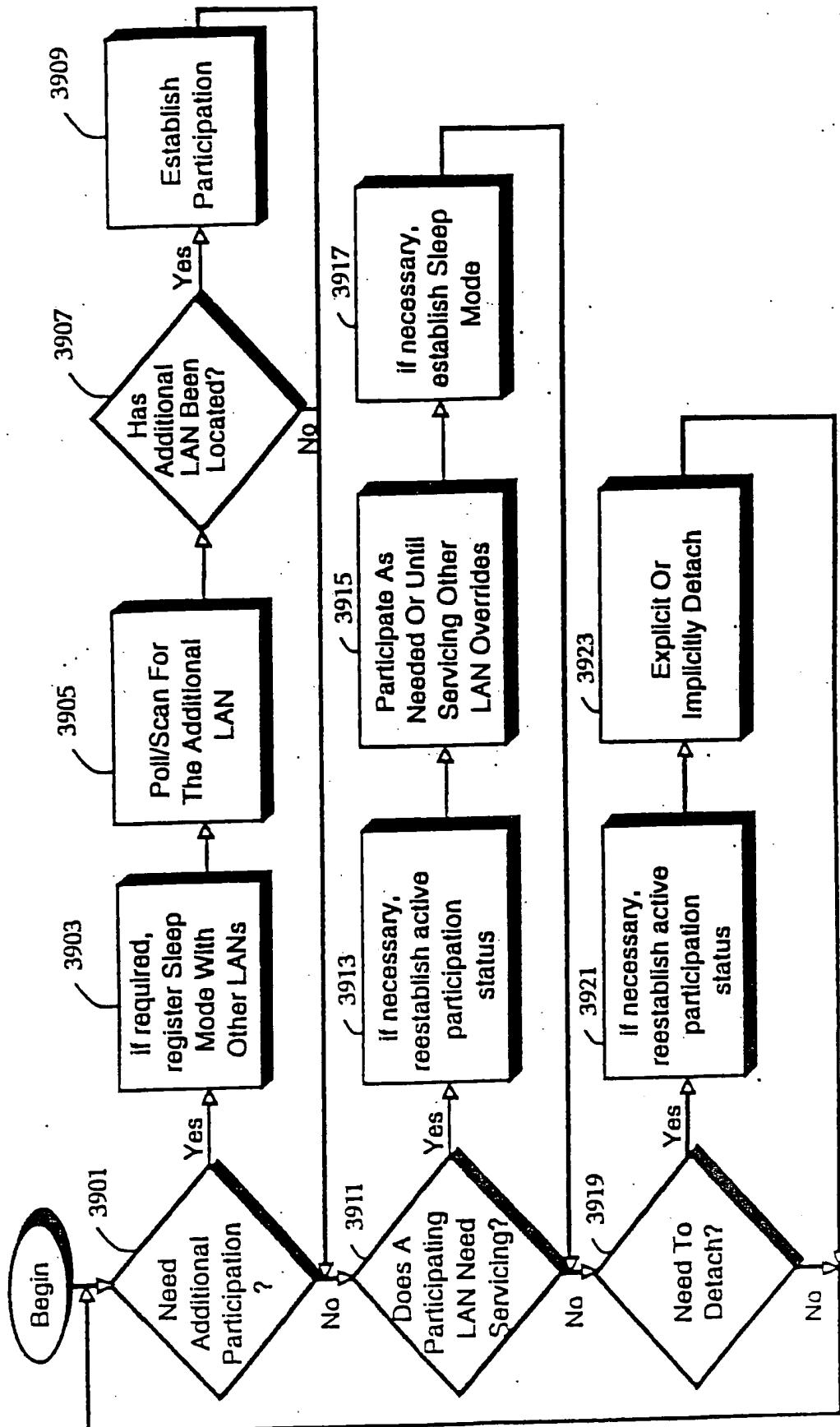


Figure 43

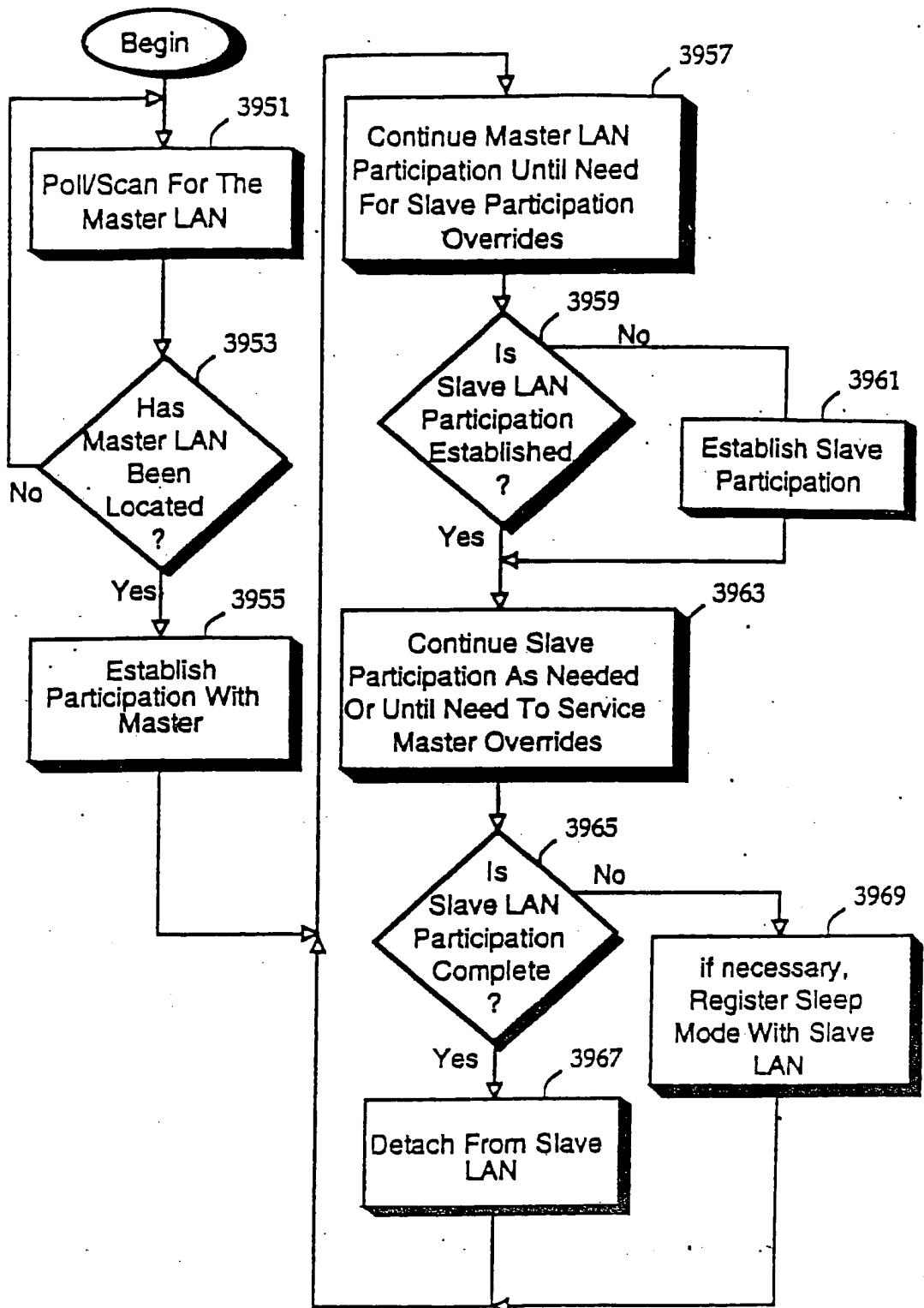
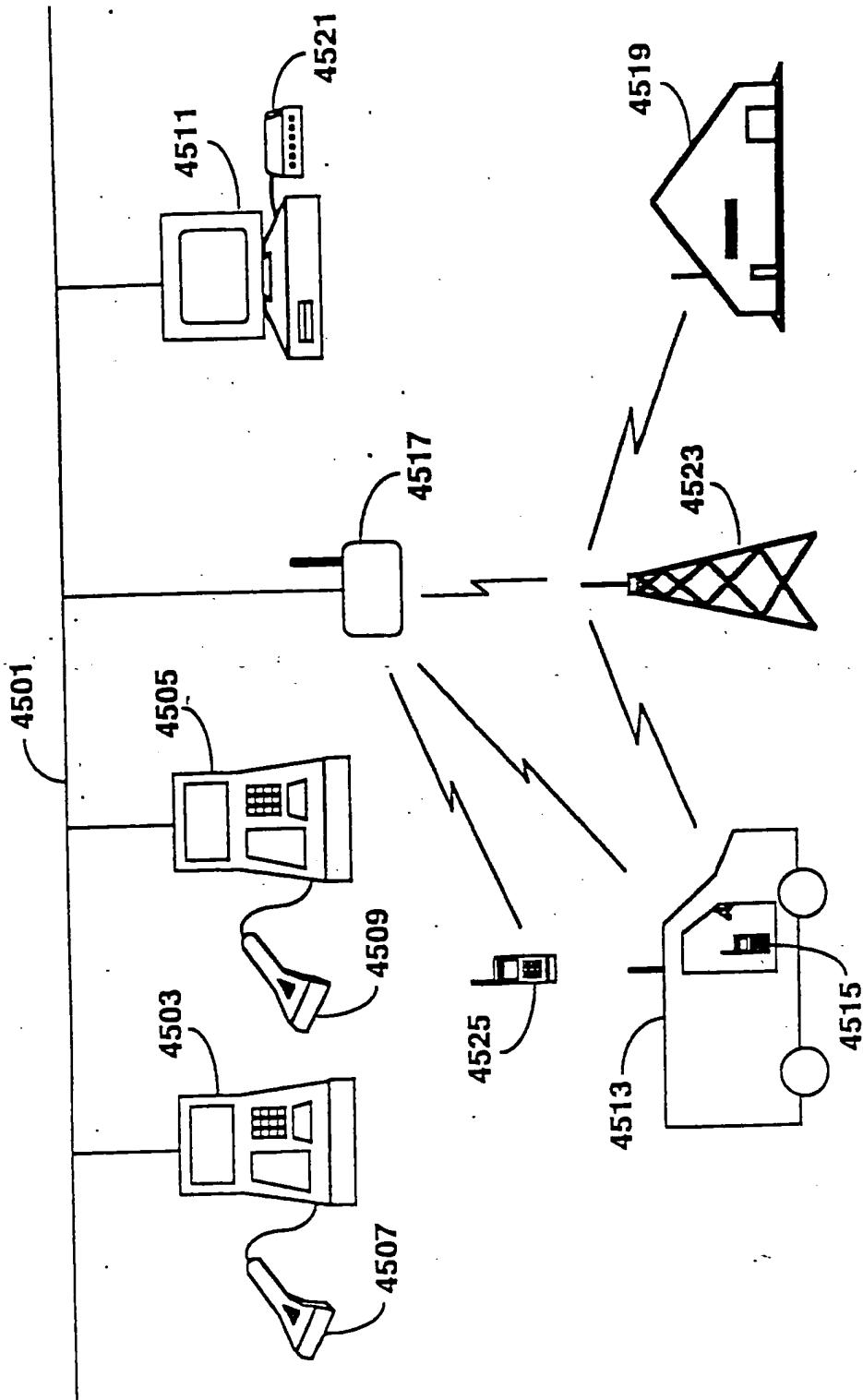
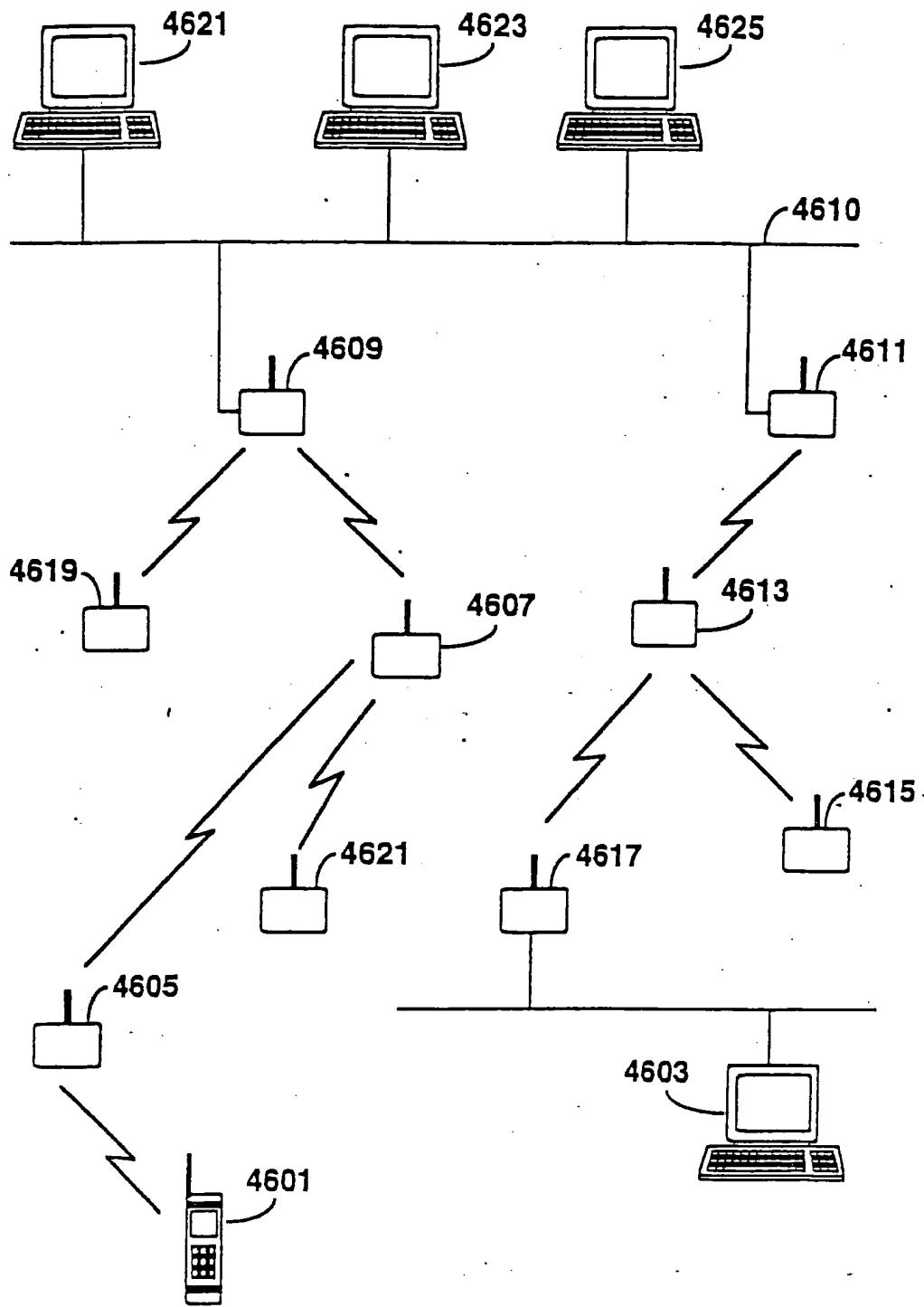


Figure 44

**Fig. 45**





**Fig. 46a**

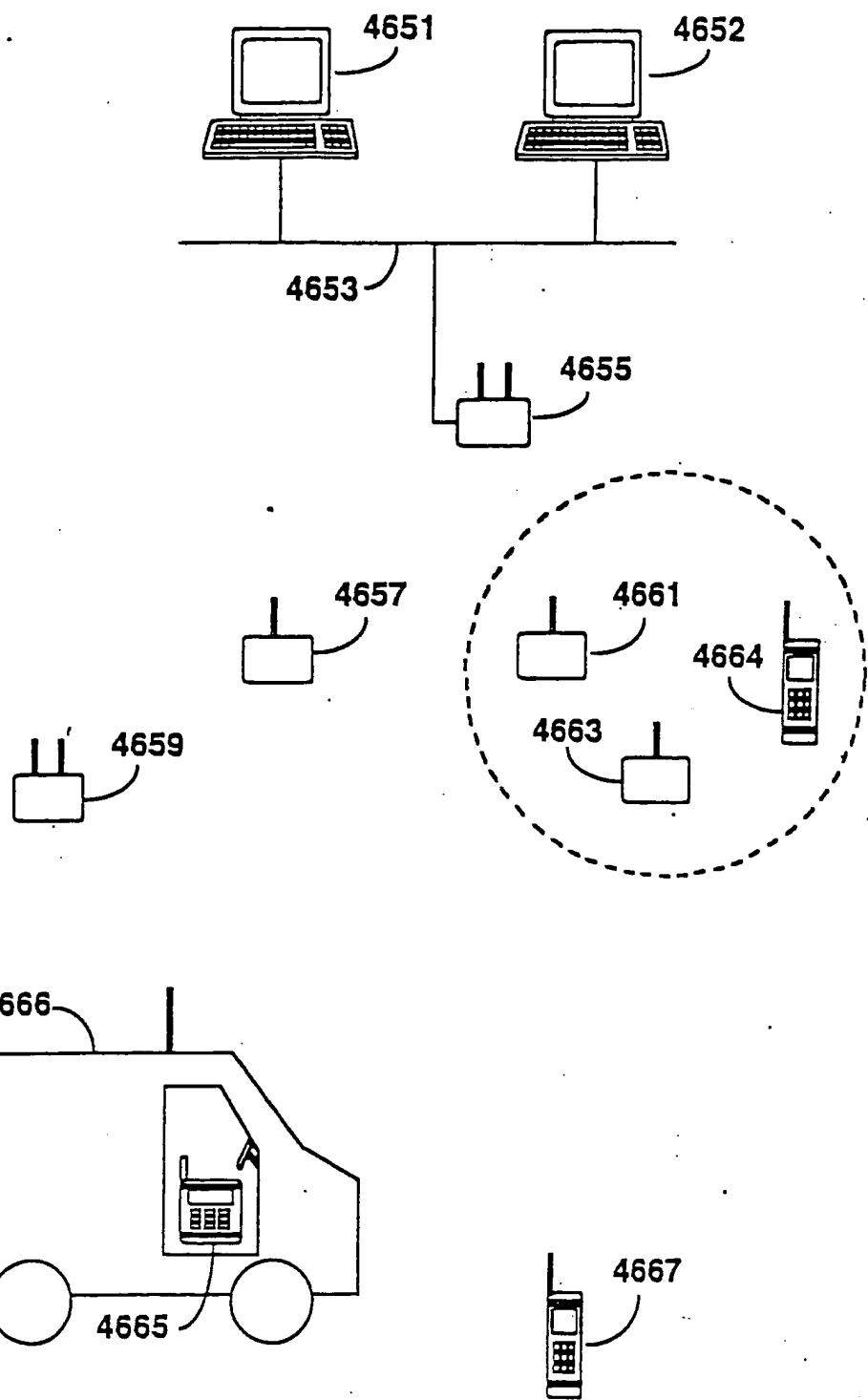


Fig. 46b

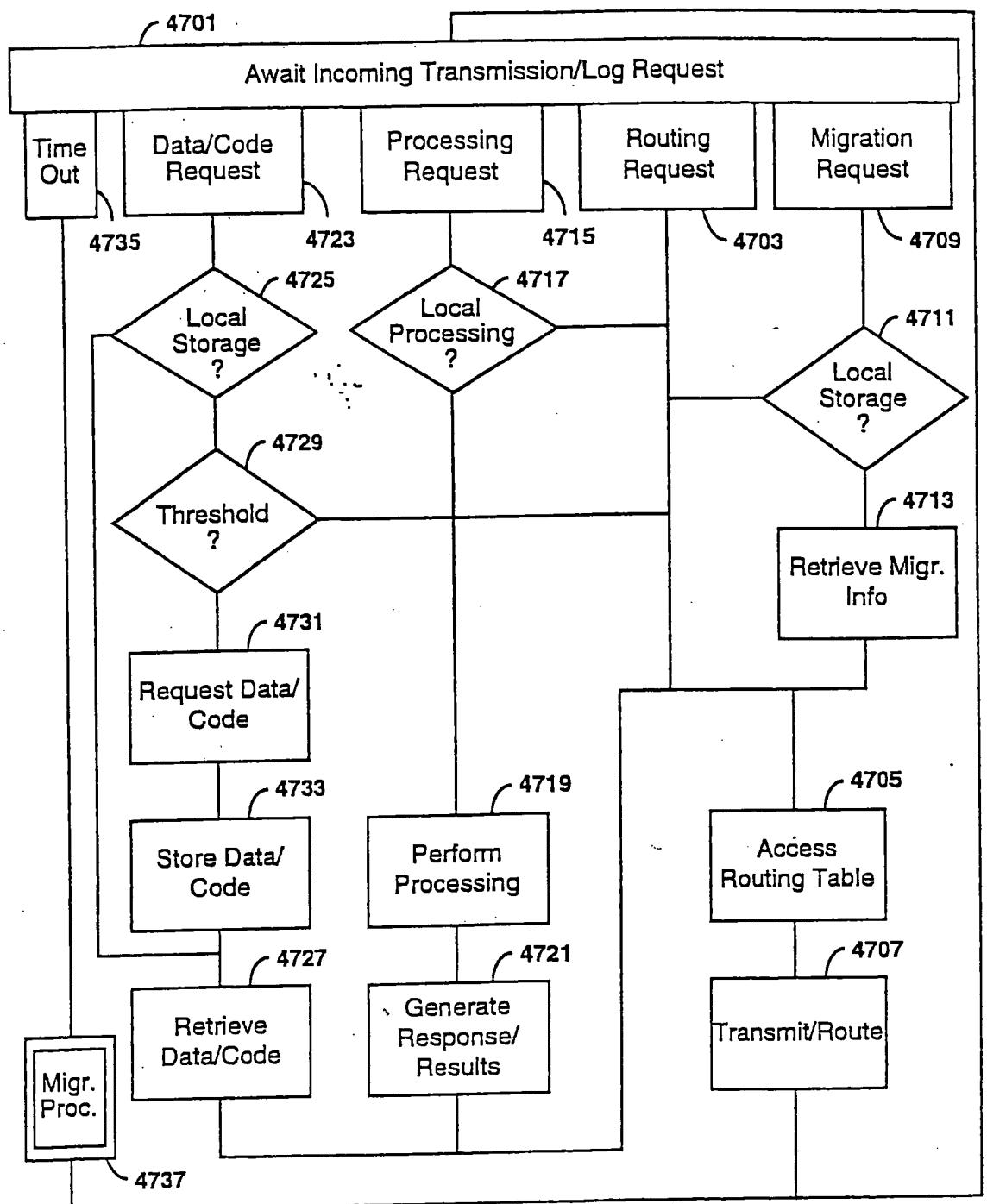


Fig. 47a

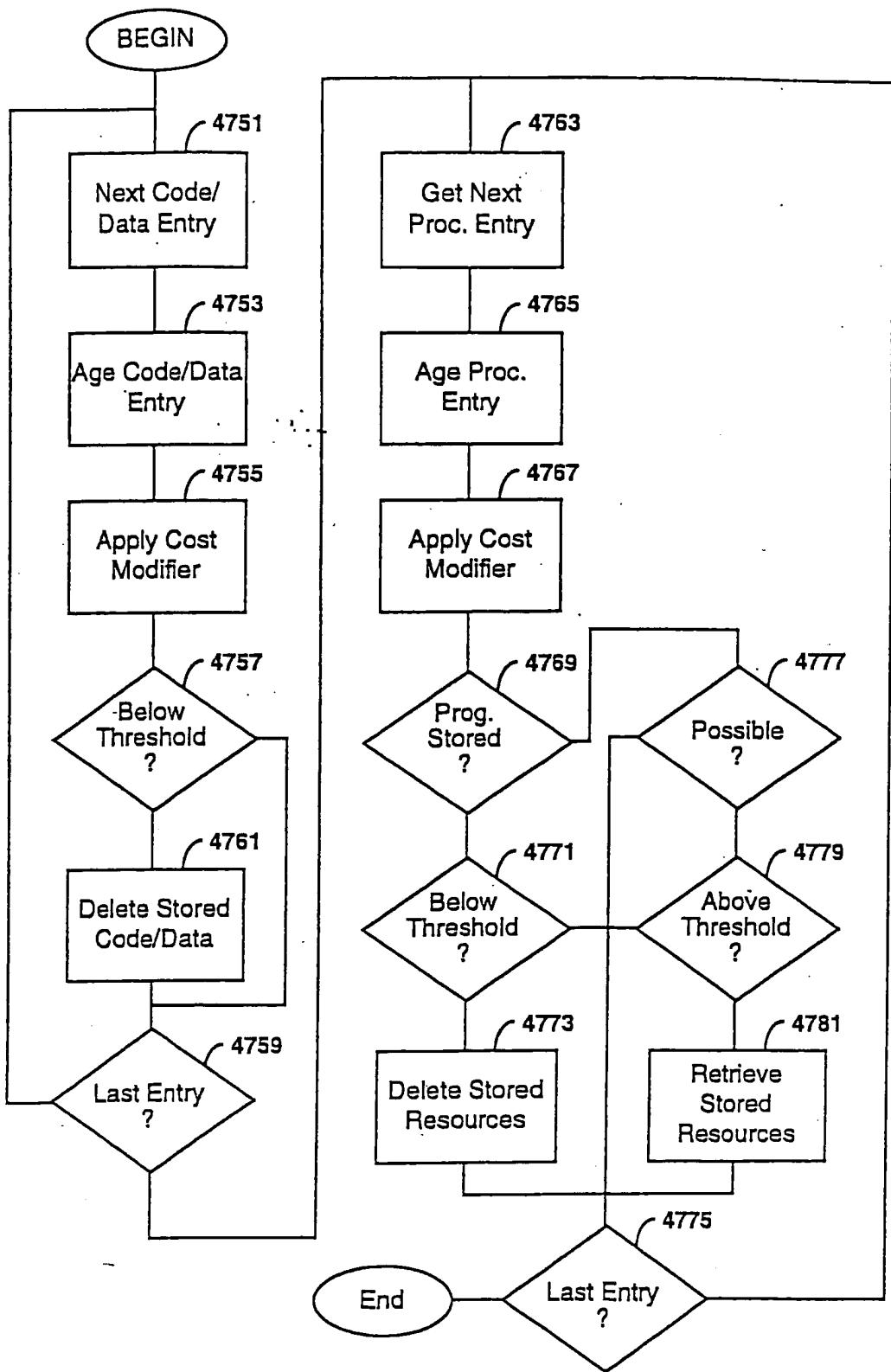
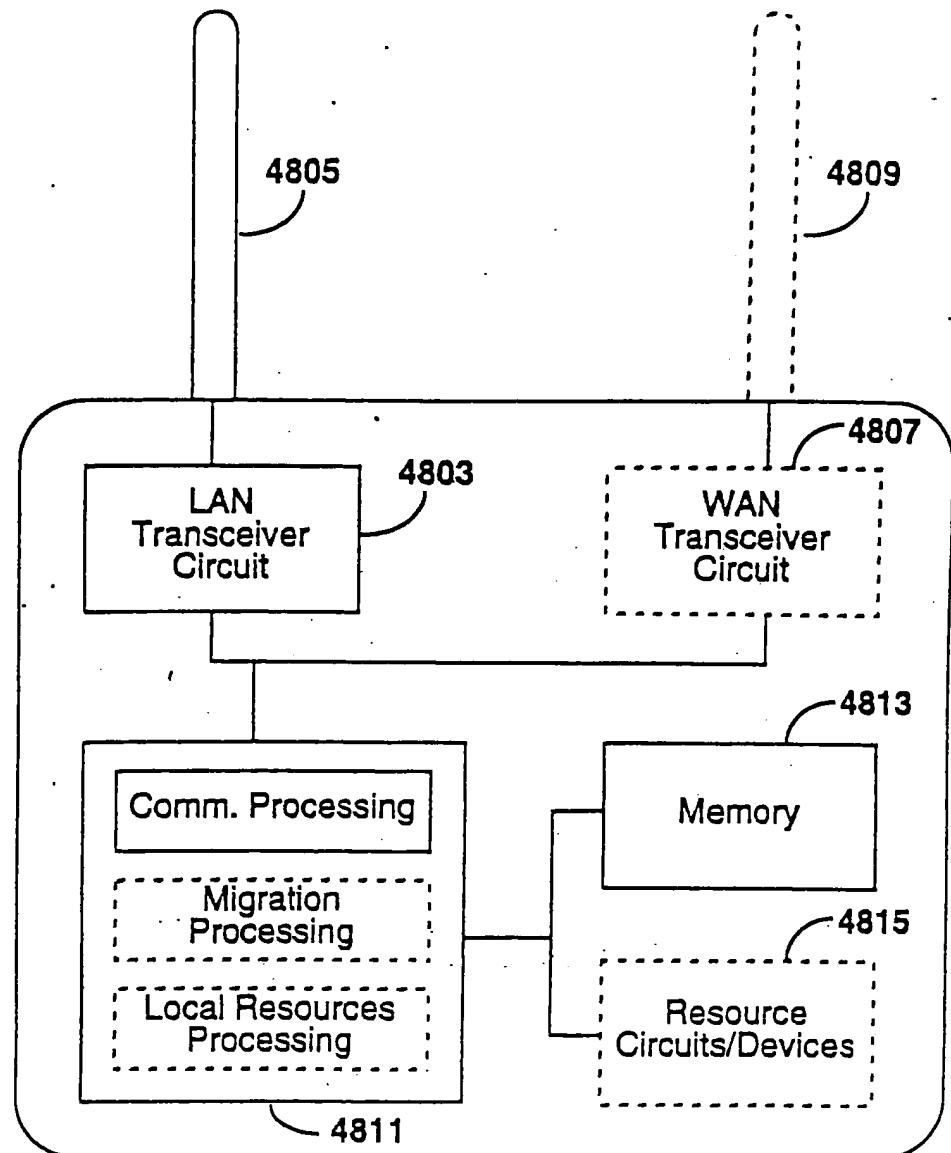


Fig. 47b



**Fig. 48**

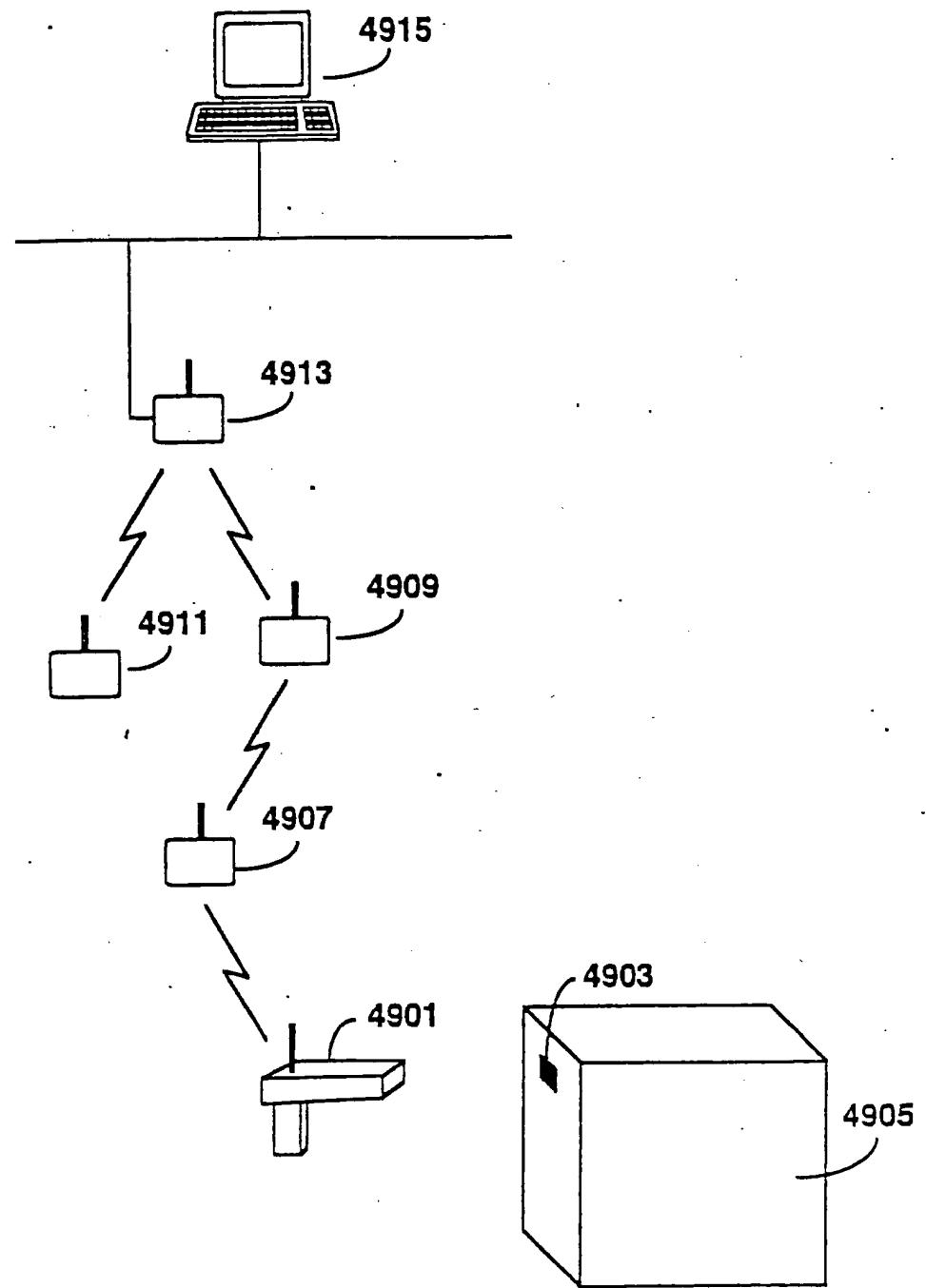


Fig. 49a

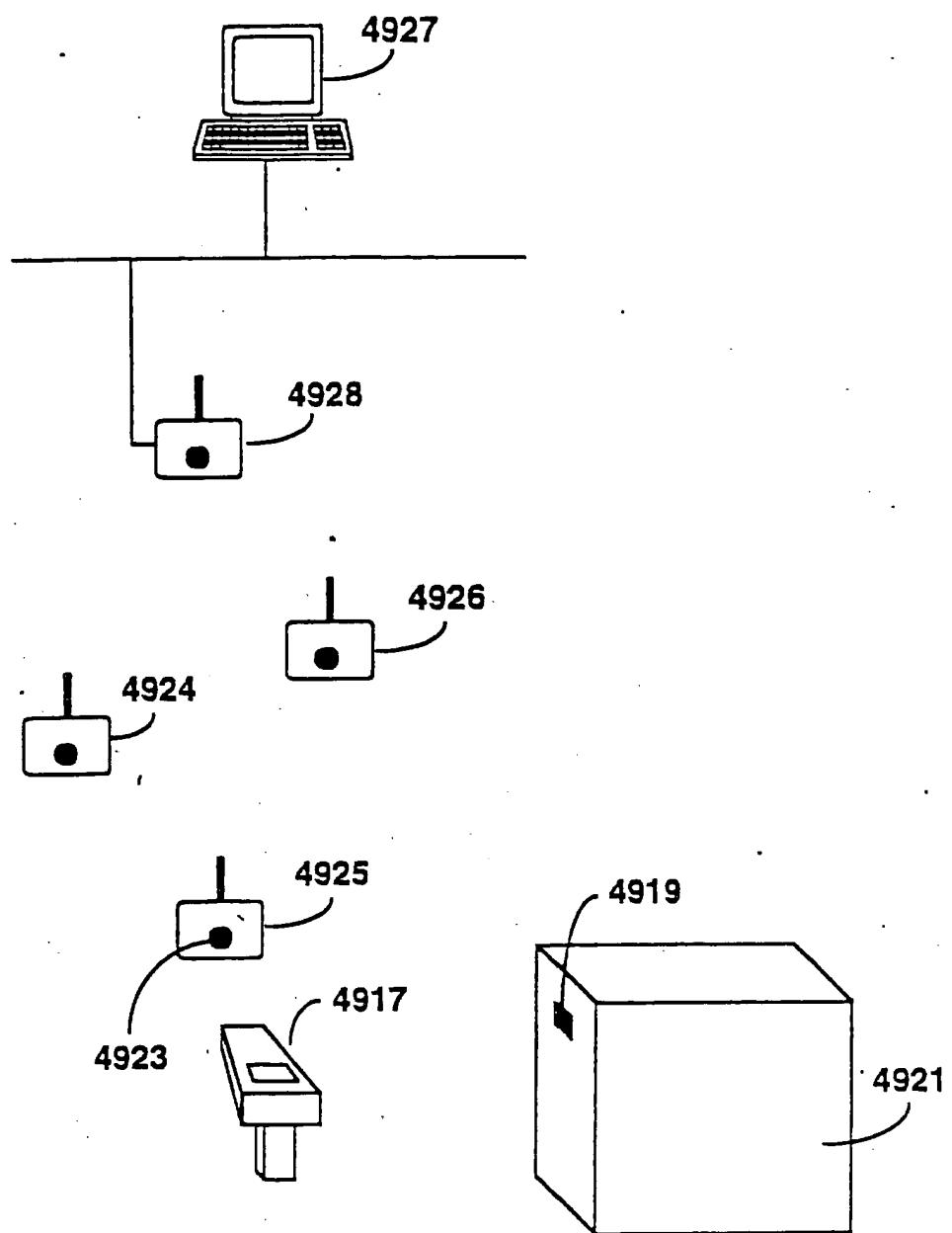
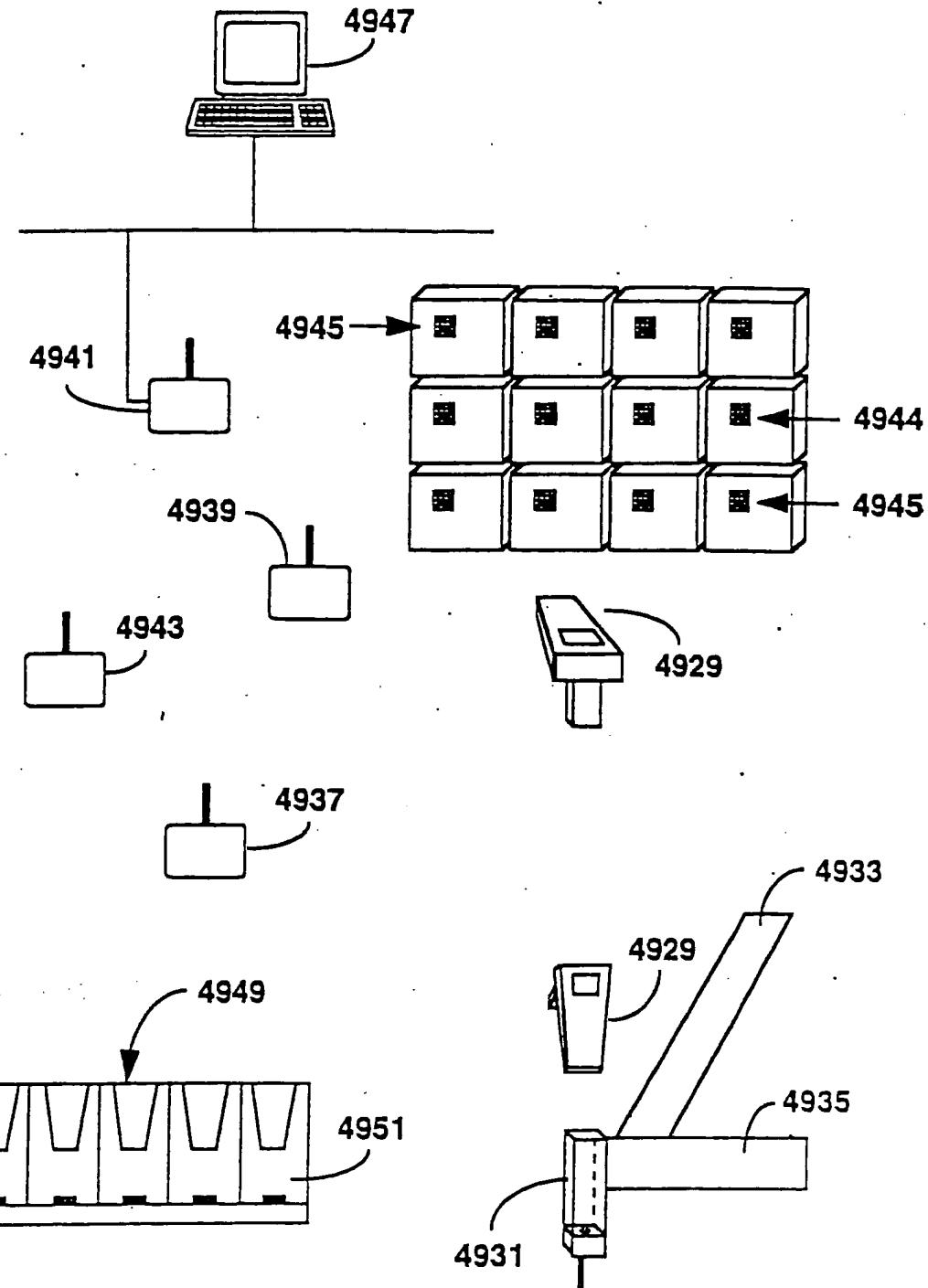
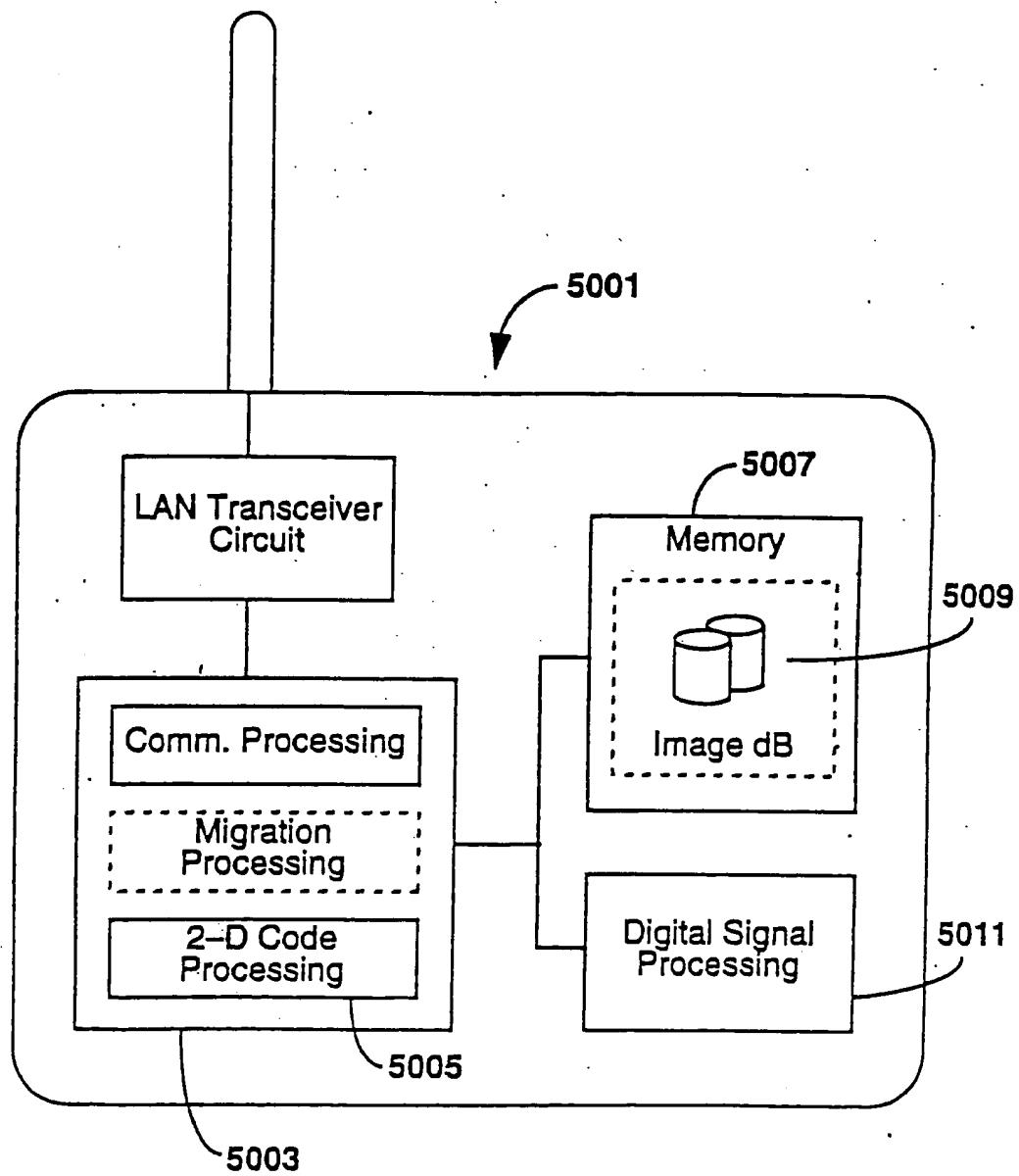


Fig. 49b



**Fig. 49c**



**Fig. 50**

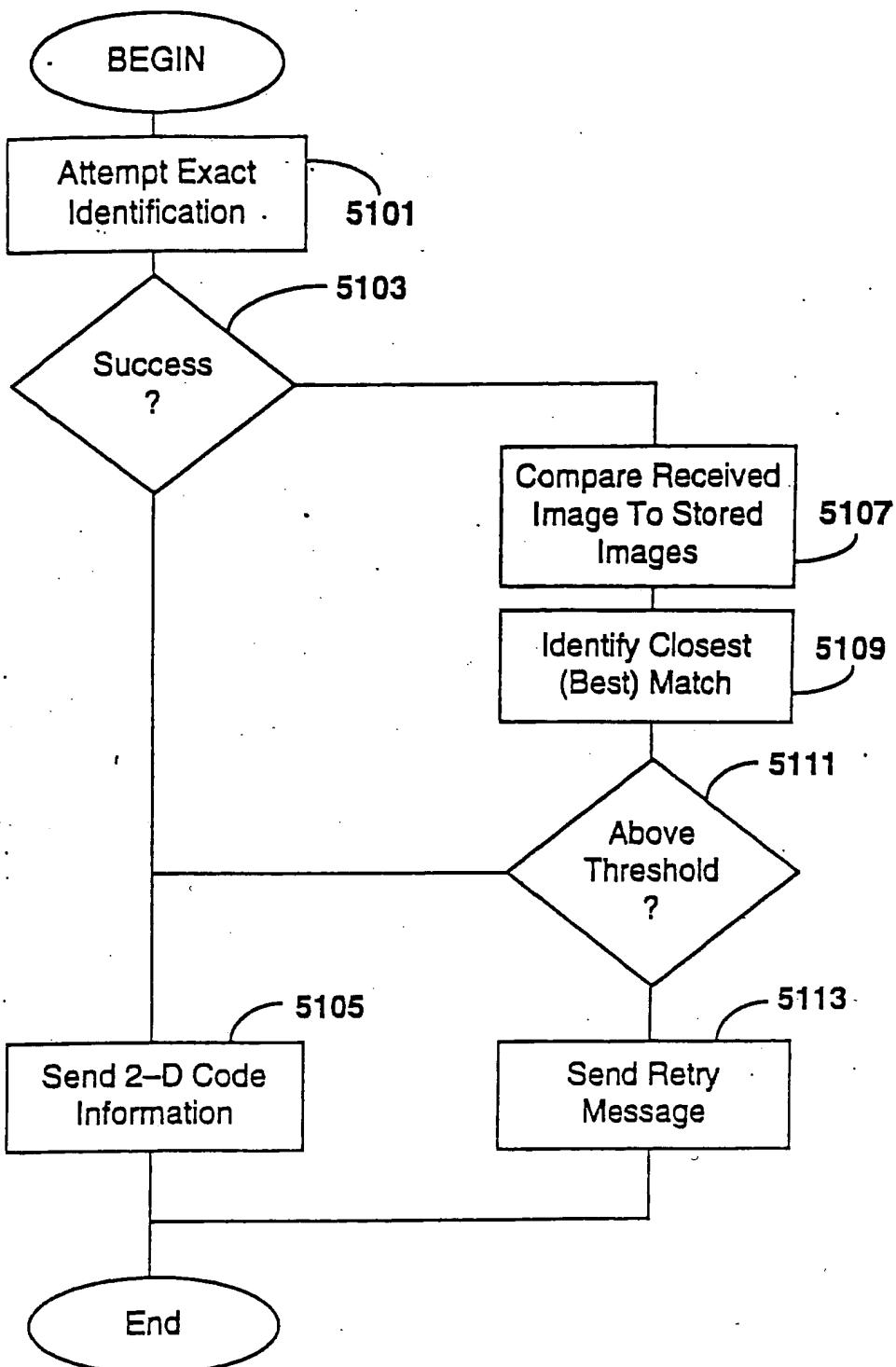


Fig. 51a

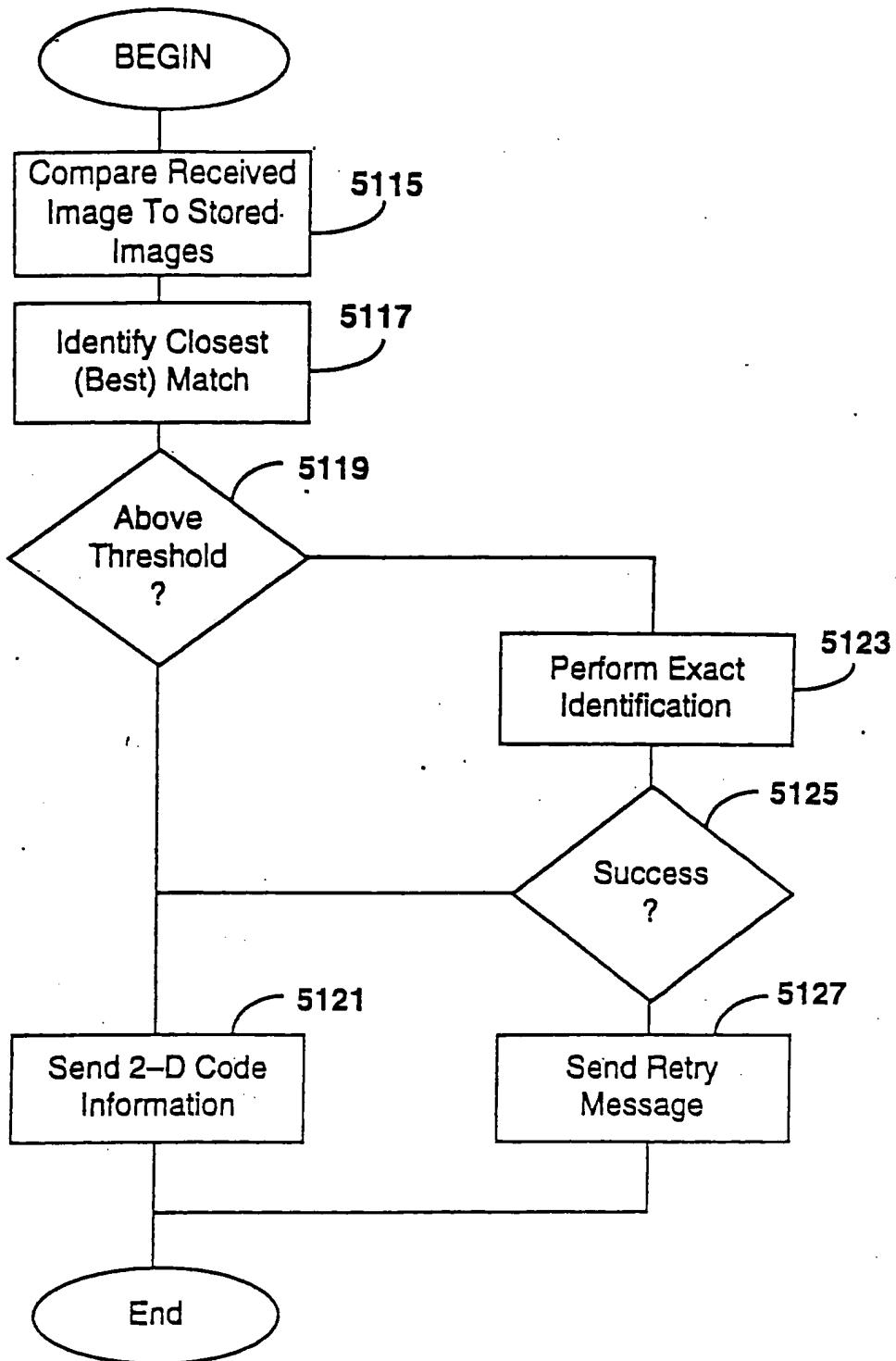


Fig. 51b

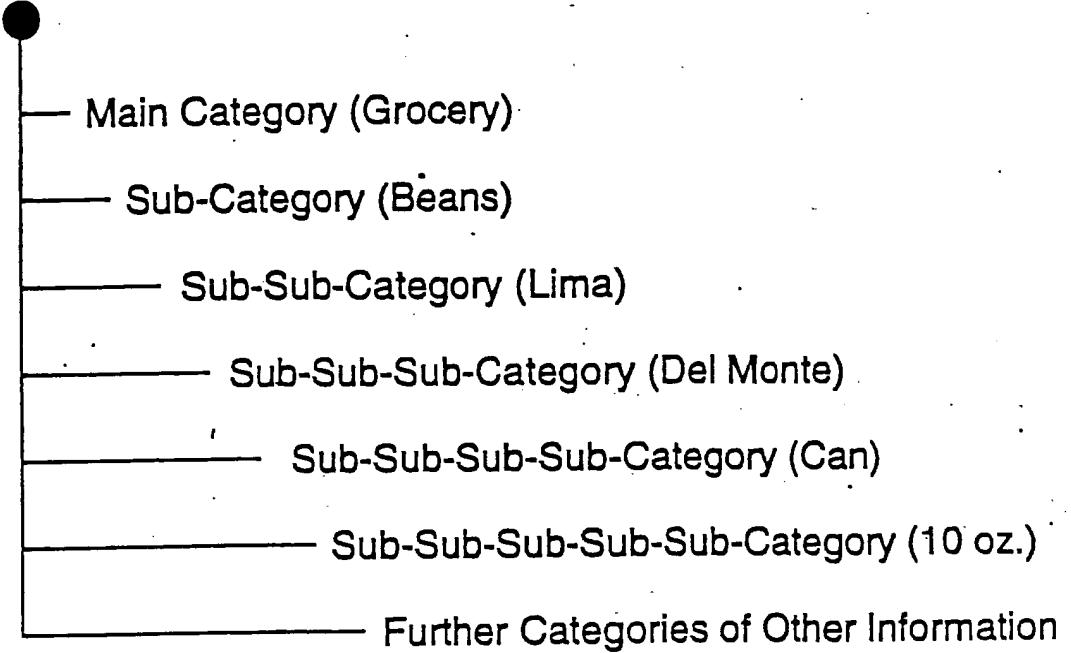


Fig. 52

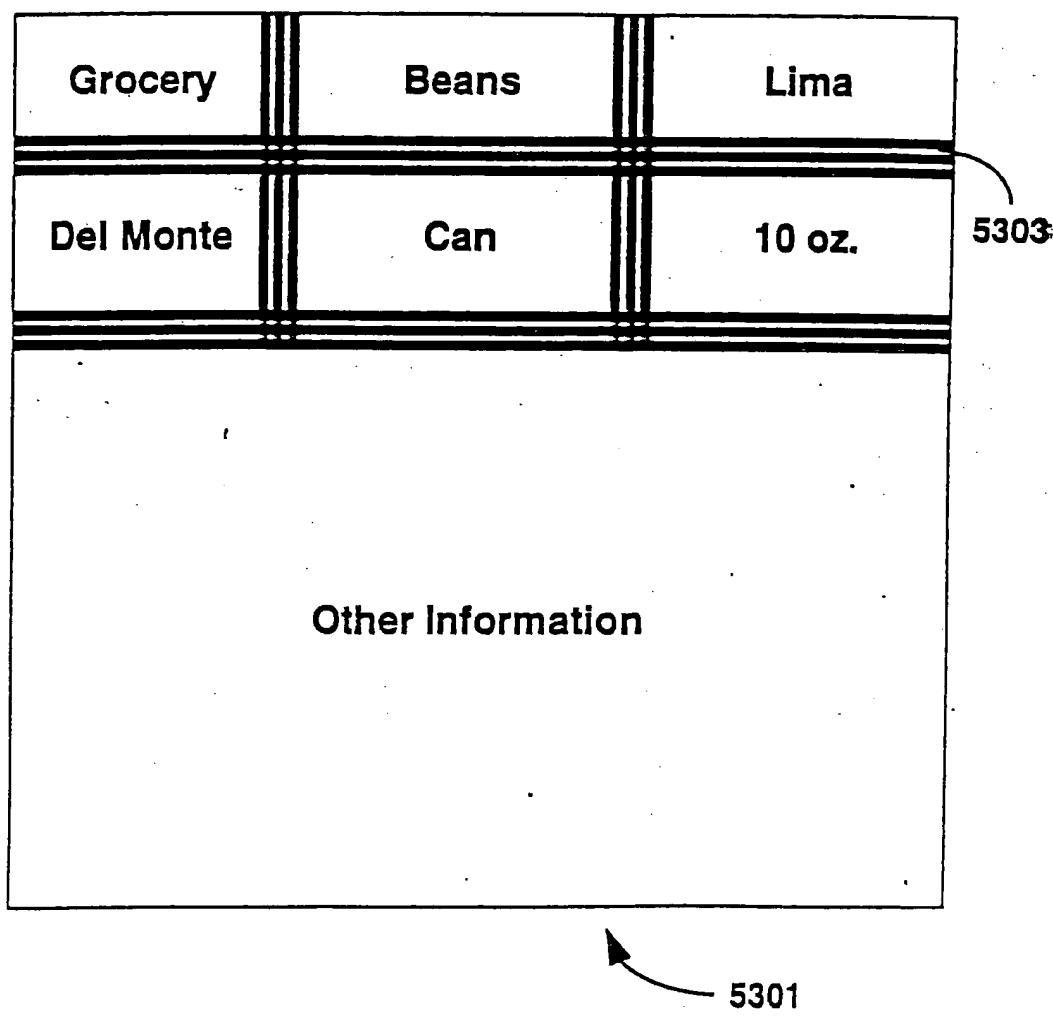


Fig. 53

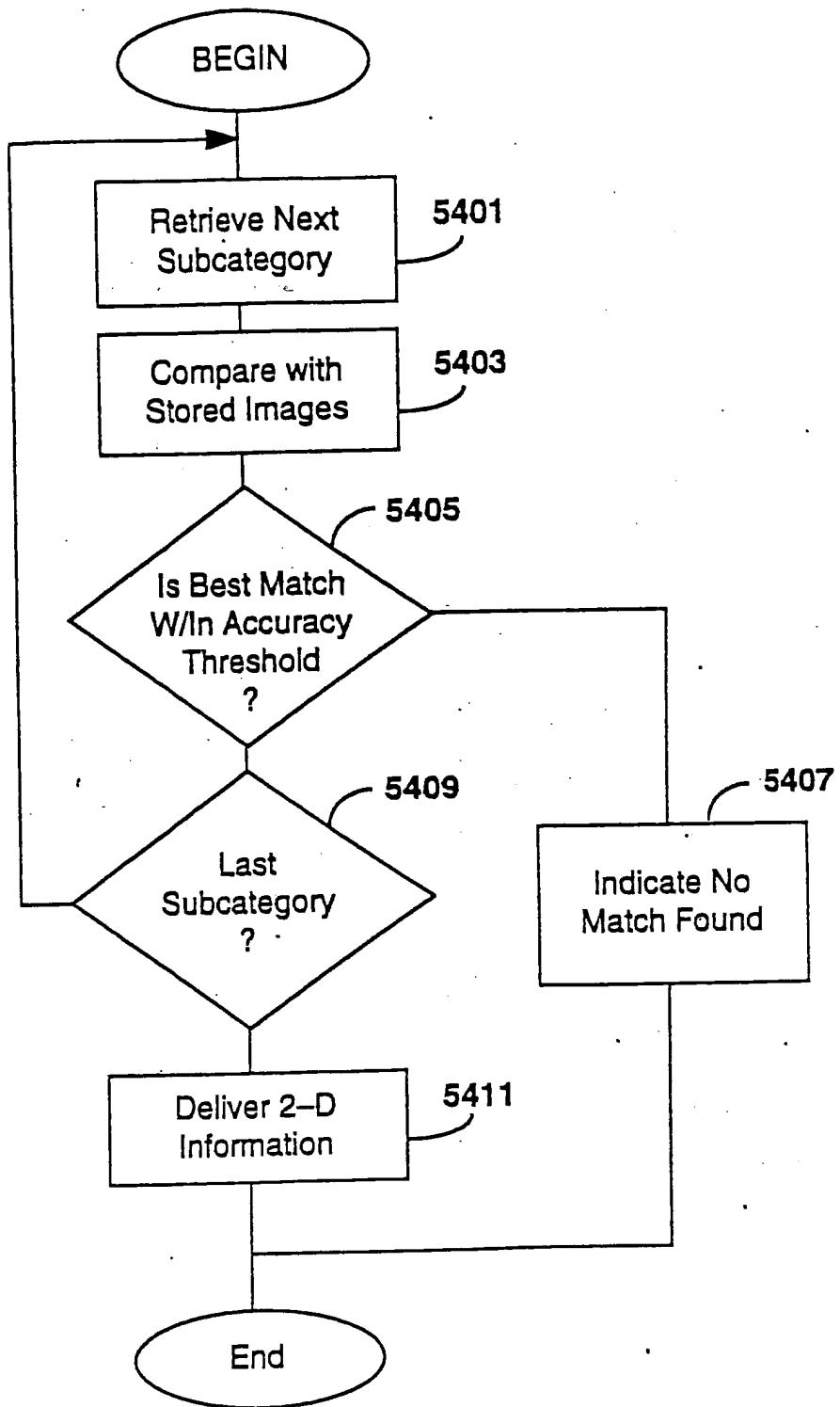
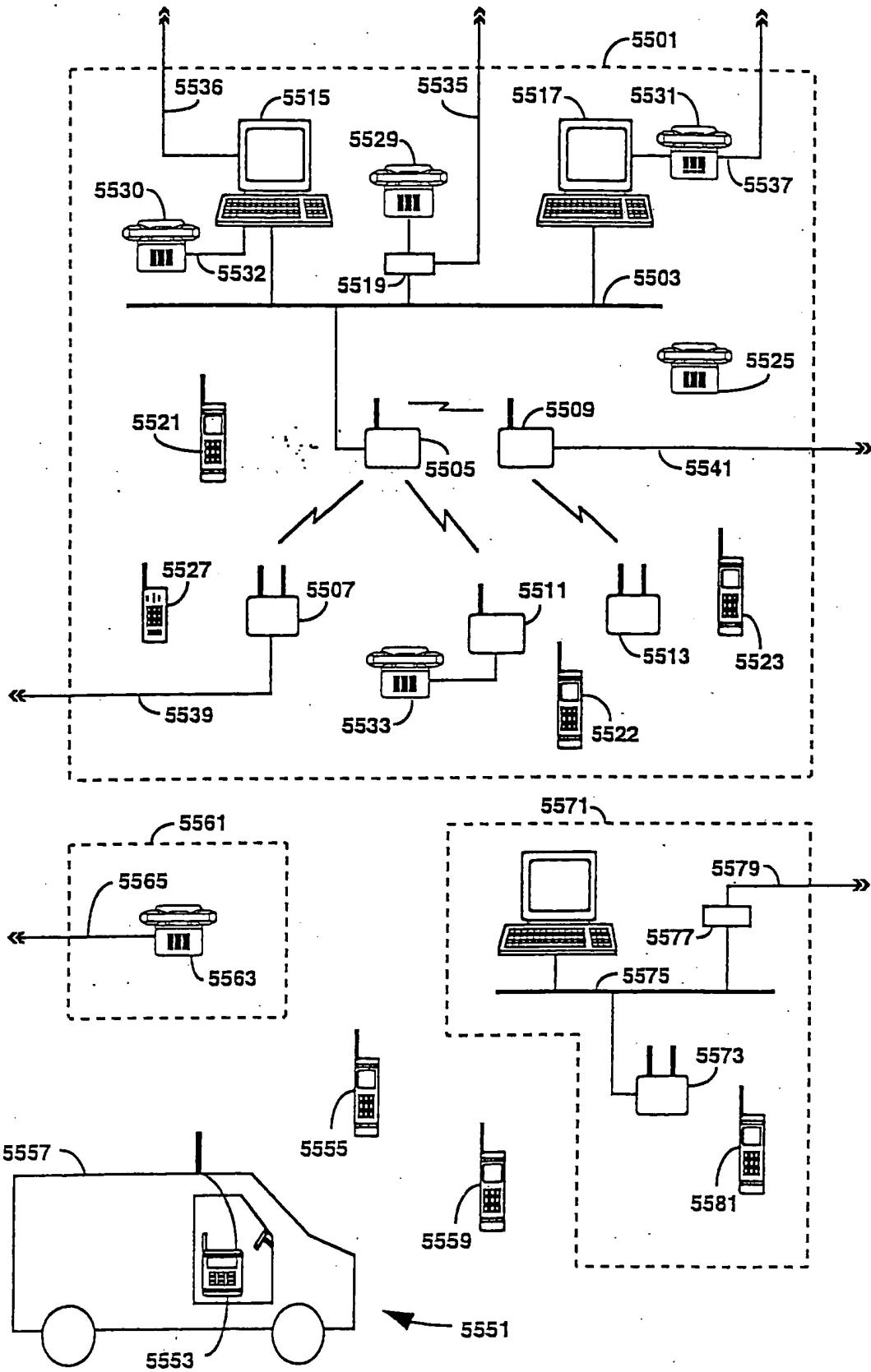


Fig. 54



**Fig. 55a**

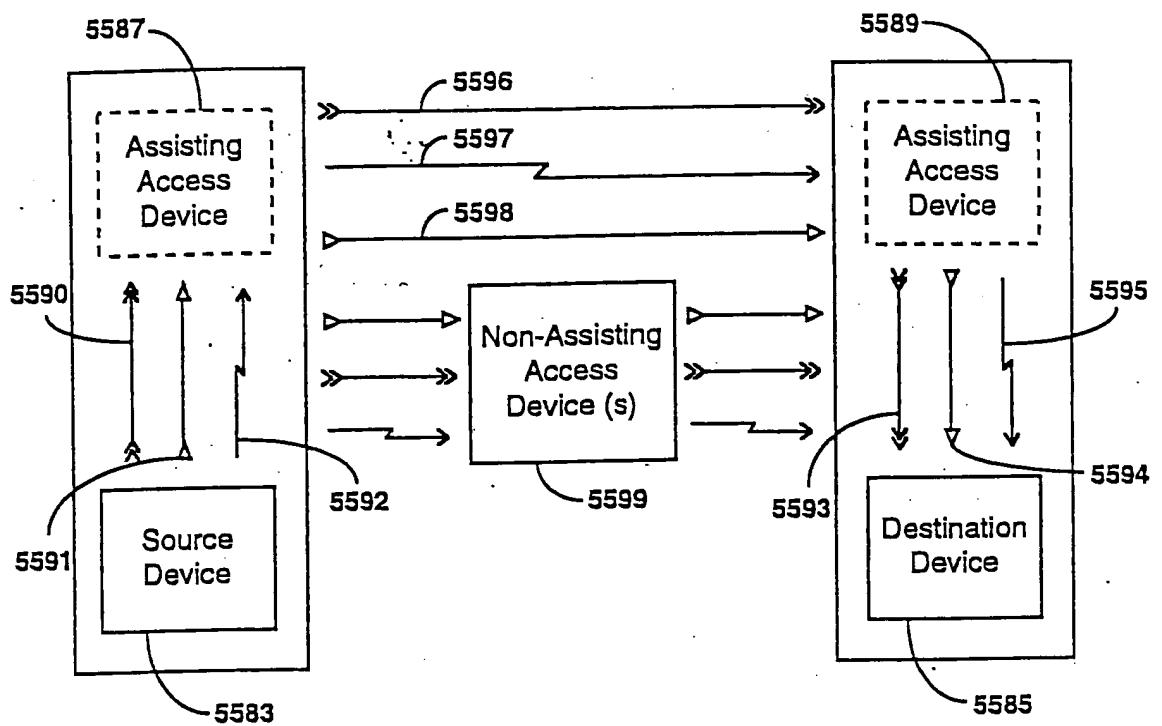


Fig. 55b

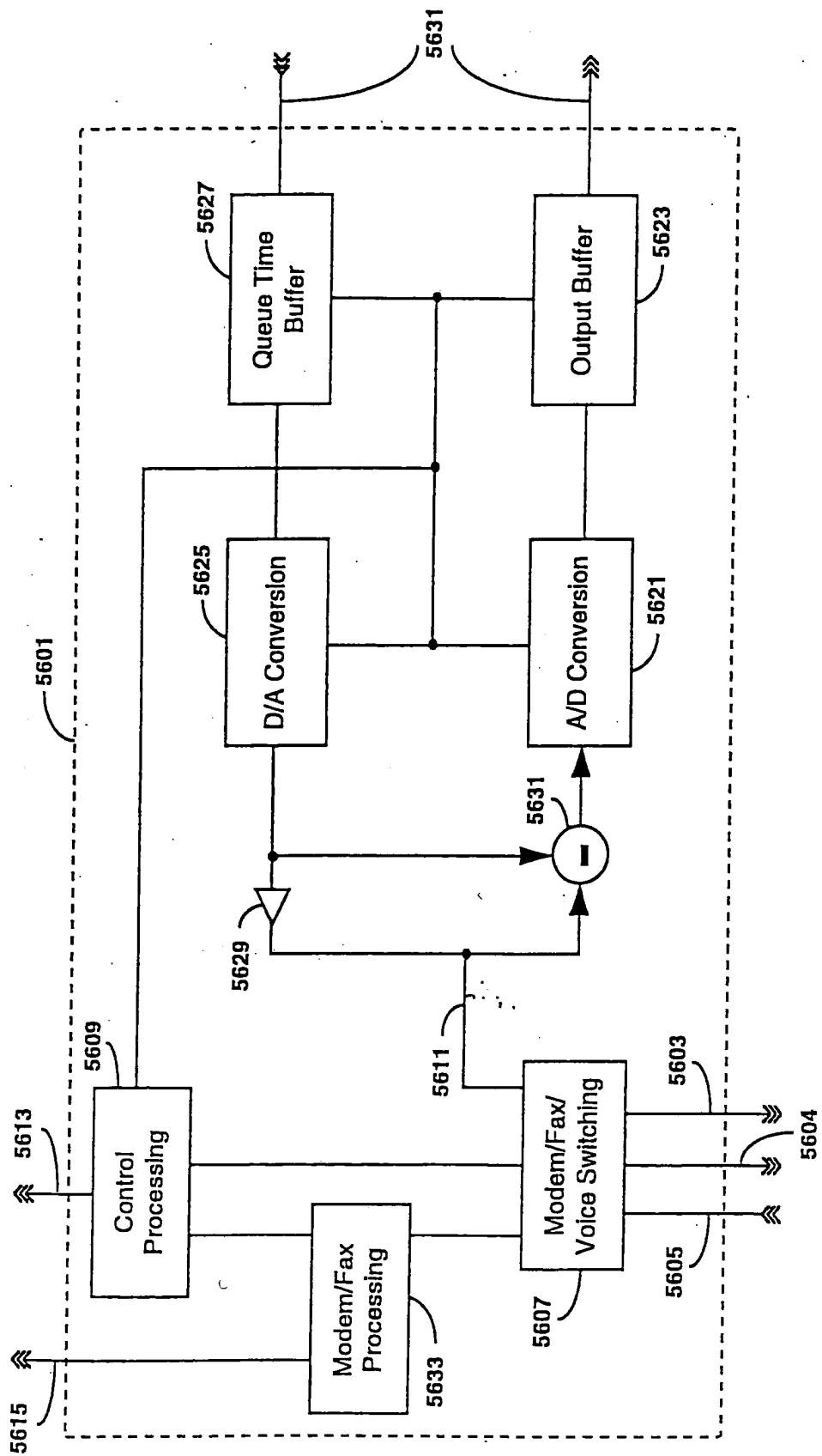
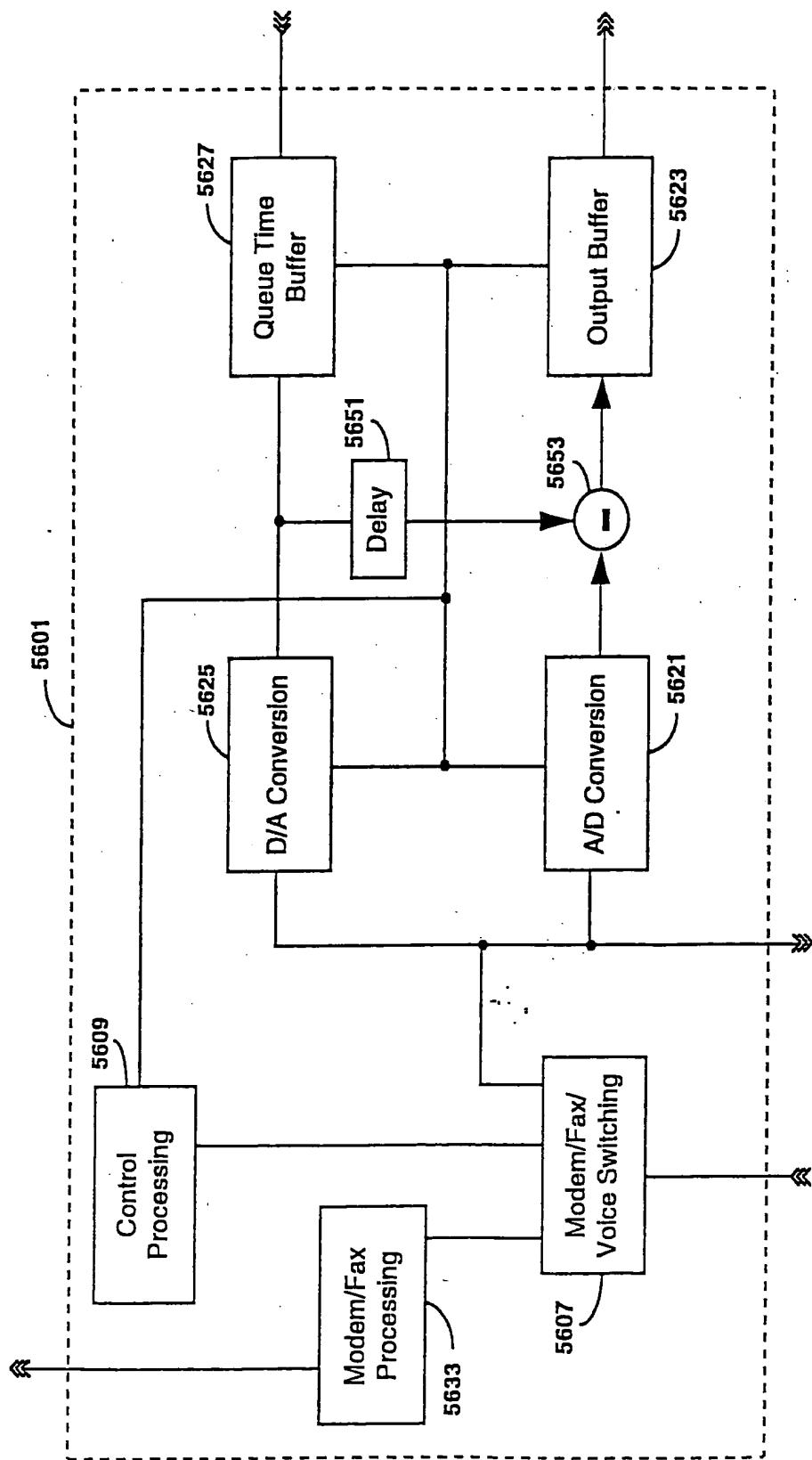


Fig. 56a



**Fig. 56b**

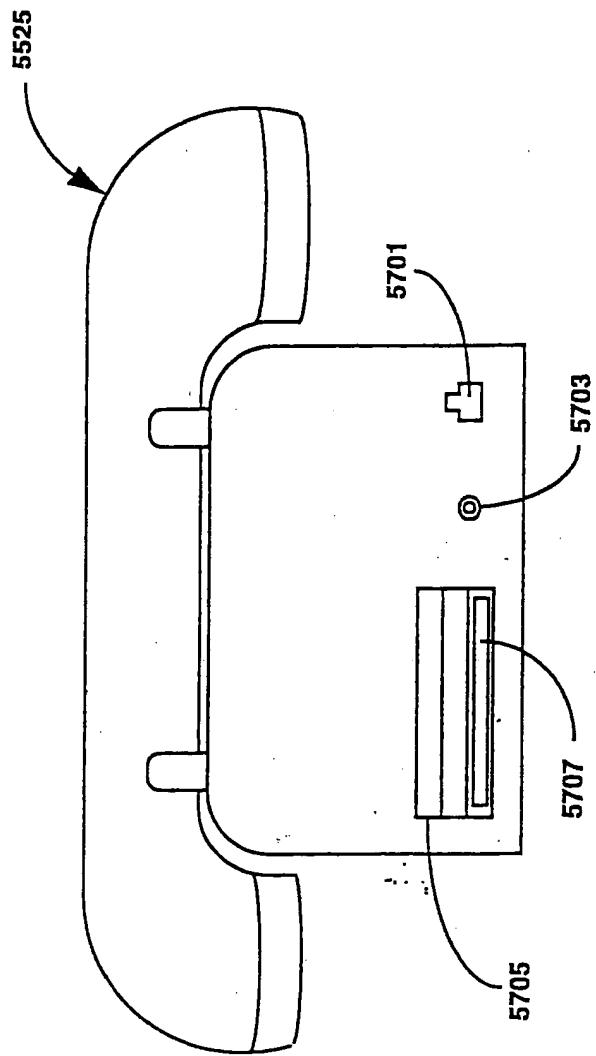
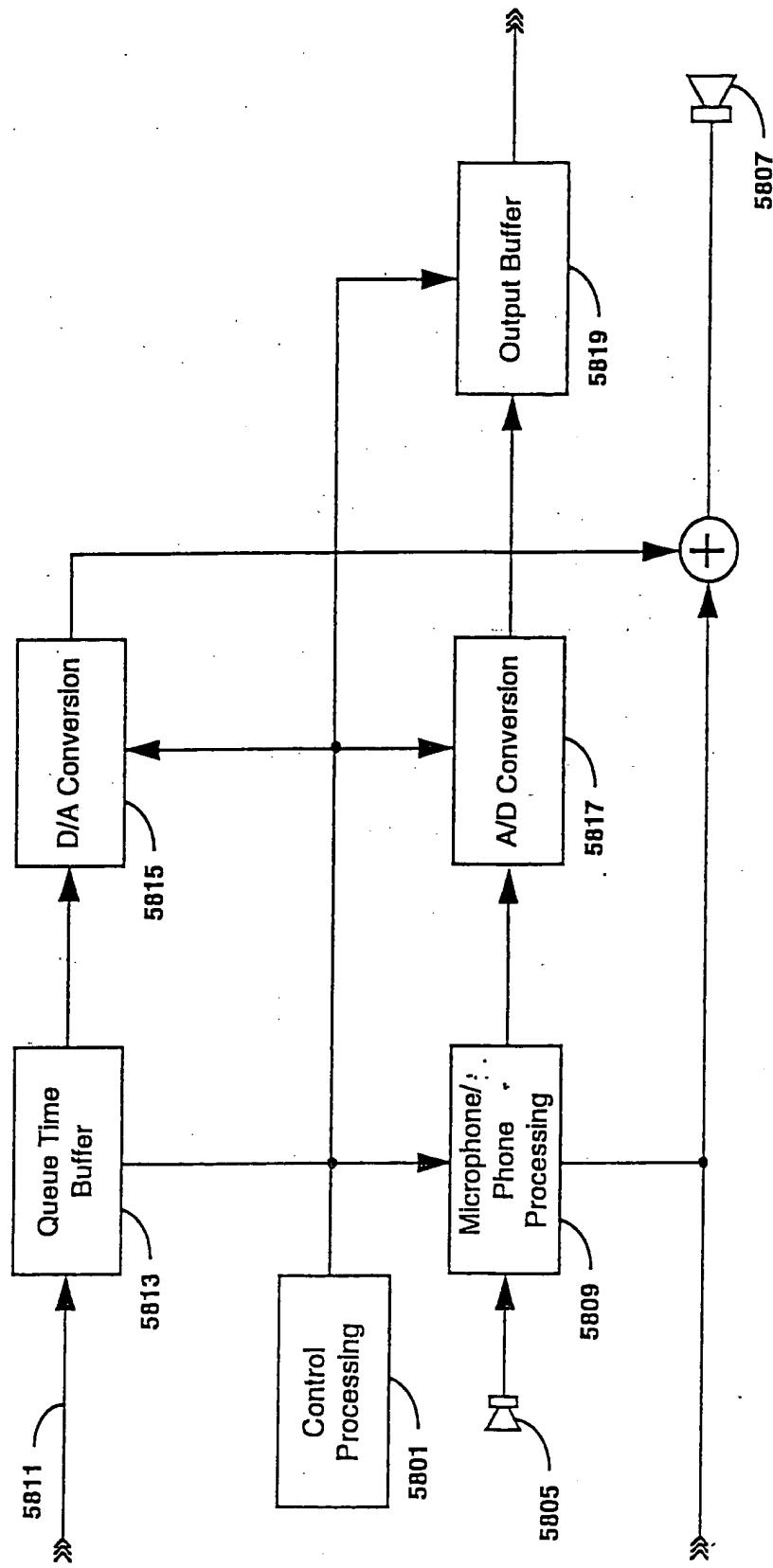


Fig. 57



**Fig. 58**

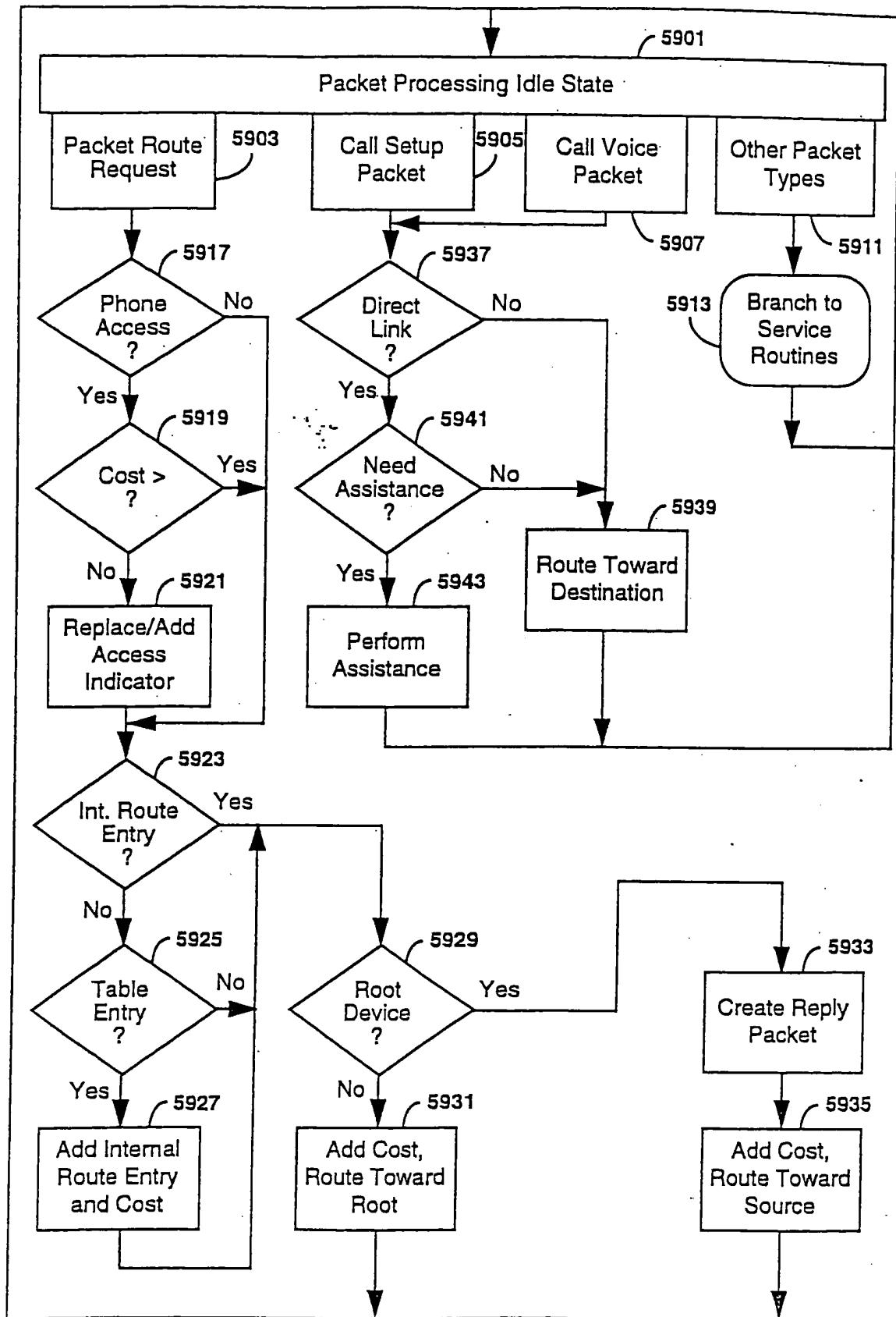


Fig. 59

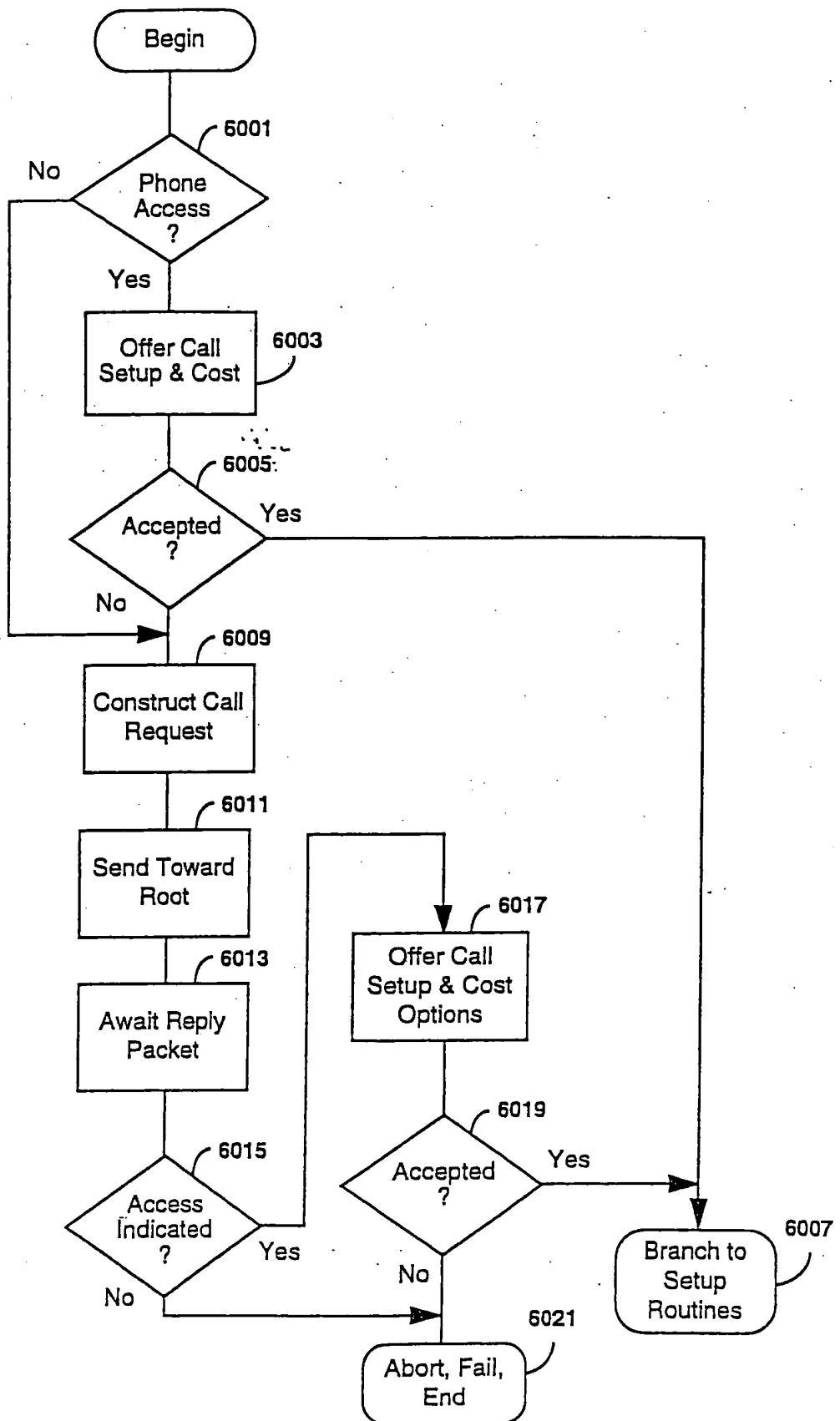


Fig. 60

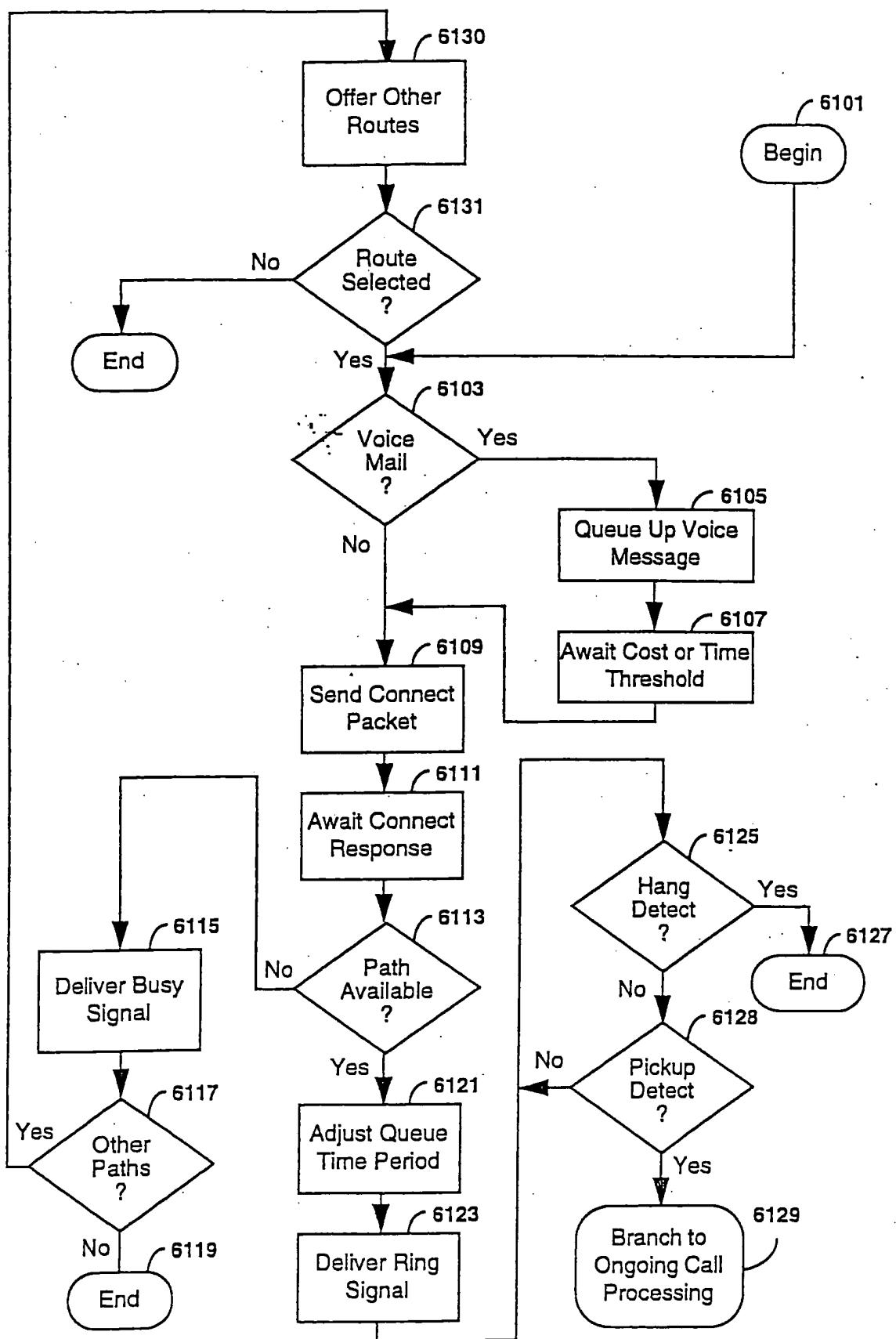
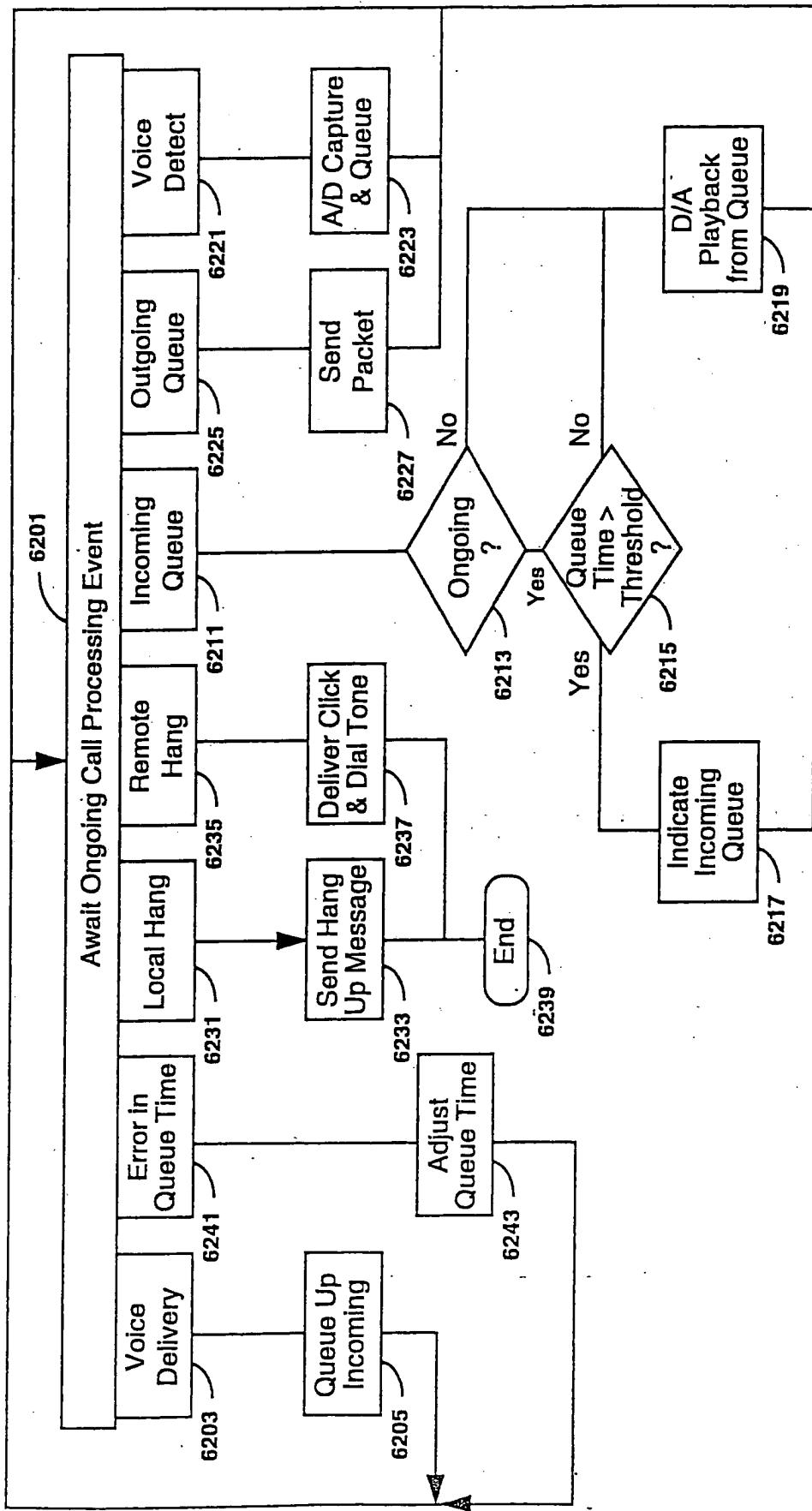


Fig. 61

**Fig. 62**



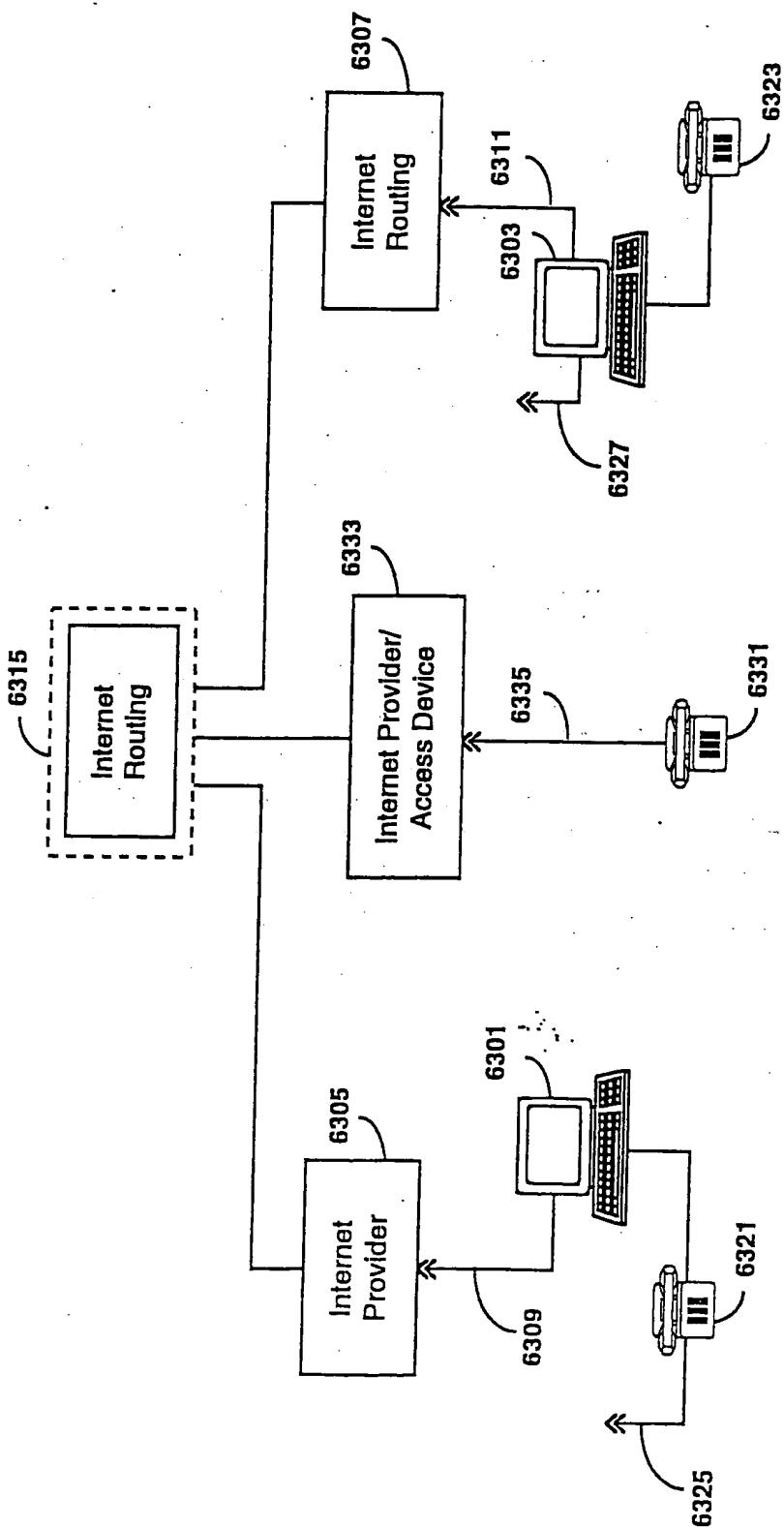


Fig. 63